

# Contents

## PROLOGUE I

### PROLOGUE TO ELECTRONICS 1

- Brief History 1
- Passive and Active Devices 2
- Electronic Circuits 2
- Discrete and Integrated Circuits 2
- Analog and Digital Signals 3
- Notation 3
- Summary 4

## PART 1

### SEMICONDUCTOR DEVICES AND BASIC APPLICATIONS 5

#### Chapter 1 Semiconductor Materials and Diodes 7

- Preview 7
- 1.1 Semiconductor Materials and Properties 8
- 1.2 The pn Junction 20
- 1.3 Diode Circuits: DC Analysis and Models 30
- 1.4 Diode Circuits: AC Equivalent Circuit 39
- 1.5 Other Diode Types 44
- 1.6 Design Application: Diode Thermometer 49
- 1.7 *Summary* 51
- Problems* 53

#### Chapter 2 Diode Circuits 61

- Preview 61
- 2.1 Rectifier Circuits 62
- 2.2 Zener Diode Circuits 80
- 2.3 Clipper and Clamper Circuits 86
- 2.4 Multiple-Diode Circuits 93
- 2.5 Photodiode and LED Circuits 102
- 2.6 Design Application: DC Power Supply 105
- 2.7 *Summary* 106
- Problems* 108

#### Chapter 3 The Field-Effect Transistor 119

- Preview 119
- 3.1 MOS Field-Effect Transistor 120
- 3.2 MOSFET DC Circuit Analysis 140

3.3	Basic MOSFET Applications: Switch, Digital Logic Gate, and Amplifier	165
3.4	Constant-Current Biasing	170
3.5	Multistage MOSFET Circuits	175
3.6	Junction Field-Effect Transistor	179
3.7	Design Application: Diode Thermometer with an MOS Transistor	192
3.8	<i>Summary</i>	194
	<i>Problems</i>	196

## Chapter 4

**Basic FET Amplifiers 207**

	Preview	207
4.1	The MOSFET Amplifier	208
4.2	Basic Transistor Amplifier Configurations	218
4.3	The Common-Source Amplifier	219
4.4	The Common-Drain (Source-Follower) Amplifier	231
4.5	The Common-Gate Configuration	239
4.6	The Three Basic Amplifier Configurations: Summary and Comparison	243
4.7	Single-Stage Integrated Circuit MOSFET Amplifiers	244
4.8	Multistage Amplifiers	259
4.9	Basic JFET Amplifiers	263
4.10	Design Application: A Two-Stage Amplifier	269
4.11	<i>Summary</i>	272
	<i>Problems</i>	273

## Chapter 5

**The Bipolar Junction Transistor 287**

	Preview	287
5.1	Basic Bipolar Junction Transistor	288
5.2	DC Analysis of Transistor Circuits	303
5.3	Basic Transistor Applications	327
5.4	Bipolar Transistor Biasing	334
5.5	Multistage Circuits	348
5.6	Design Application: Diode Thermometer with a Bipolar Transistor	353
5.7	<i>Summary</i>	355
	<i>Problems</i>	356

## Chapter 6

**Basic BJT Amplifiers 369**

	Preview	369
6.1	Analog Signals and Linear Amplifiers	370
6.2	The Bipolar Linear Amplifier	371
6.3	Basic Transistor Amplifier Configurations	397
6.4	Common-Emitter Amplifiers	399
6.5	AC Load Line Analysis	415

6.6	Common-Collector (Emitter-Follower) Amplifier	424
6.7	Common-Base Amplifier	435
6.8	The Three Basic Amplifiers: Summary and Comparison	439
6.9	Multistage Amplifiers	440
6.10	Power Considerations	447
6.11	Design Application: Audio Amplifier	449
6.12	<i>Summary</i>	454
	<i>Problems</i>	455

## Chapter 7 Frequency Response 471

	Preview	471
7.1	Amplifier Frequency Response	472
7.2	System Transfer Functions	474
7.3	Frequency Response: Transistor Amplifiers with Circuit Capacitors	488
7.4	Frequency Response: Bipolar Transistor	506
7.5	Frequency Response: The FET	518
7.6	High-Frequency Response of Transistor Circuits	525
7.7	Design Application: A Two-Stage Amplifier with Coupling Capacitors	541
7.8	<i>Summary</i>	543
	<i>Problems</i>	544

## Chapter 8 Output Stages and Power Amplifiers 561

	Preview	561
8.1	Power Amplifiers	562
8.2	Power Transistors	562
8.3	Classes of Amplifiers	574
8.4	Class-A Power Amplifiers	588
8.5	Class-AB Push-Pull Complementary Output Stages	593
8.6	Design Application: An Output Stage Using MOSFETs	602
8.7	<i>Summary</i>	605
	<i>Problems</i>	606

## PROLOGUE II

### PROLOGUE TO ELECTRONIC DESIGN 615

Preview	615
Design Approach	615
System Design	616
Electronic Design	617
Conclusion	618

**PART 2****ANALOG ELECTRONICS 619****Chapter 9      Ideal Operational Amplifiers and Op-Amp Circuits 621**

- Preview 621  
9.1 The Operational Amplifier 622  
9.2 Inverting Amplifier 627  
9.3 Summing Amplifier 636  
9.4 Noninverting Amplifier 638  
9.5 Op-Amp Applications 641  
9.6 Operational Transconductance Amplifiers 656  
9.7 Op-Amp Circuit Design 658  
9.8 Design Application: Electronic Thermometer with an Instrumentation Amplifier 665  
9.9 *Summary* 667  
*Problems* 668

**Chapter 10      Integrated Circuit Biasing and Active Loads 683**

- Preview 683  
10.1 Bipolar Transistor Current Sources 684  
10.2 FET Current Sources 704  
10.3 Circuits with Active Loads 716  
10.4 Small-Signal Analysis: Active Load Circuits 723  
10.5 Design Application: An NMOS Current Source 730  
10.6 *Summary* 732  
*Problems* 734

**Chapter 11      Differential and Multistage Amplifiers 749**

- Preview 749  
11.1 The Differential Amplifier 750  
11.2 Basic BJT Differential Pair 750  
11.3 Basic FET Differential Pair 777  
11.4 Differential Amplifier with Active Load 788  
11.5 BiCMOS Circuits 799  
11.6 Gain Stage and Simple Output Stage 803  
11.7 Simplified BJT Operational Amplifier Circuit 809  
11.8 Diff-Amp Frequency Response 813  
11.9 Design Application: A CMOS Diff-Amp 819  
11.10 *Summary* 821  
*Problems* 823

**Chapter 12      Feedback and Stability 847**

- Preview 847  
12.1 Introduction to Feedback 848  
12.2 Basic Feedback Concepts 849  
12.3 Ideal Feedback Topologies 859

12.4	Voltage (Series–Shunt) Amplifiers	869
12.5	Current (Shunt–Series) Amplifiers	875
12.6	Transconductance (Series–Series) Amplifiers	882
12.7	Transresistance (Shunt–Shunt) Amplifiers	889
12.8	Loop Gain	899
12.9	Stability of the Feedback Circuit	905
12.10	Frequency Compensation	915
12.11	Design Application: A MOSFET Feedback Circuit	921
12.12	<i>Summary</i>	923
	<i>Problems</i>	925

**Chapter 13****Operational Amplifier Circuits 943**

	Preview	943
13.1	General Op-Amp Circuit Design	944
13.2	A Bipolar Operational Amplifier Circuit	946
13.3	CMOS Operational Amplifier Circuits	966
13.4	BiCMOS Operational Amplifier Circuits	977
13.5	JFET Operational Amplifier Circuits	985
13.6	Design Application: A Two-Stage CMOS Op-Amp to Match a Given Output Stage	988
13.7	<i>Summary</i>	991
	<i>Problems</i>	993

**Chapter 14****Nonideal Effects in Operational Amplifier Circuits 1003**

	Preview	1003
14.1	Practical Op-Amp Parameters	1004
14.2	Finite Open-Loop Gain	1007
14.3	Frequency Response	1017
14.4	Offset Voltage	1024
14.5	Input Bias Current	1036
14.6	Additional Nonideal Effects	1039
14.7	Design Application: An Offset Voltage Compensation Network	1041
14.8	<i>Summary</i>	1043
	<i>Problems</i>	1045

**Chapter 15****Applications and Design of Integrated Circuits 1055**

	Preview	1055
15.1	Active Filters	1056
15.2	Oscillators	1069
15.3	Schmitt Trigger Circuits	1078
15.4	Nonsinusoidal Oscillators and Timing Circuits	1090
15.5	Integrated Circuit Power Amplifiers	1102
15.6	Voltage Regulators	1108
15.7	Design Application: An Active Bandpass Filter	1117
15.8	<i>Summary</i>	1119
	<i>Problems</i>	1121

## PROLOGUE III

### PROLOGUE TO DIGITAL ELECTRONICS 1133

- Preview 1133
- Logic Functions and Logic Gates 1133
- Logic Levels 1135
- Noise Margin 1135
- Propagation Delay Times and Switching Times 1136
- Summary 1136

## PART 3

### DIGITAL ELECTRONICS 1137

#### Chapter 16 MOSFET Digital Circuits 1139

- Preview 1139
- 16.1 NMOS Inverters 1140
- 16.2 NMOS Logic Circuits 1157
- 16.3 CMOS Inverter 1162
- 16.4 CMOS Logic Circuits 1176
- 16.5 Clocked CMOS Logic Circuits 1184
- 16.6 Transmission Gates 1187
- 16.7 Sequential Logic Circuits 1195
- 16.8 Memories: Classifications and Architectures 1201
- 16.9 RAM Memory Cells 1205
- 16.10 Read-Only Memory 1214
- 16.11 Data Converters 1219
- 16.12 Design Application: A Static CMOS Logic Gate 1225
- 16.13 *Summary* 1227  
*Problems* 1231

#### Chapter 17 Bipolar Digital Circuits 1249

- Preview 1249
- 17.1 Emitter-Coupled Logic (ECL) 1250
- 17.2 Modified ECL Circuit Configurations 1261
- 17.3 Transistor–Transistor Logic 1271
- 17.4 Schottky Transistor–Transistor Logic 1283
- 17.5 BiCMOS Digital Circuits 1290
- 17.6 Design Application: A Static ECL Gate 1292
- 17.7 *Summary* 1294  
*Problems* 1295

#### Appendix A Introduction to PSpice 1309

#### Appendix B Answers to Selected Problems 1317

### Index 1339