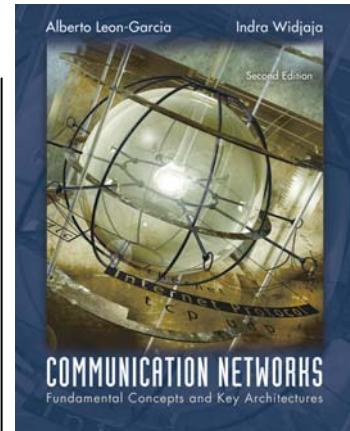
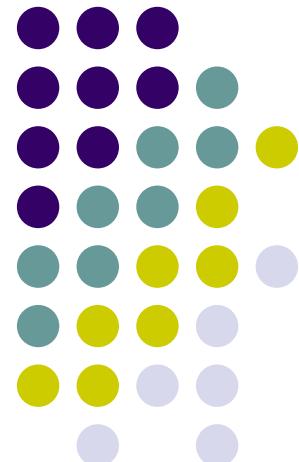


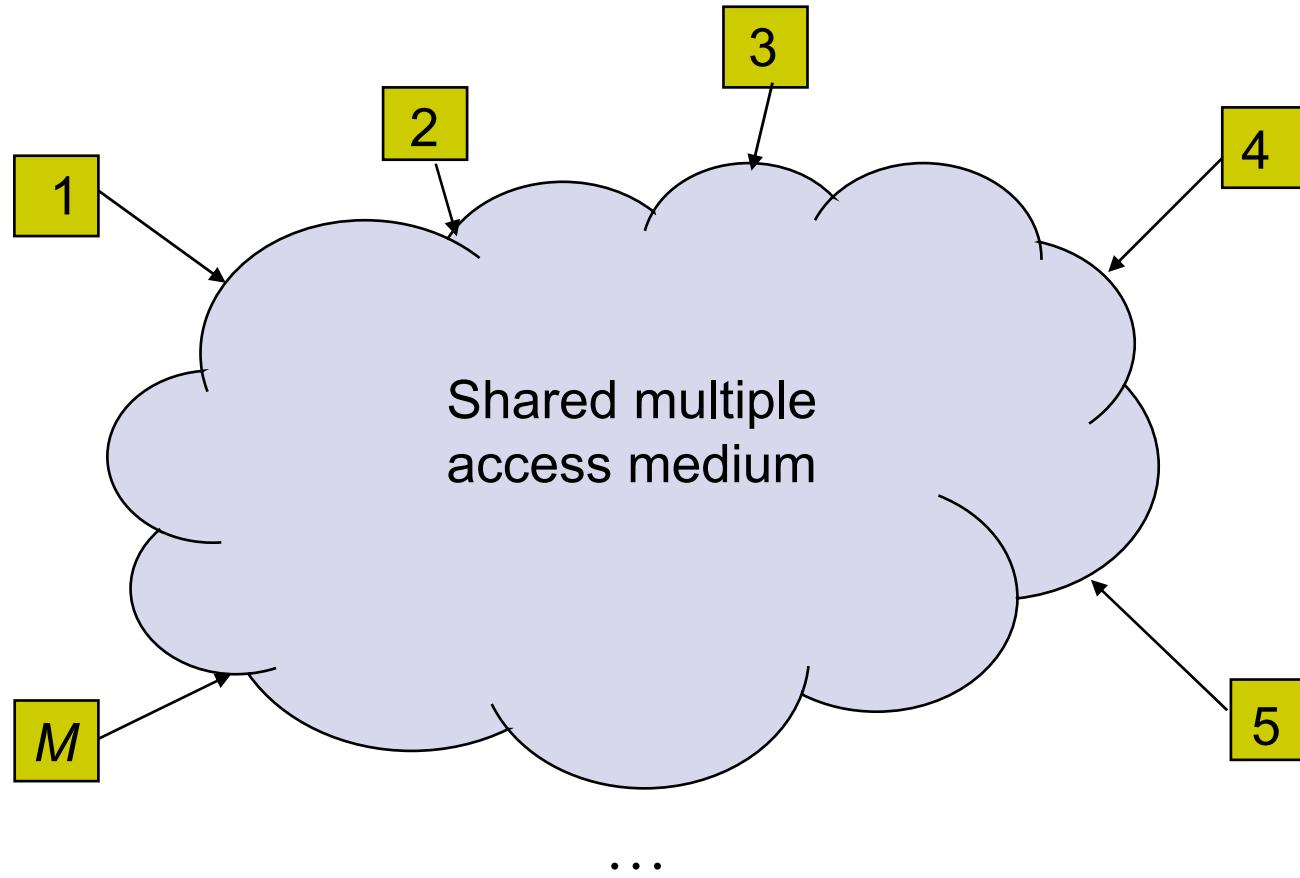
Chapter 6

Medium Access Control Protocols and Local Area Networks

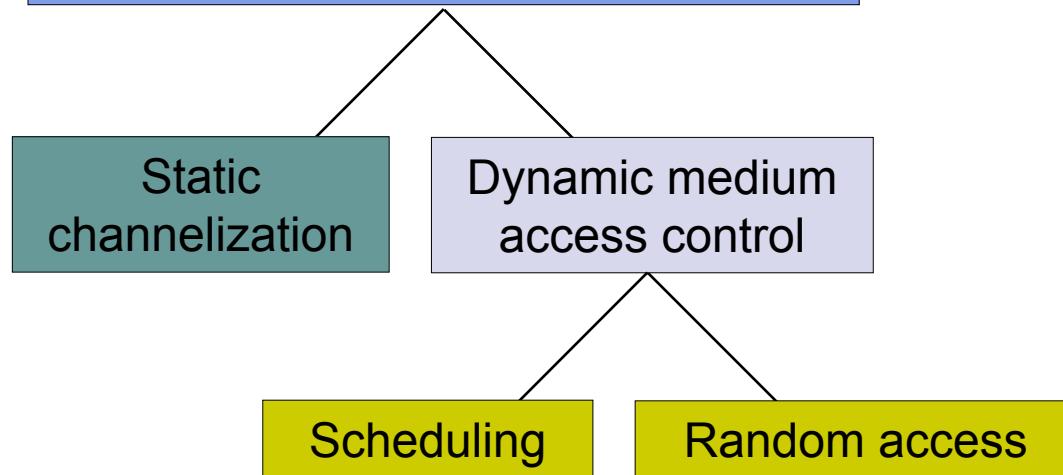


Chapter Figures

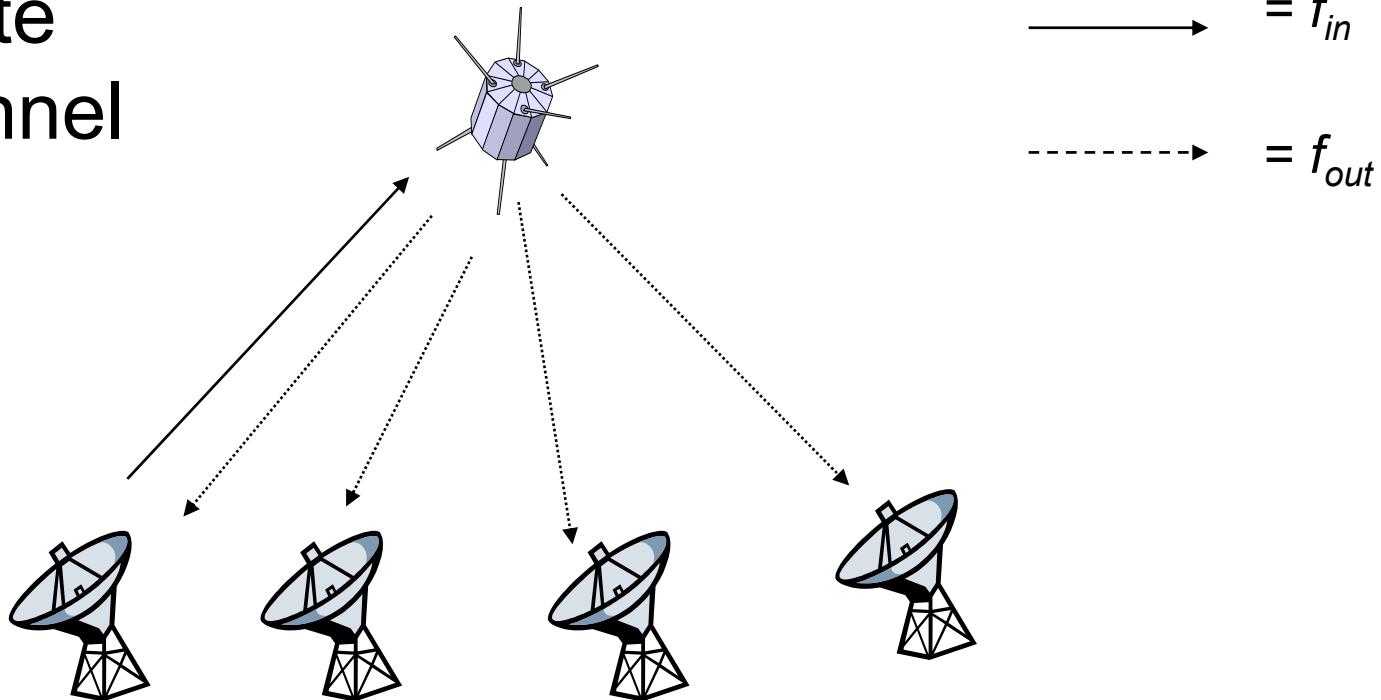


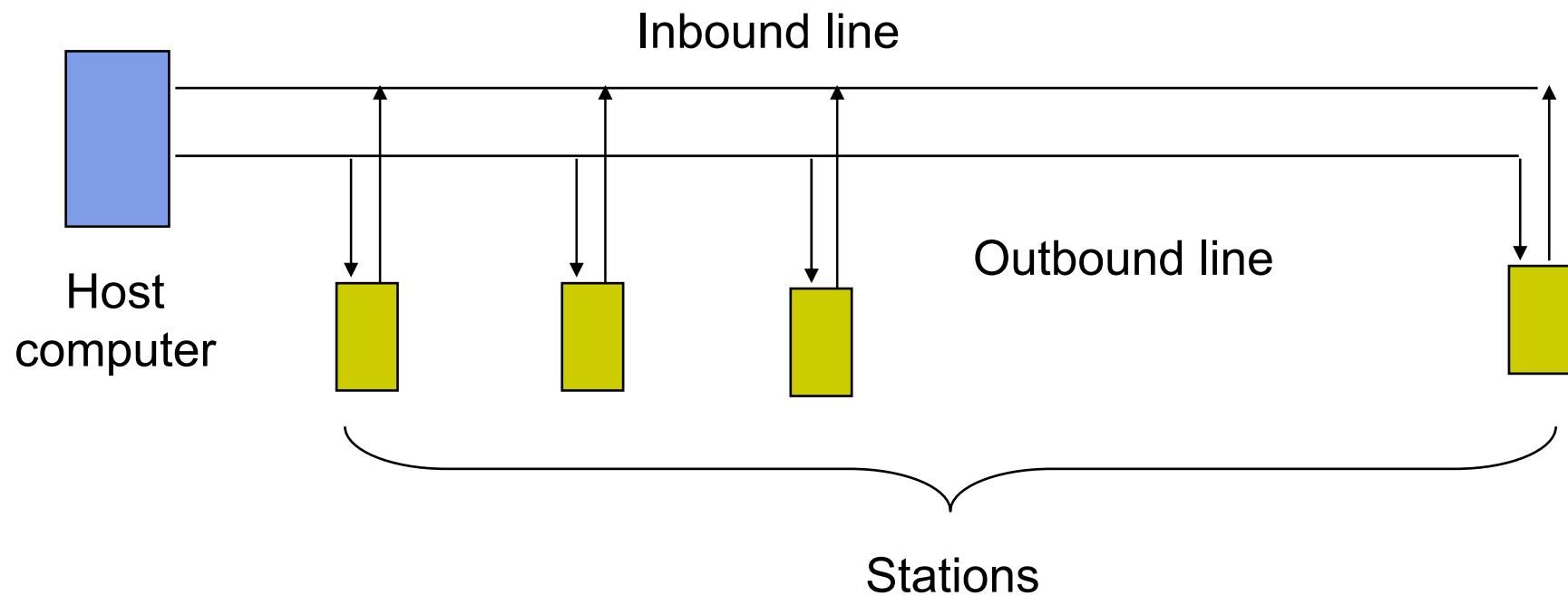


Medium sharing techniques

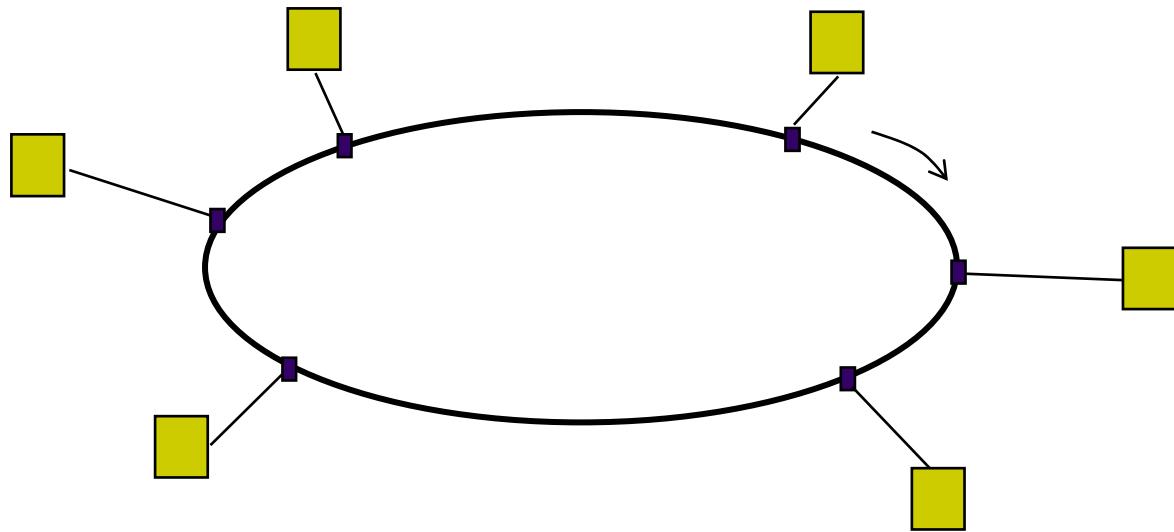


Satellite channel

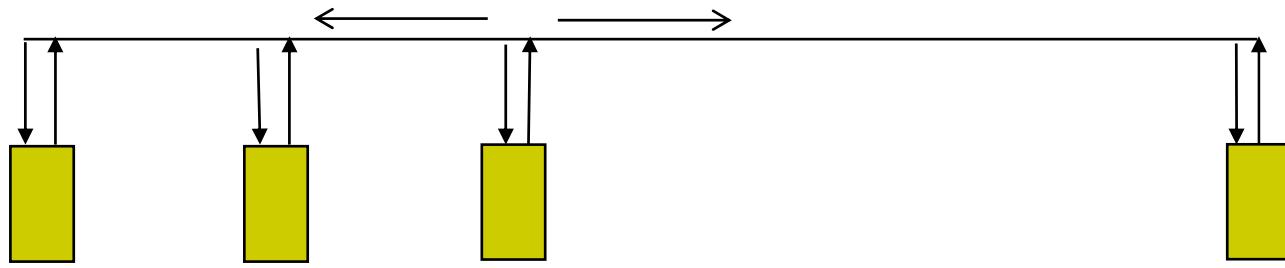


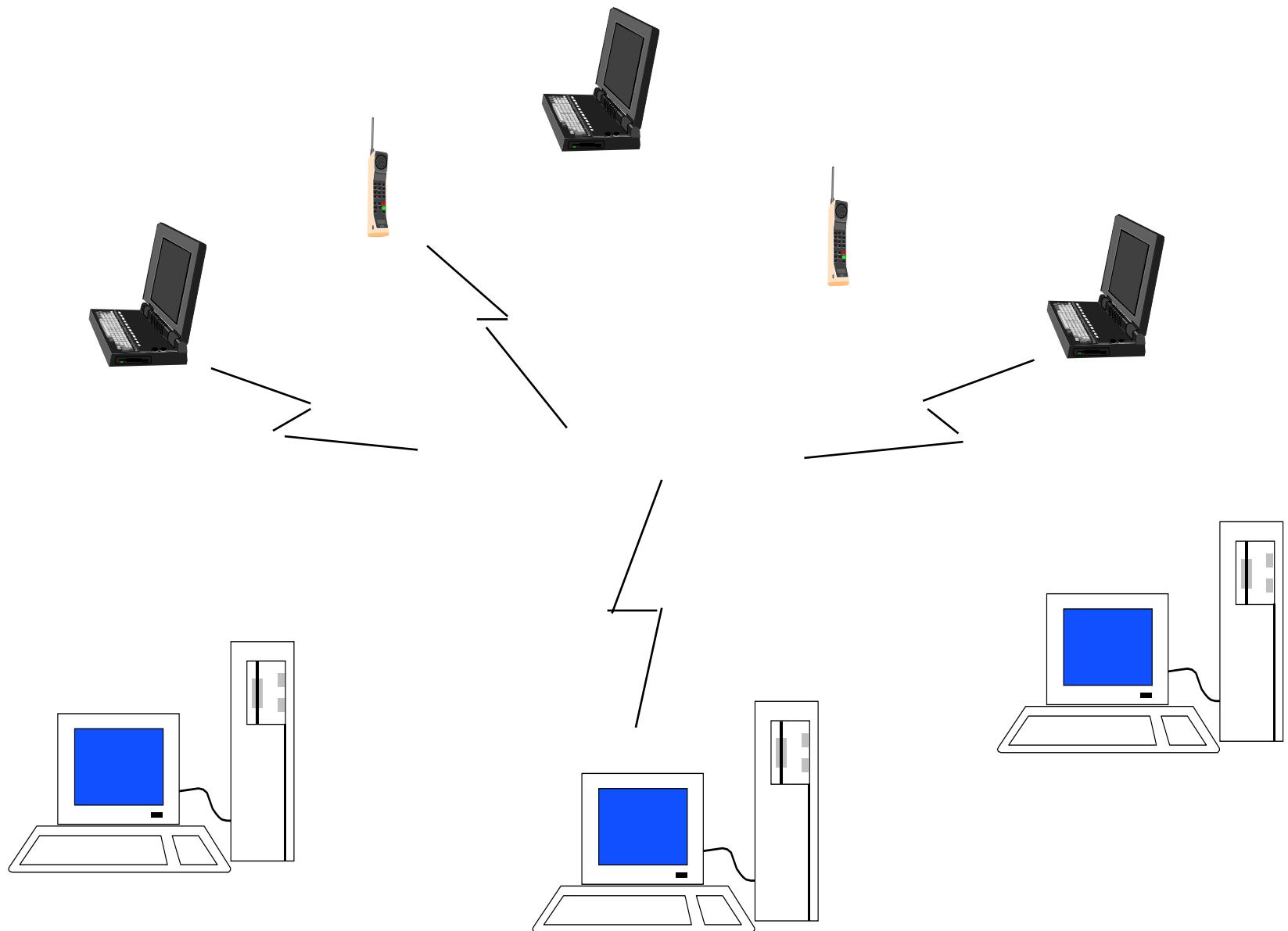


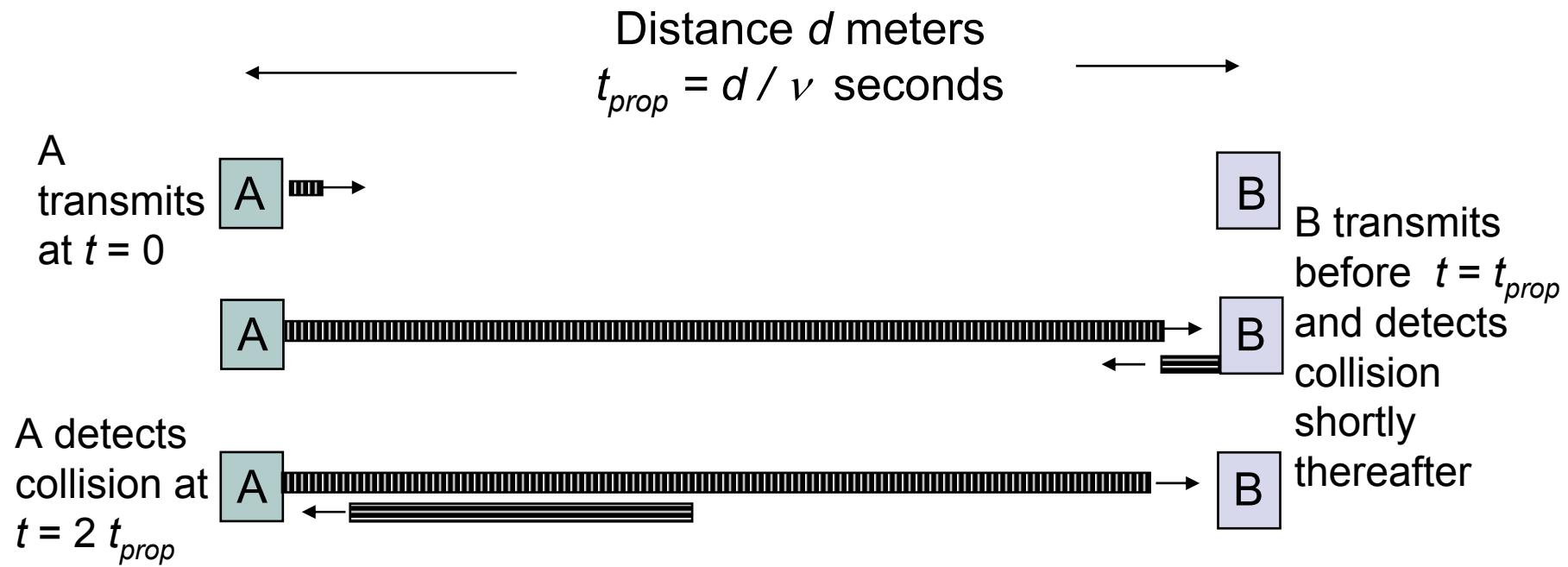
(a)

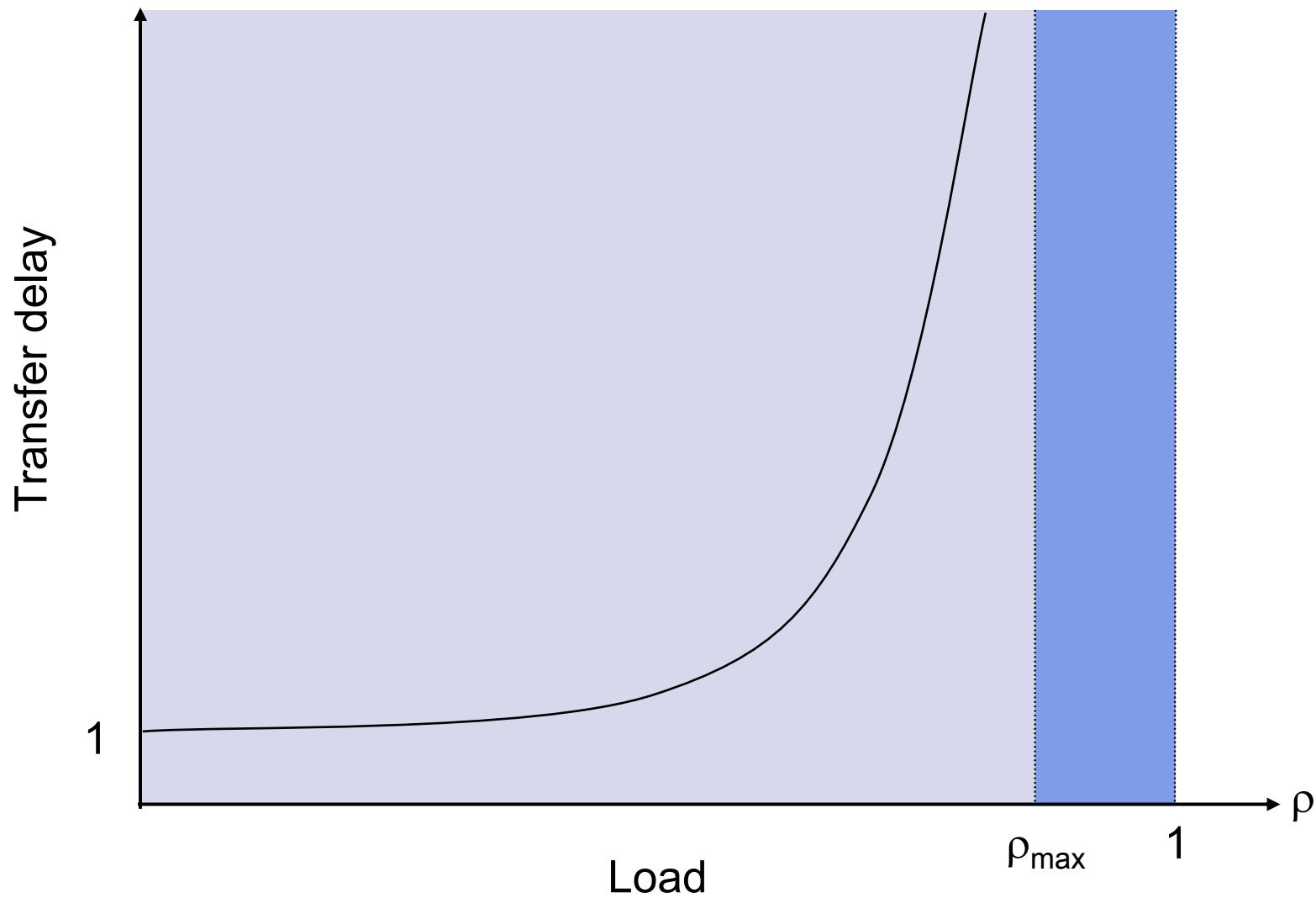


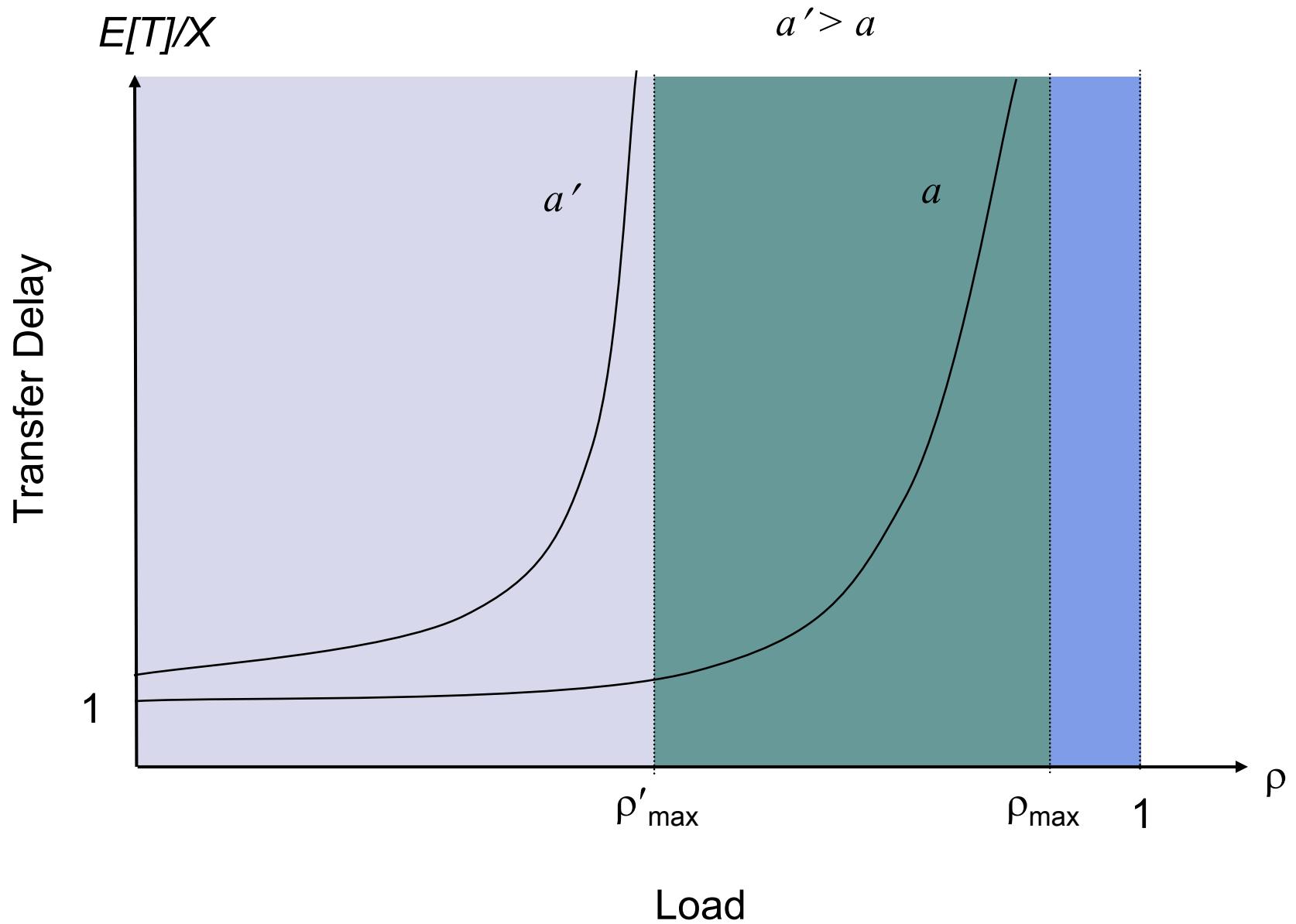
(b)

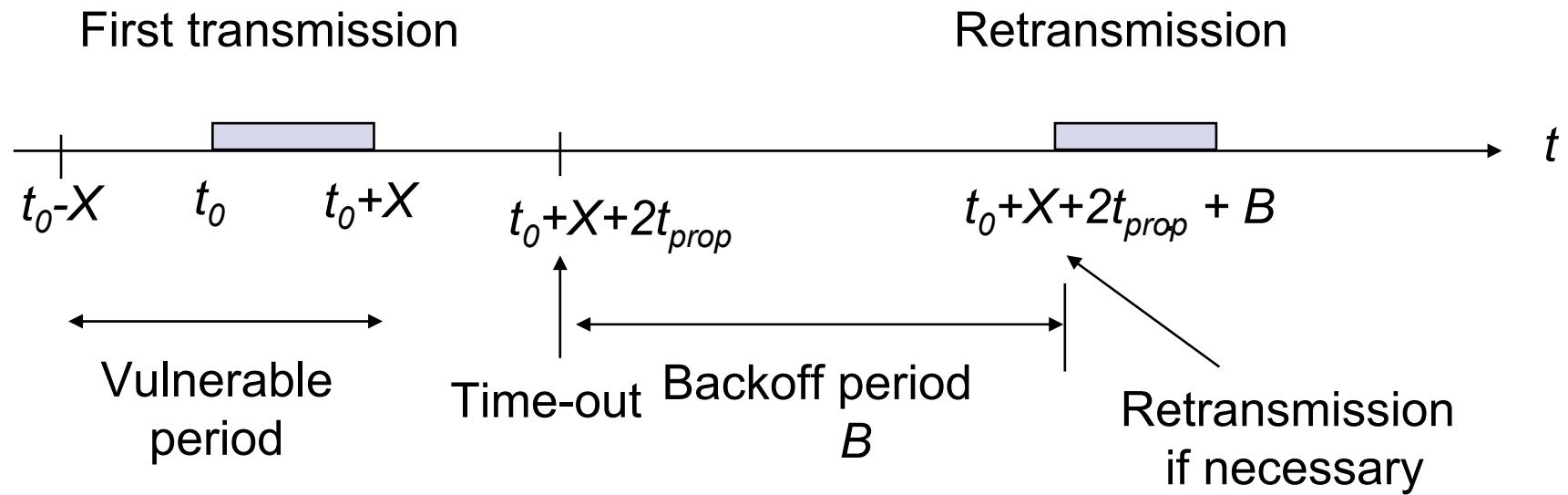


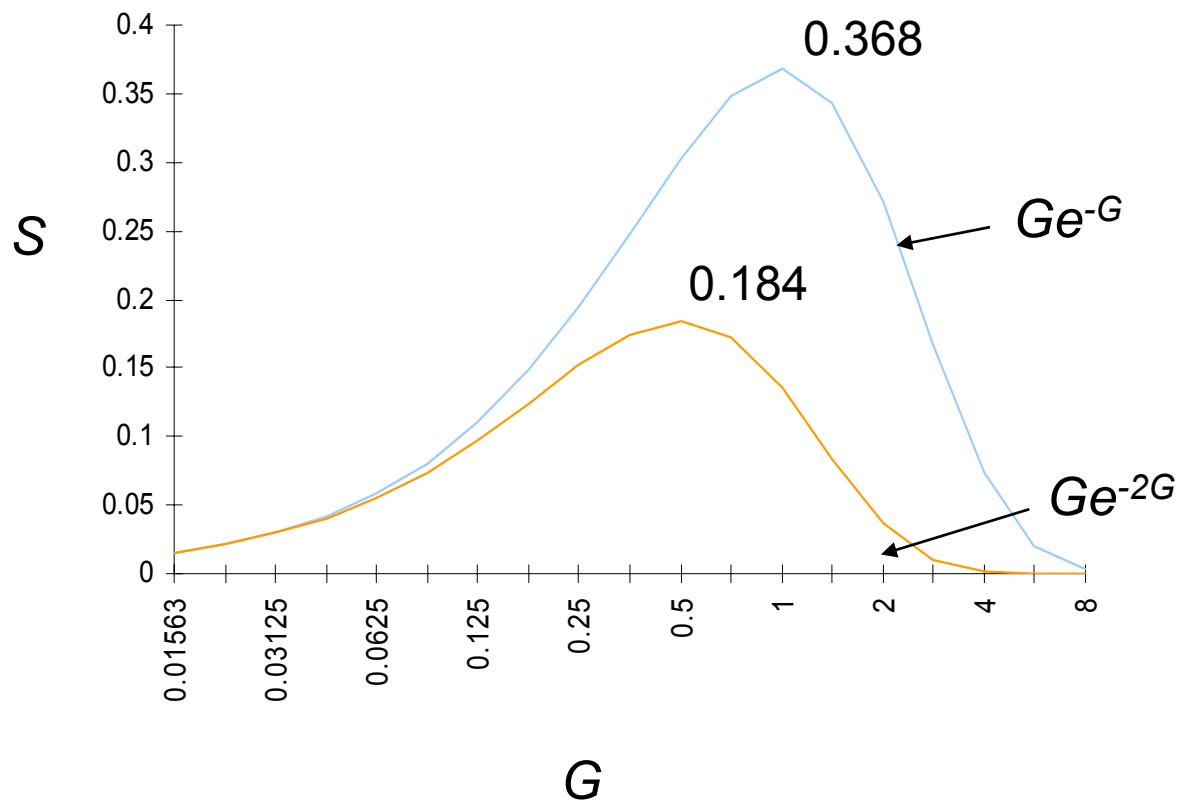


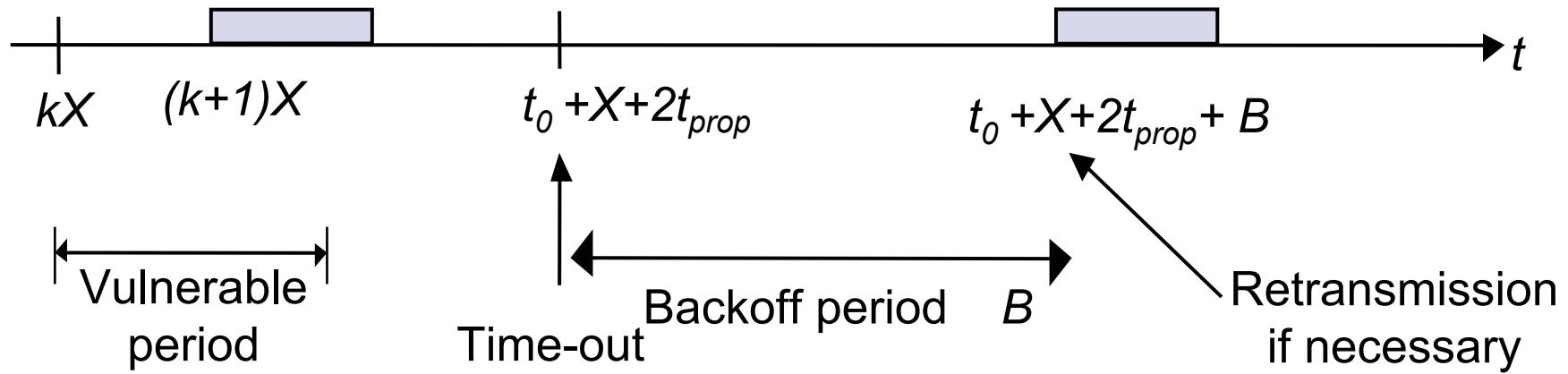


$E[T]/X$ 

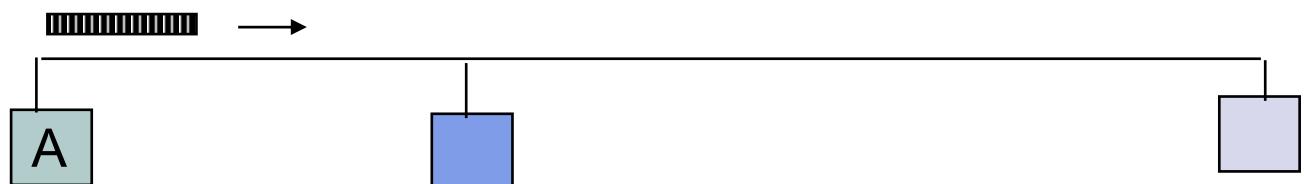




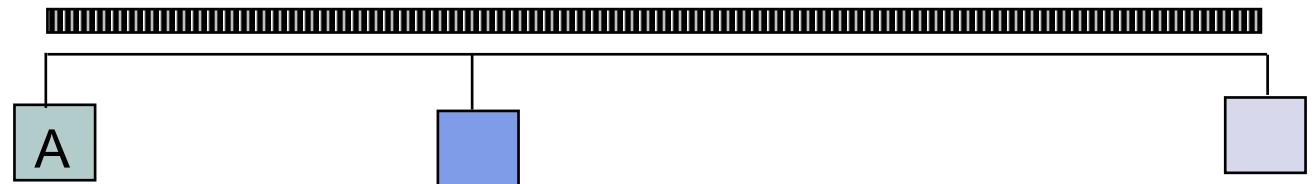


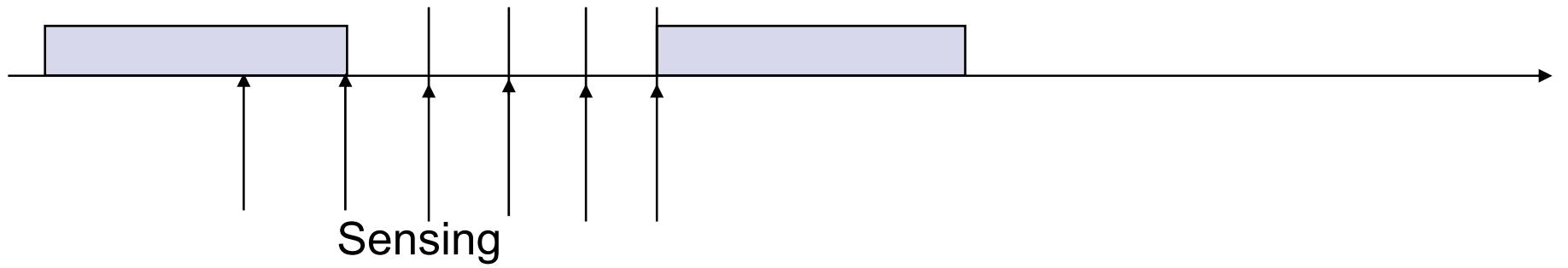


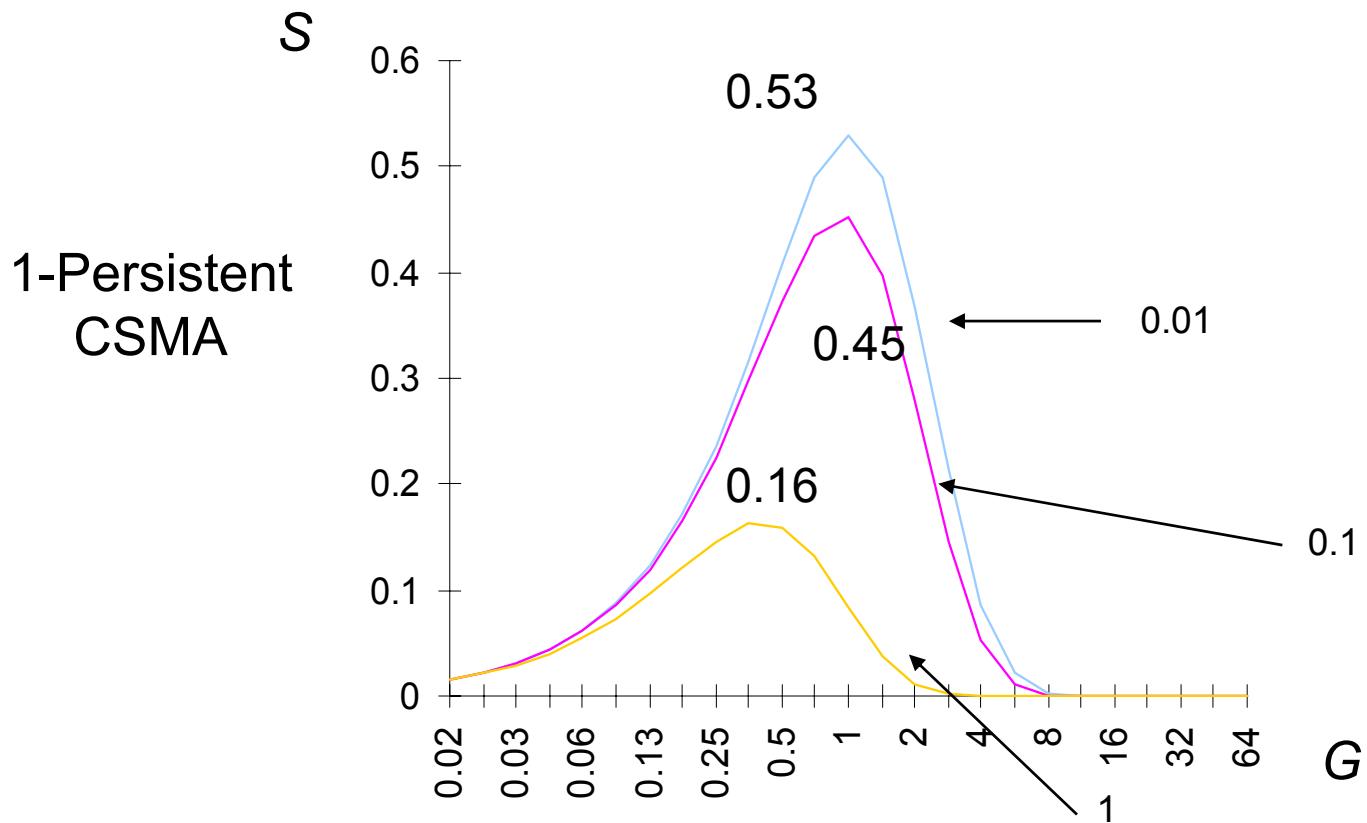
Station A
begins
transmission
at $t=0$



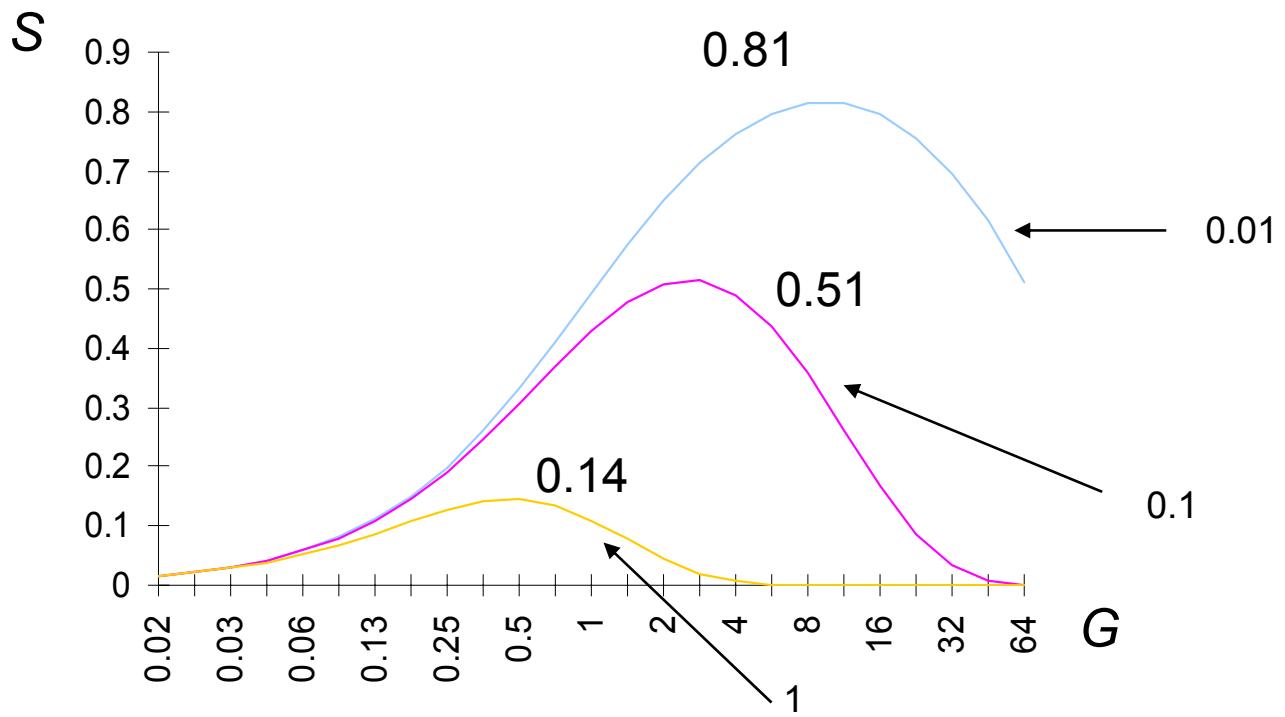
Station A
captures
channel
at $t=t_{prop}$

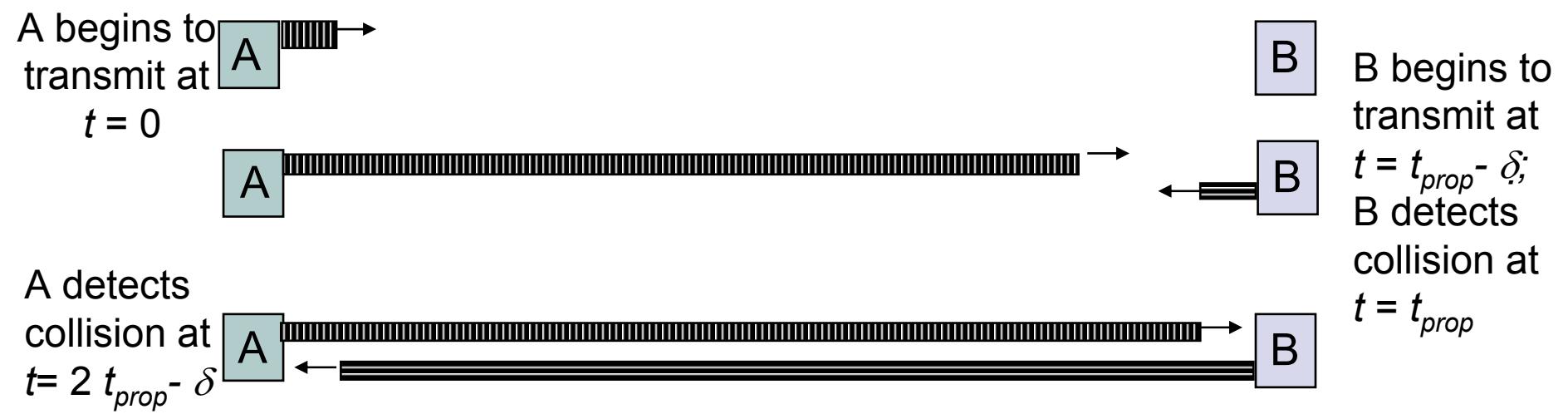


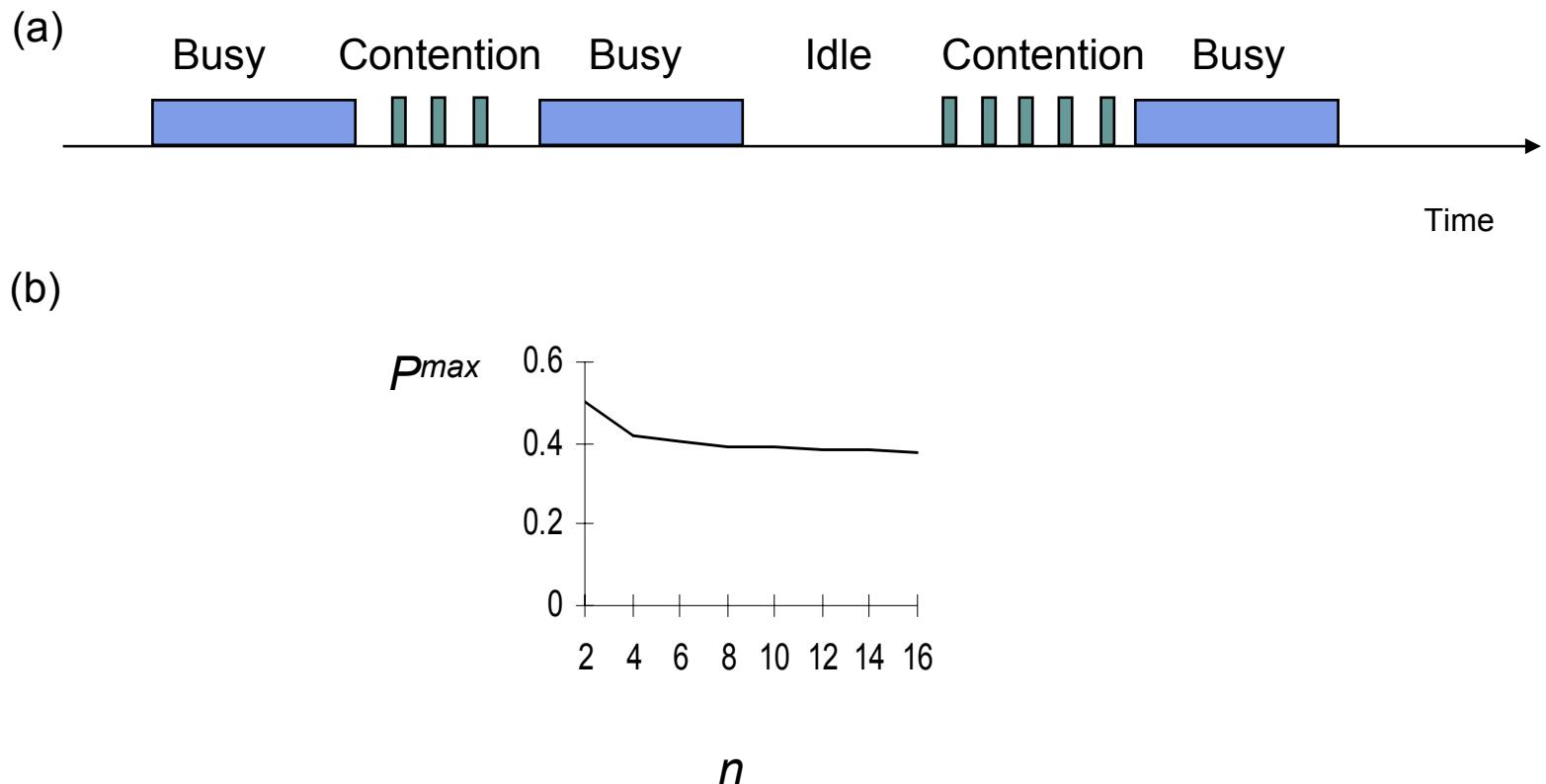


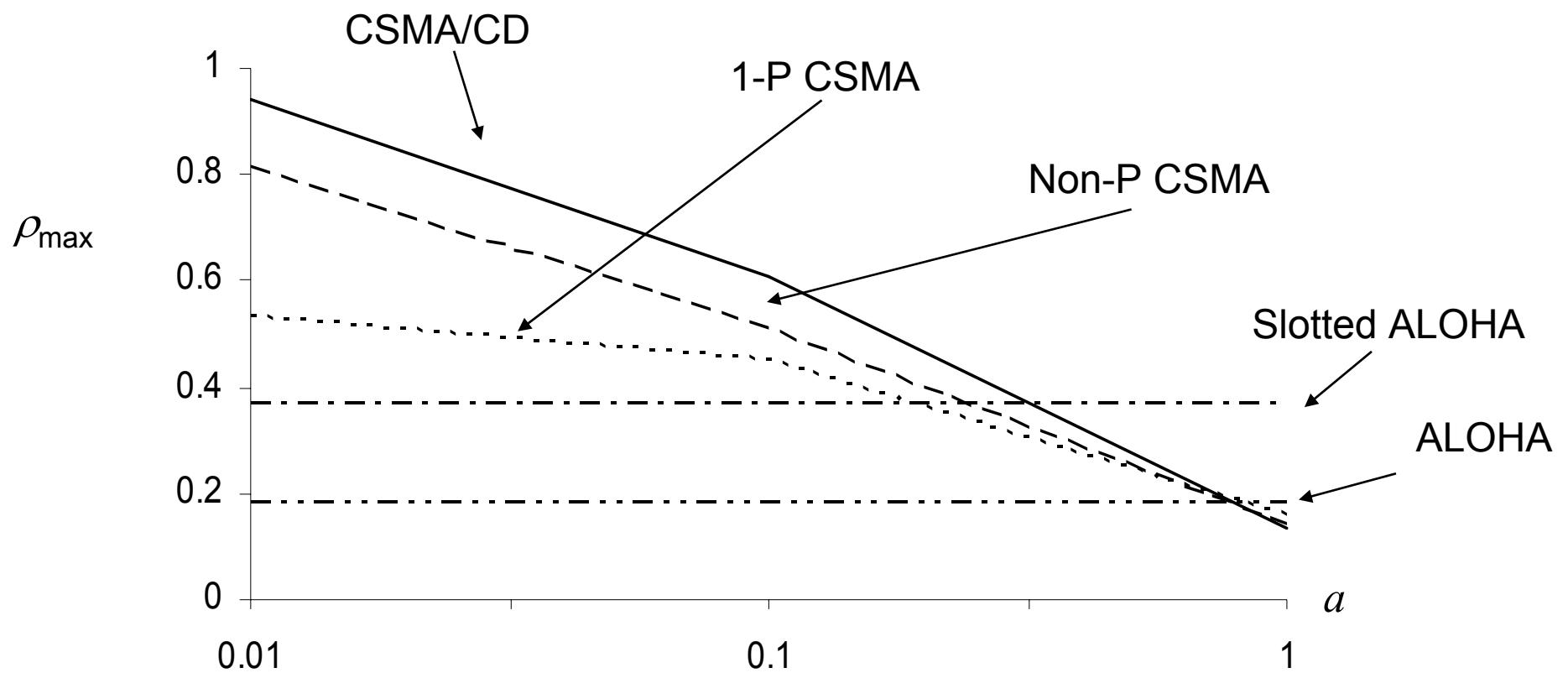


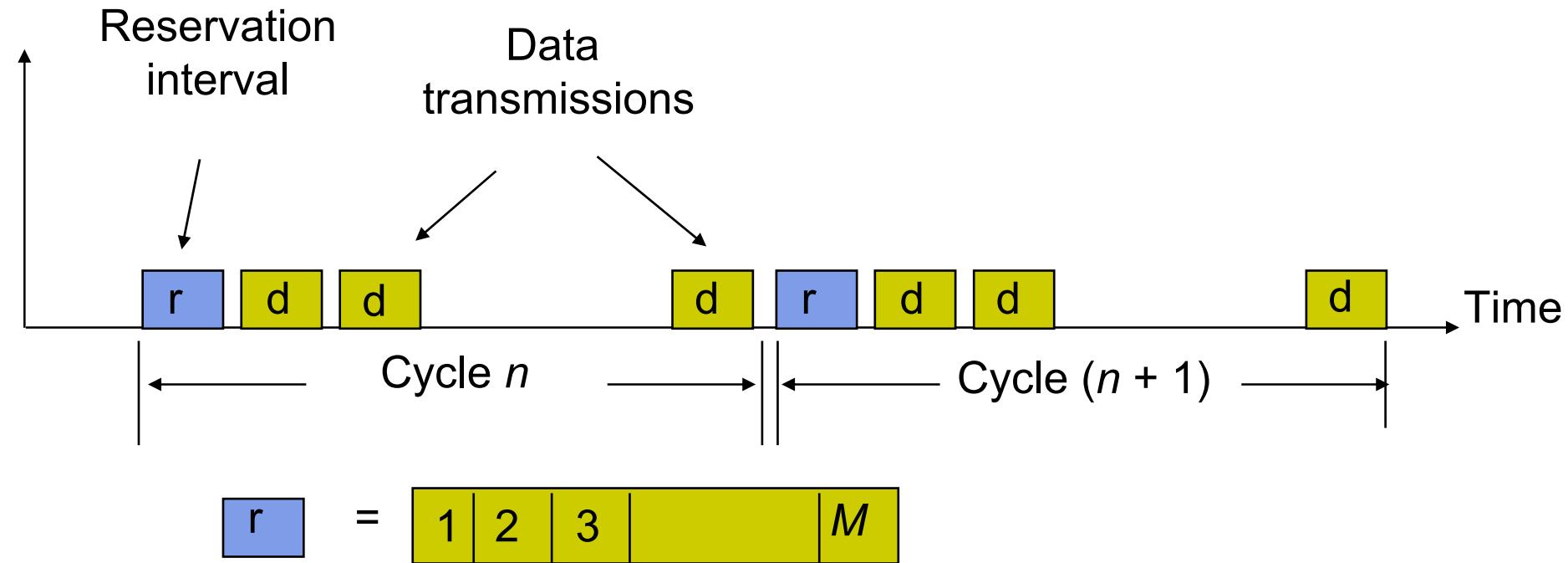
Non-Persistent CSMA







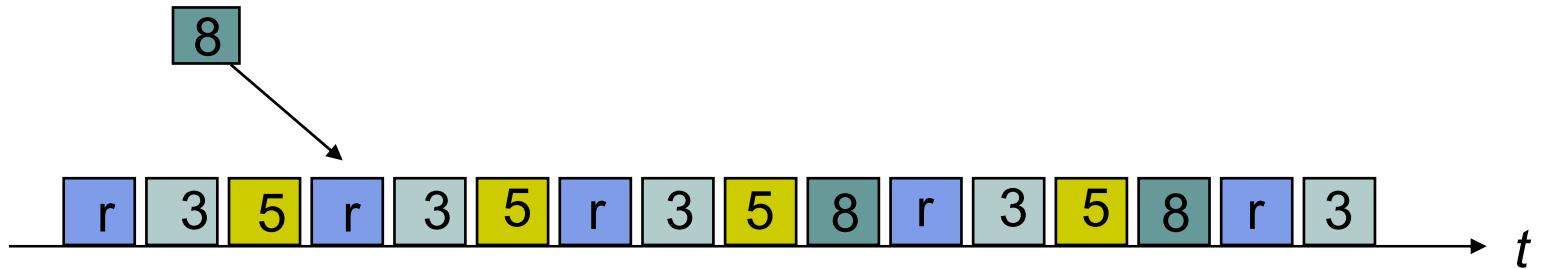


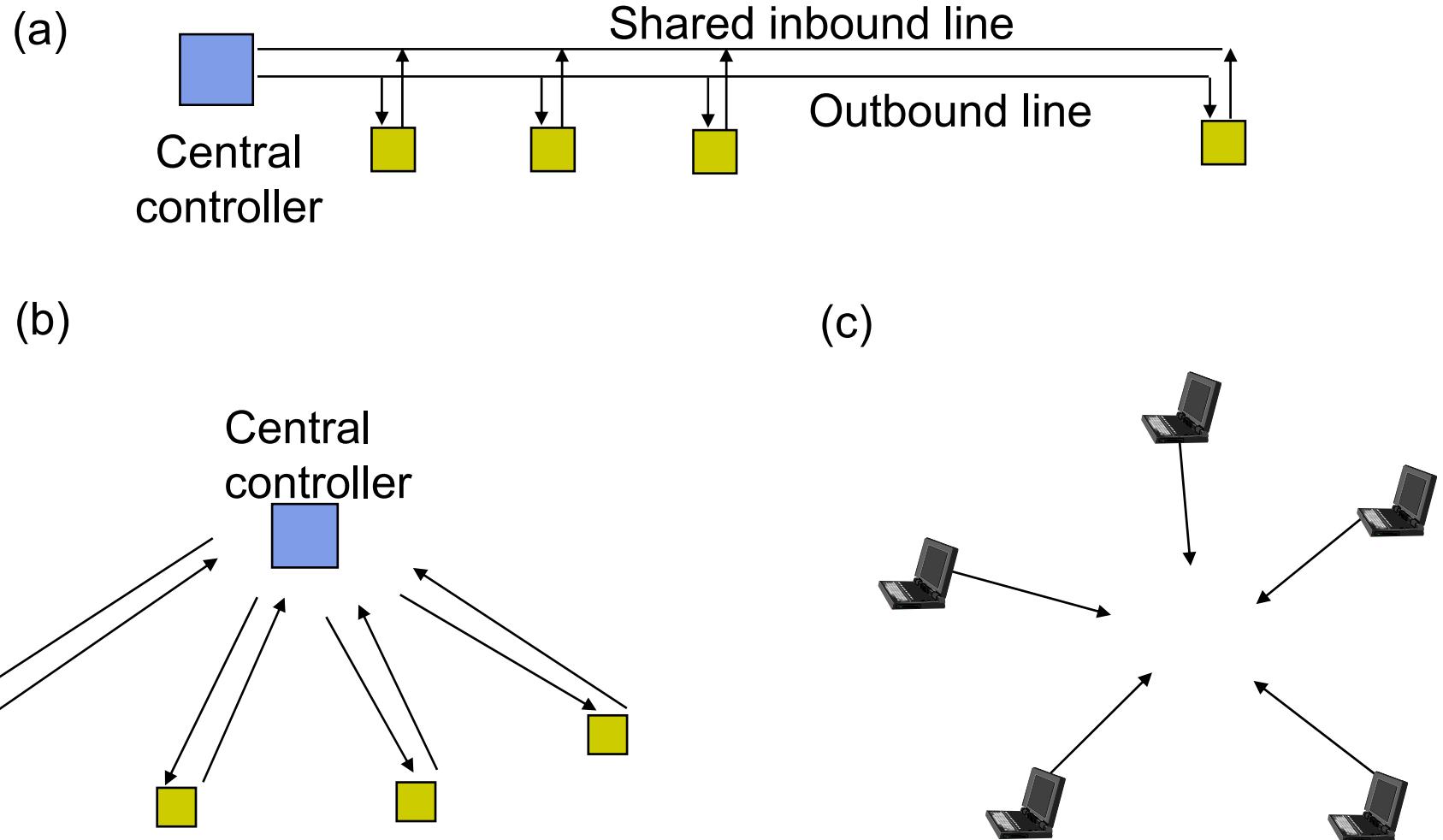


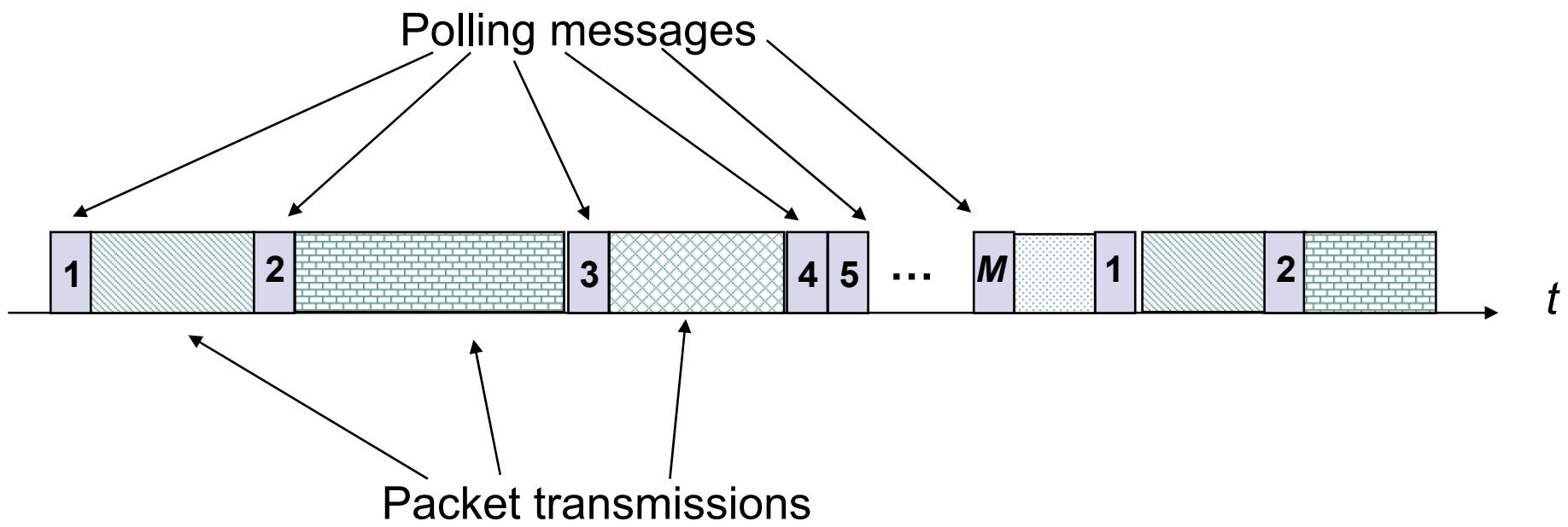
(a)

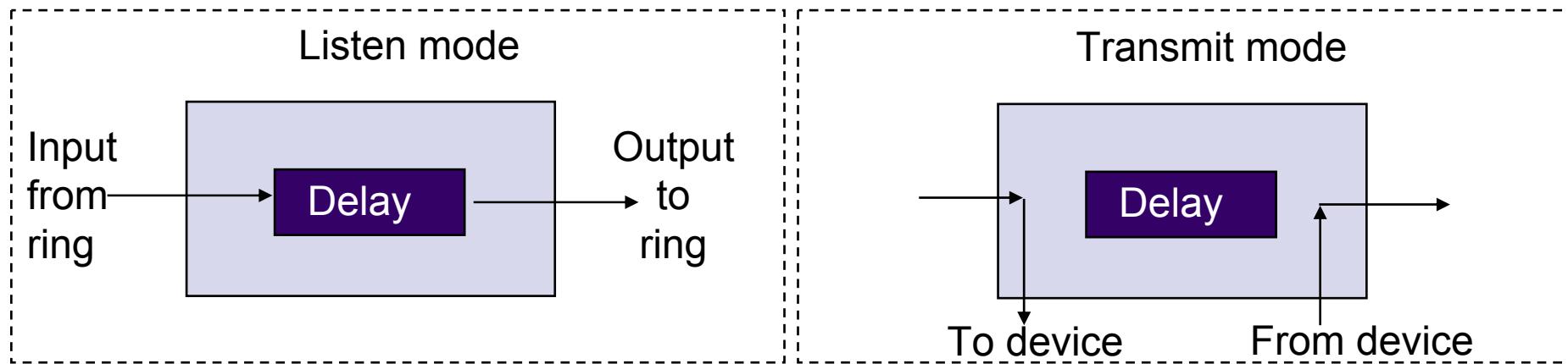
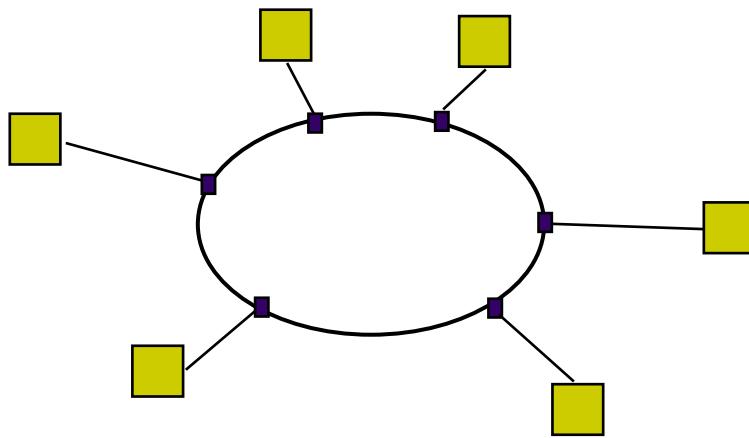


(b)

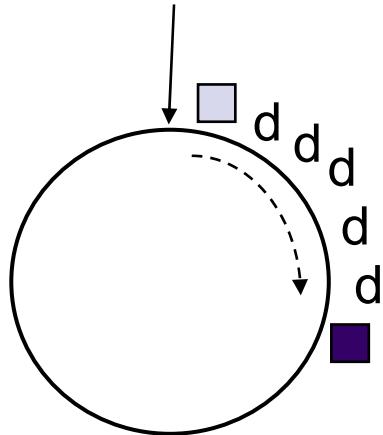




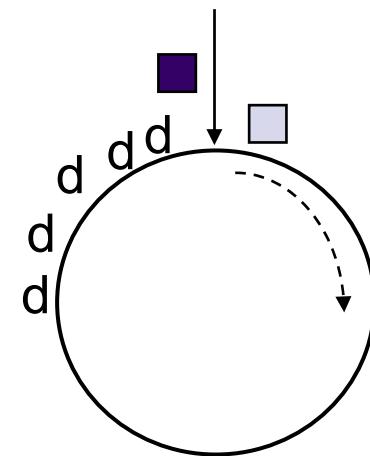




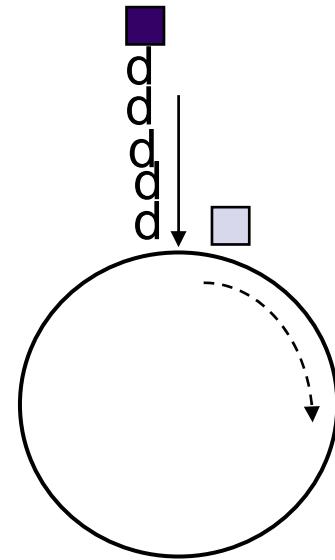
a)



b)

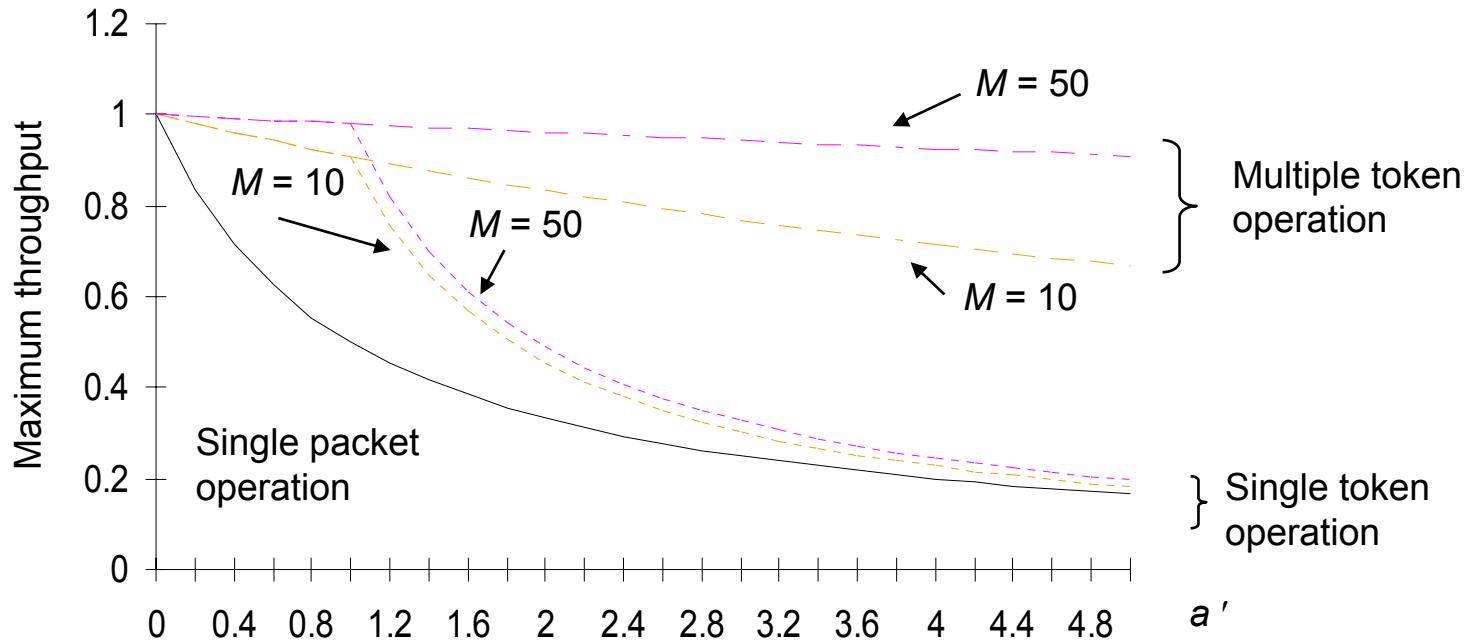


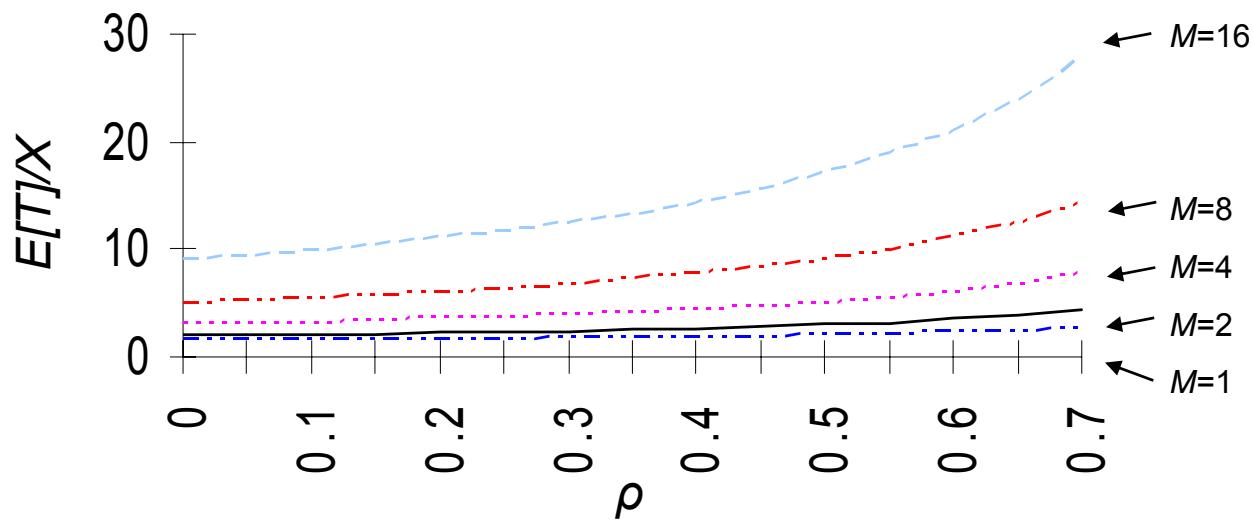
c)

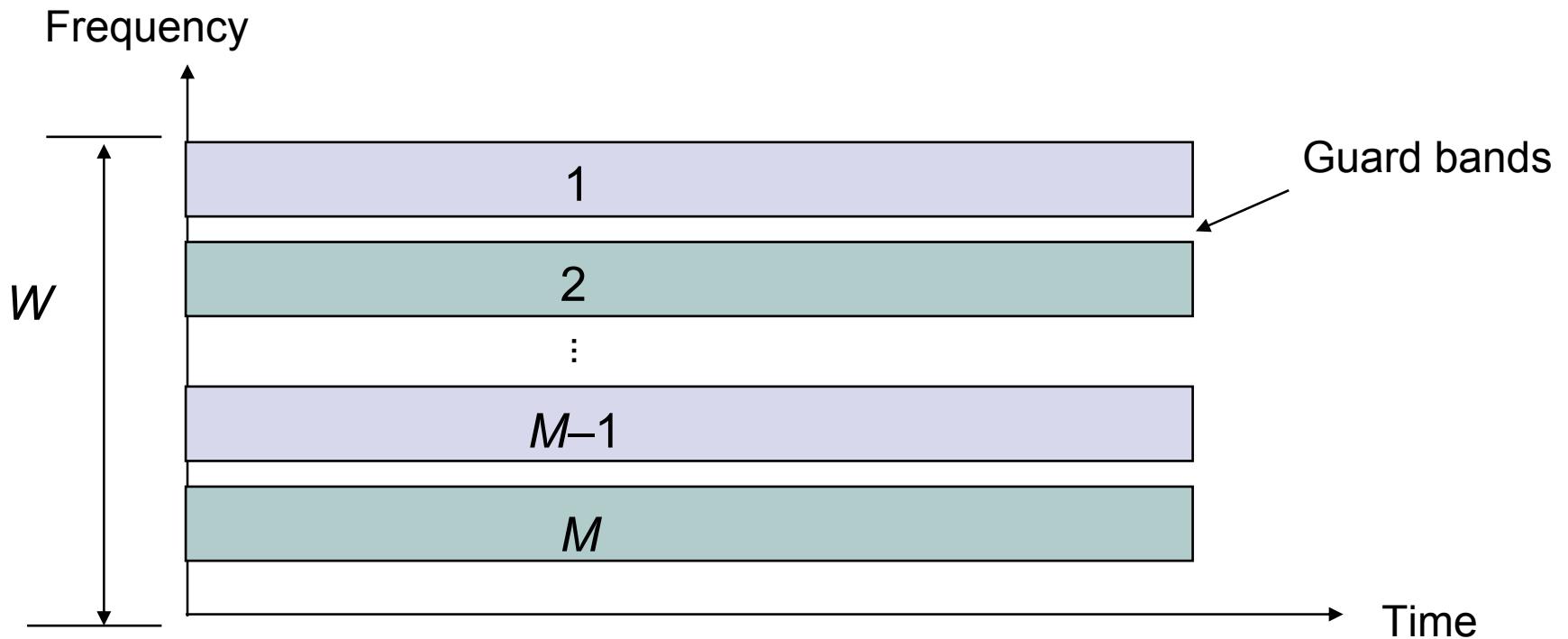


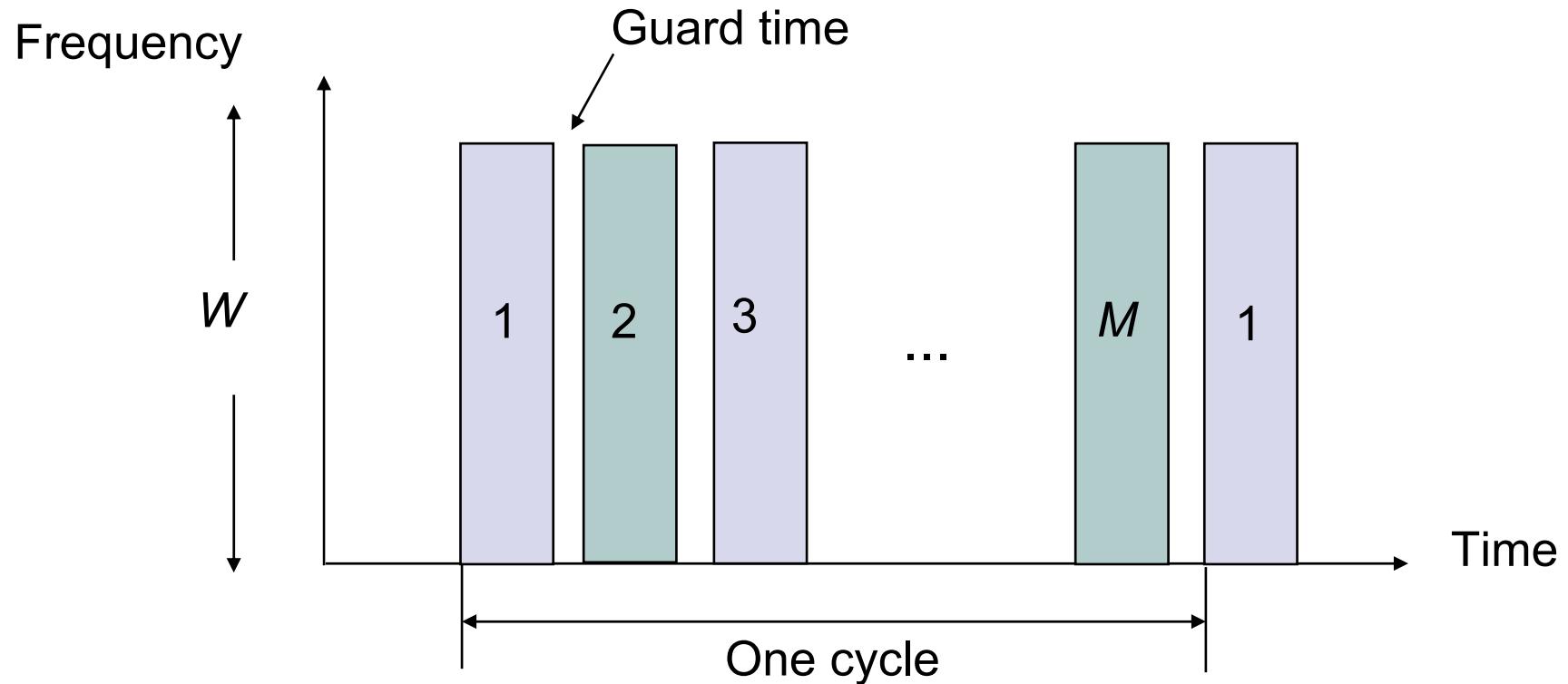
■ Busy token

■ Free token



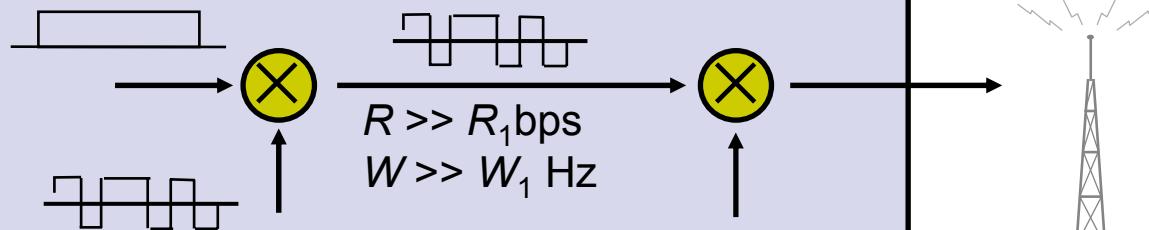




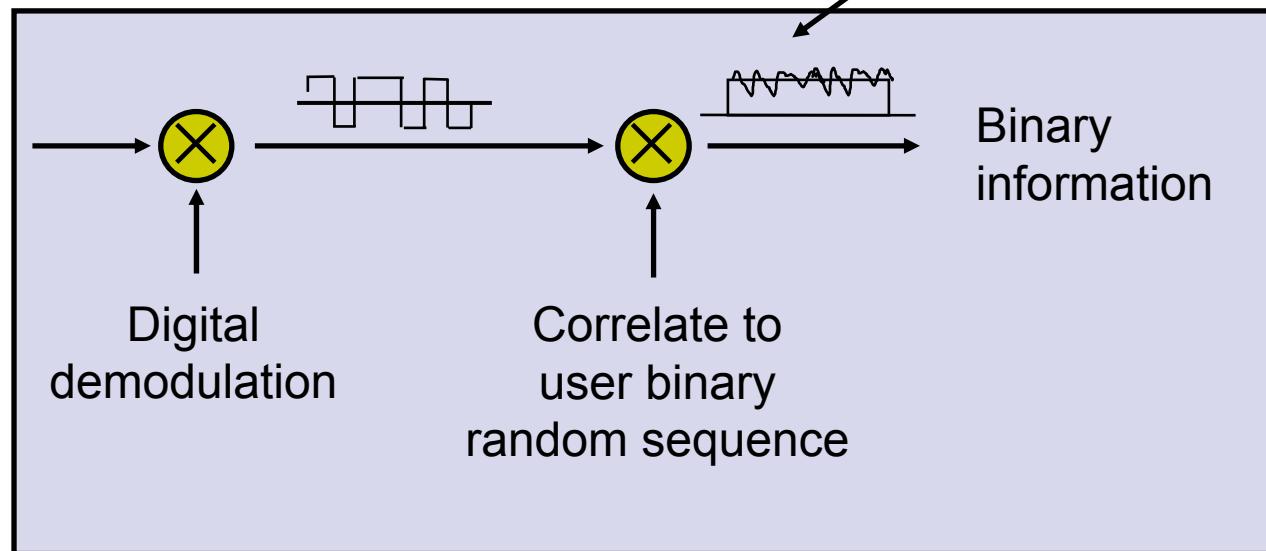


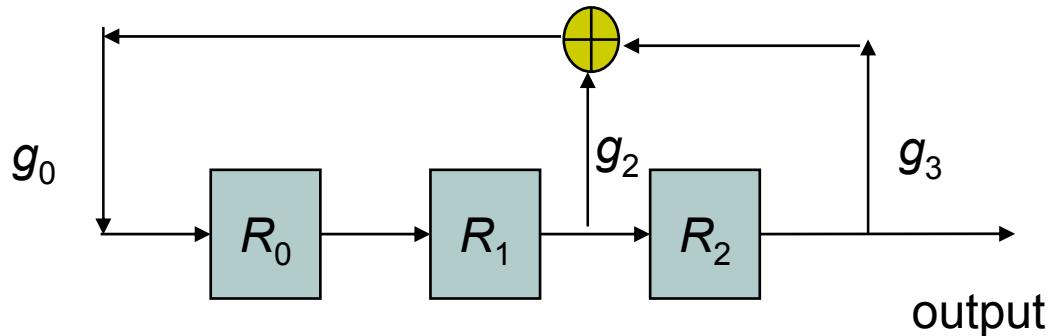
Transmitter from one user

Binary information
 R_1 bps
 W_1 Hz



Signals from all transmitters



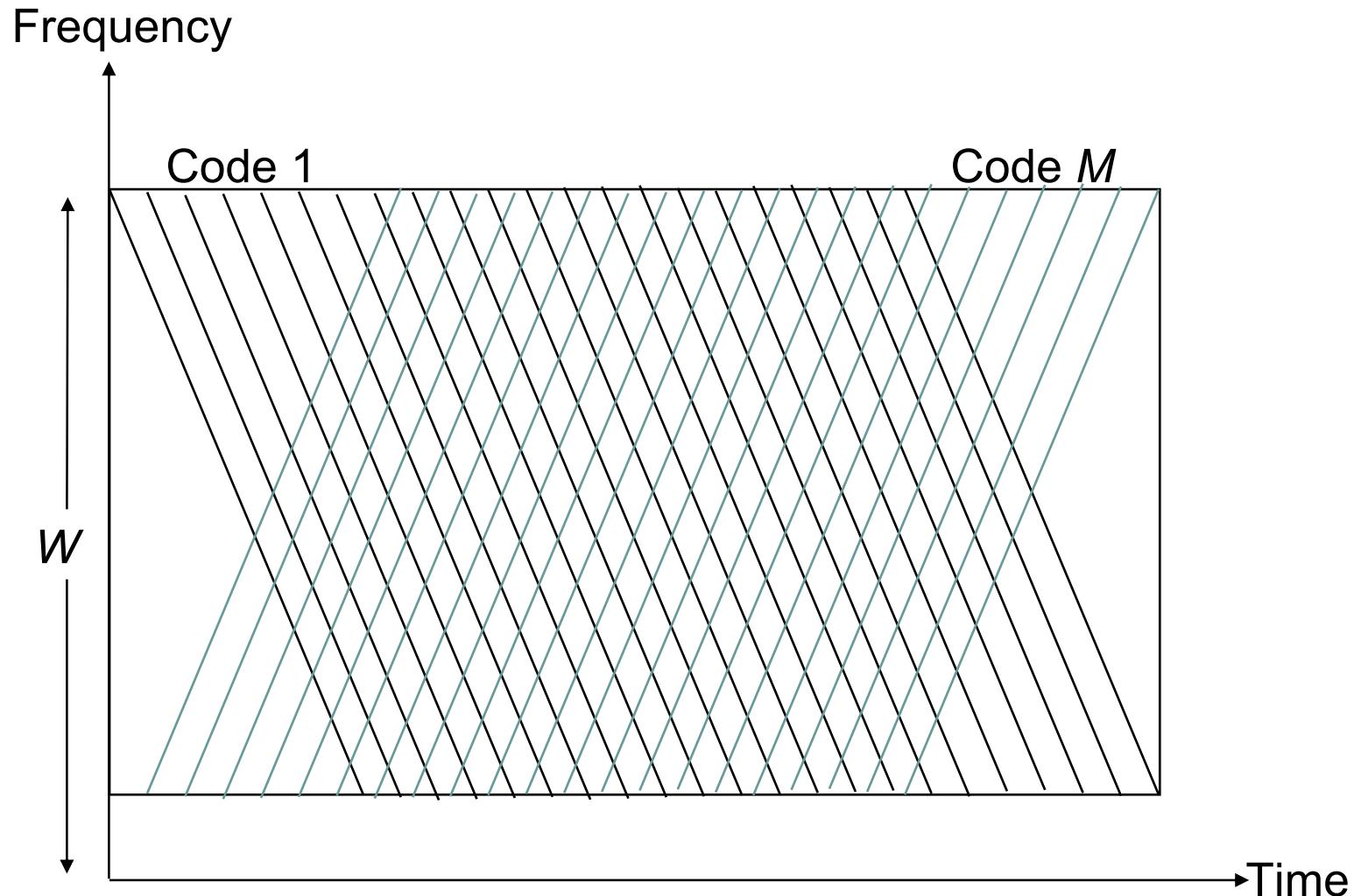


$$g(x) = x^3 + x^2 + 1$$

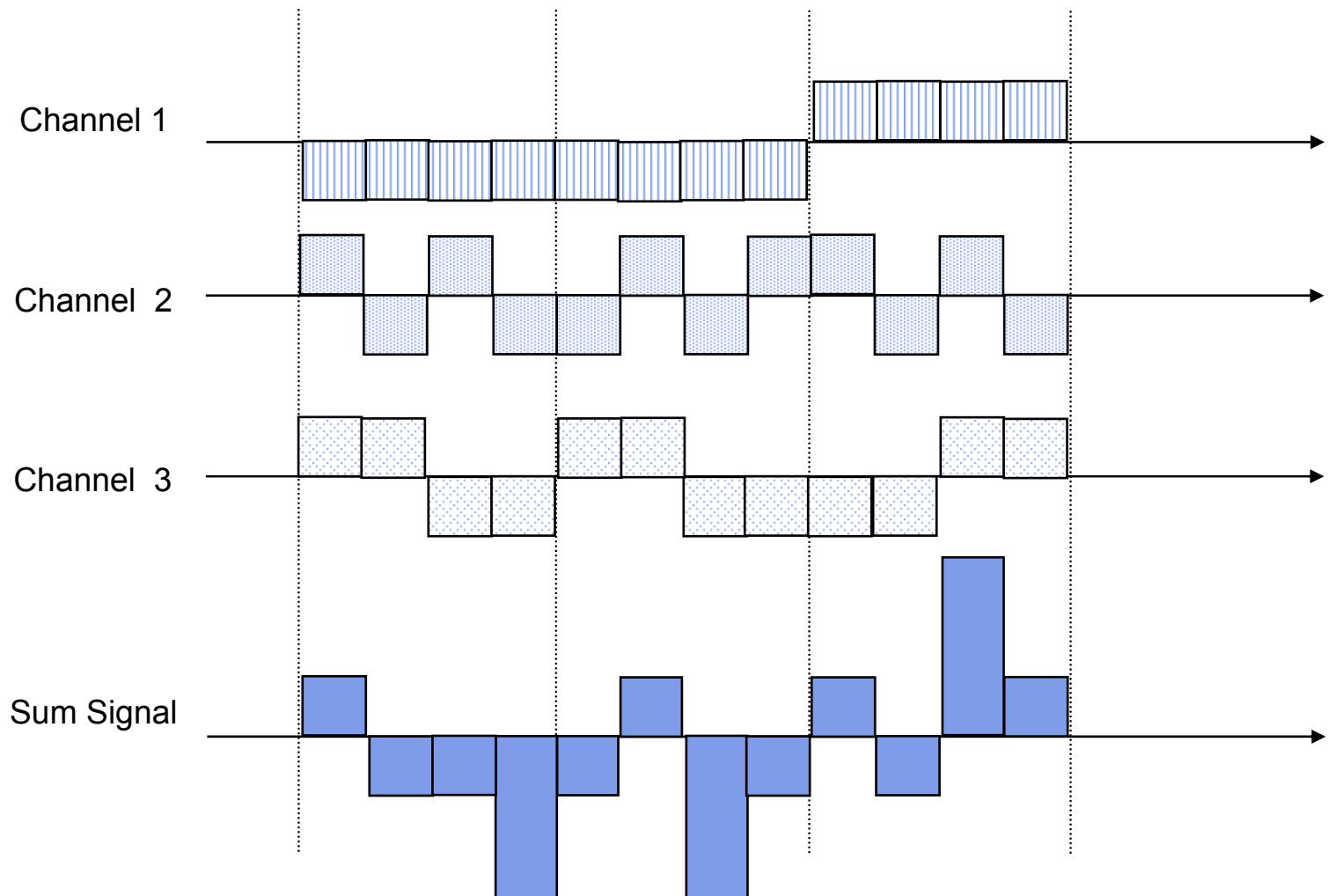
The coefficients of a primitive generator polynomial determine the feedback taps

Time	R_0	R_1	R_2
0	1	0	0
1	0	1	0
2	1	0	1
3	1	1	0
4	1	1	1
5	0	1	1
6	0	0	1
7	1	0	0

Sequence repeats from here onwards

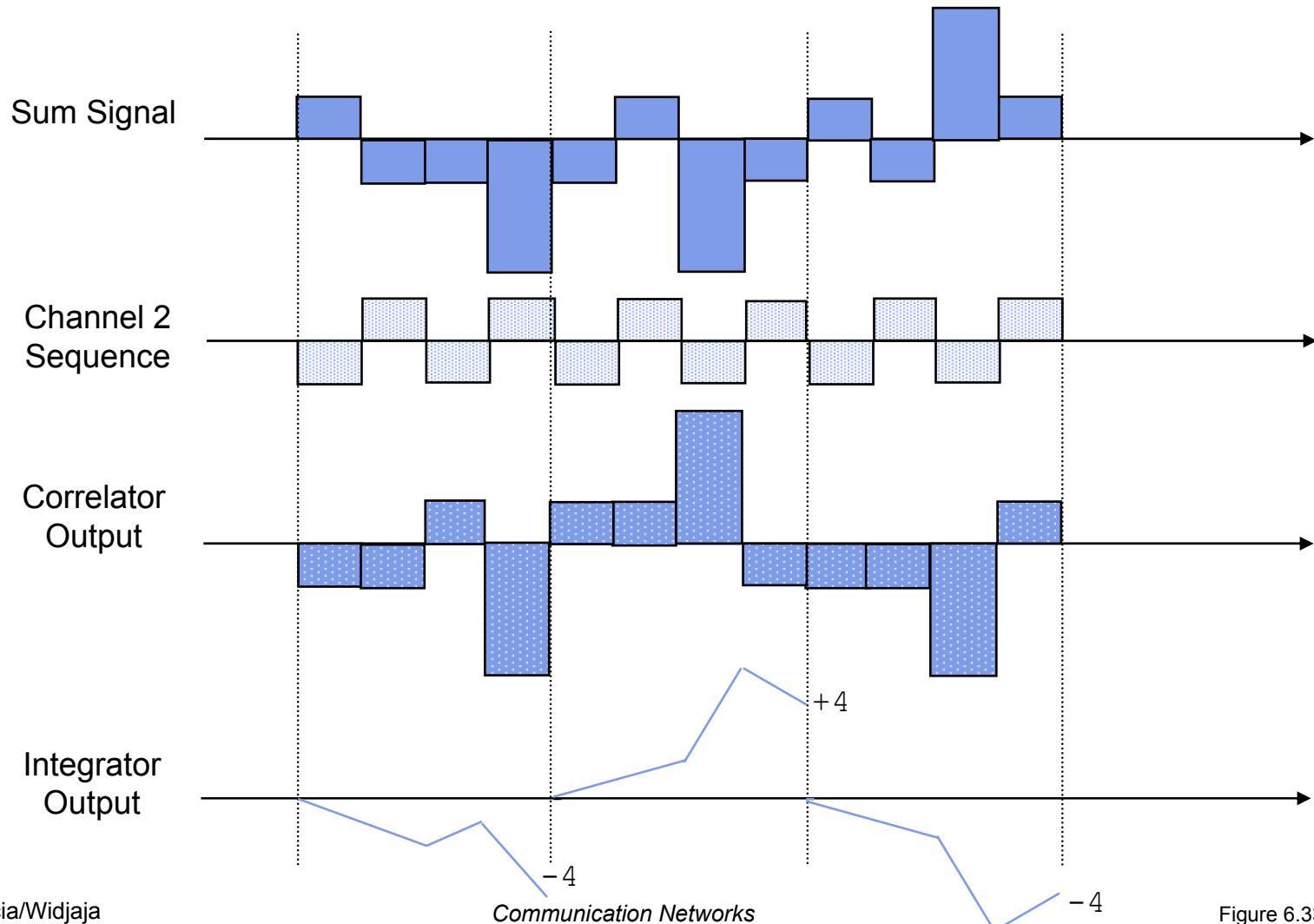


Channel 1: 110 \rightarrow +1+1+1 \rightarrow $(-1, -1, -1, -1), (-1, -1, -1, -1), (+1, +1, +1, +1)$
 Channel 2: 010 \rightarrow -1+1-1 \rightarrow $(+1, -1, +1, -1), (-1, +1, -1, +1), (+1, -1, +1, -1)$
 Channel 3: 001 \rightarrow -1-1+1 \rightarrow $(+1, +1, -1, -1), (+1, +1, -1, -1), (-1, -1, +1, +1)$
 Sum Signal: $(+1, -1, -1, -3), (-1, +1, -3, -1), (+1, -1, +3, +1)$



Sum Signal:
 Channel 2 Sequence:
 Correlator Output:
 Integrated Output:
 Binary Output:

$(+1, -1, -1, -3)$	$(-1, +1, -3, -1)$	$(+1, -1, +3, +1)$
$(-1, +1, -1, +1)$	$(-1, +1, -1, +1)$	$(-1, +1, -1, +1)$
$(-1, -1, +1, -3)$	$(+1, +1, +3, -1)$	$(-1, -1, -3, +1)$
-4,	+4,	-4
0,	1,	0

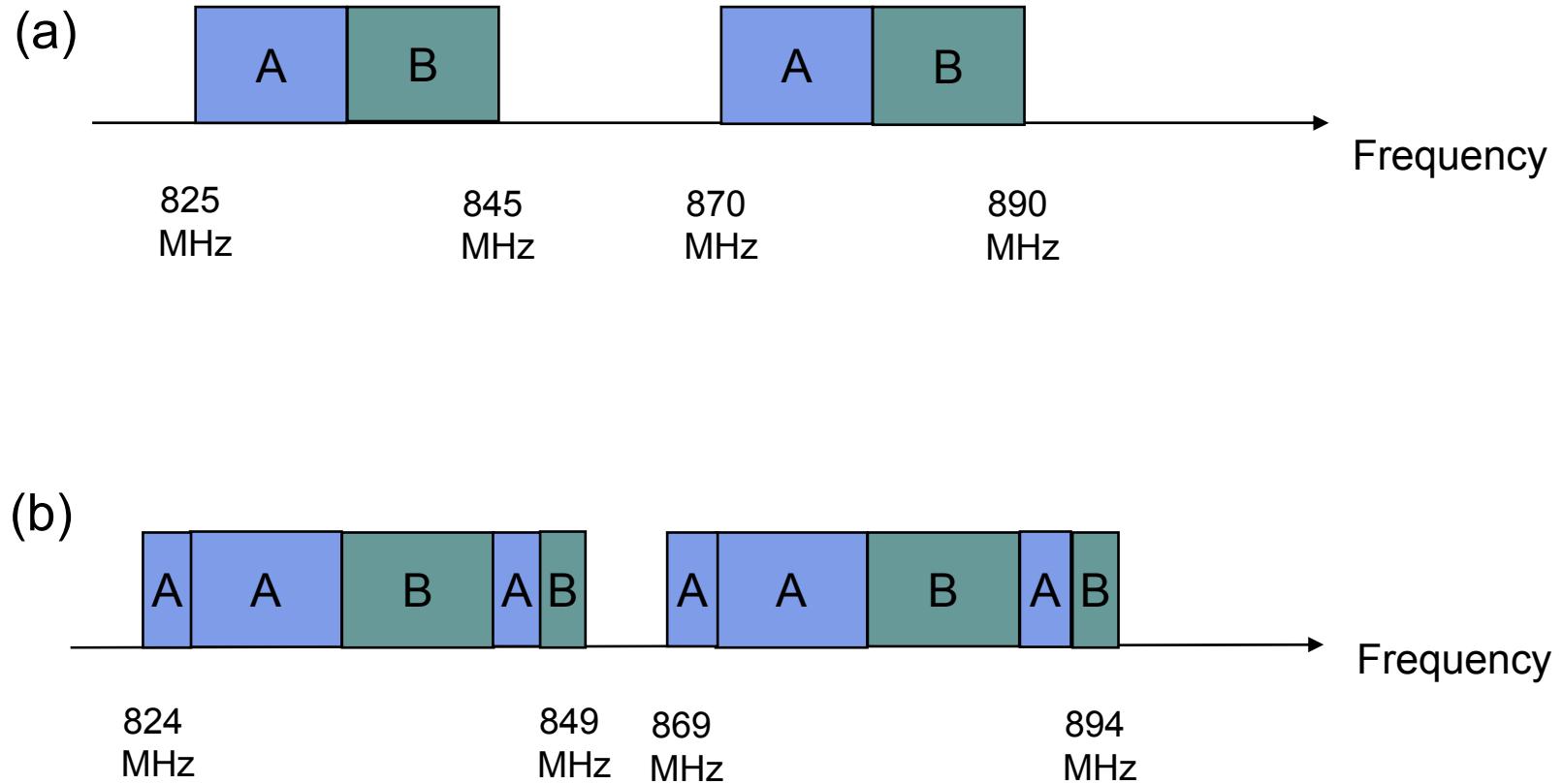


$$\mathbf{W}_1 = \begin{bmatrix} 0 \end{bmatrix}$$

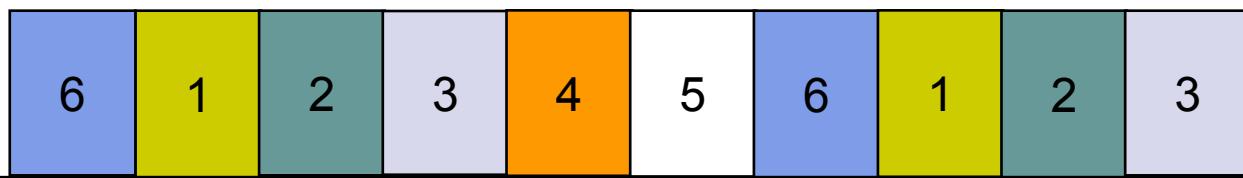
$$\mathbf{W}_2 = \begin{bmatrix} 0 & 0 \\ 0 & 1 \end{bmatrix}$$

$$\mathbf{W}_4 = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 \\ 0 & 1 & 1 & 0 \end{bmatrix}$$

$$\mathbf{W}_8 = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 & 0 & 0 & 1 & 1 \\ 0 & 1 & 1 & 0 & 0 & 1 & 1 & 0 \\ \hline 0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\ 0 & 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 & 1 & 0 & 0 & 1 \end{bmatrix}$$

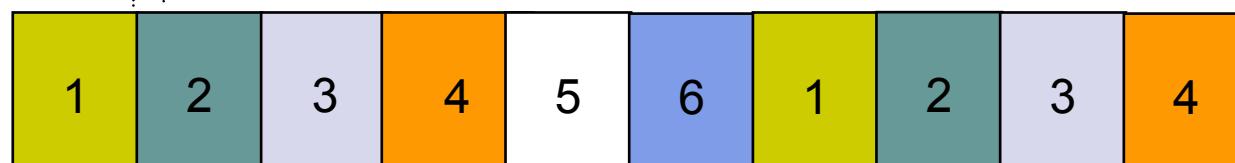


Base to mobile



Time

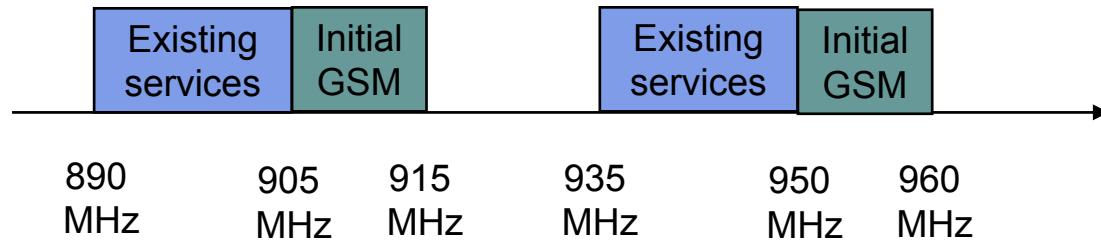
Mobile to base



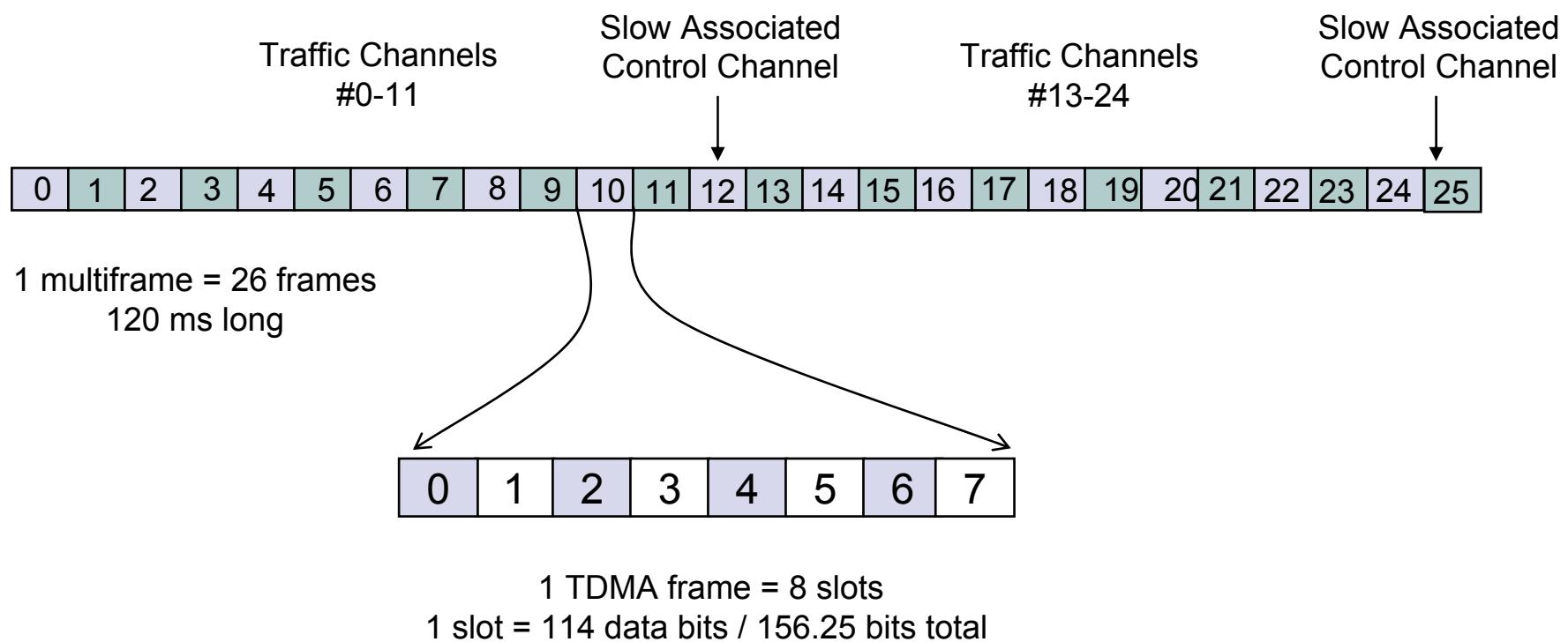
Time

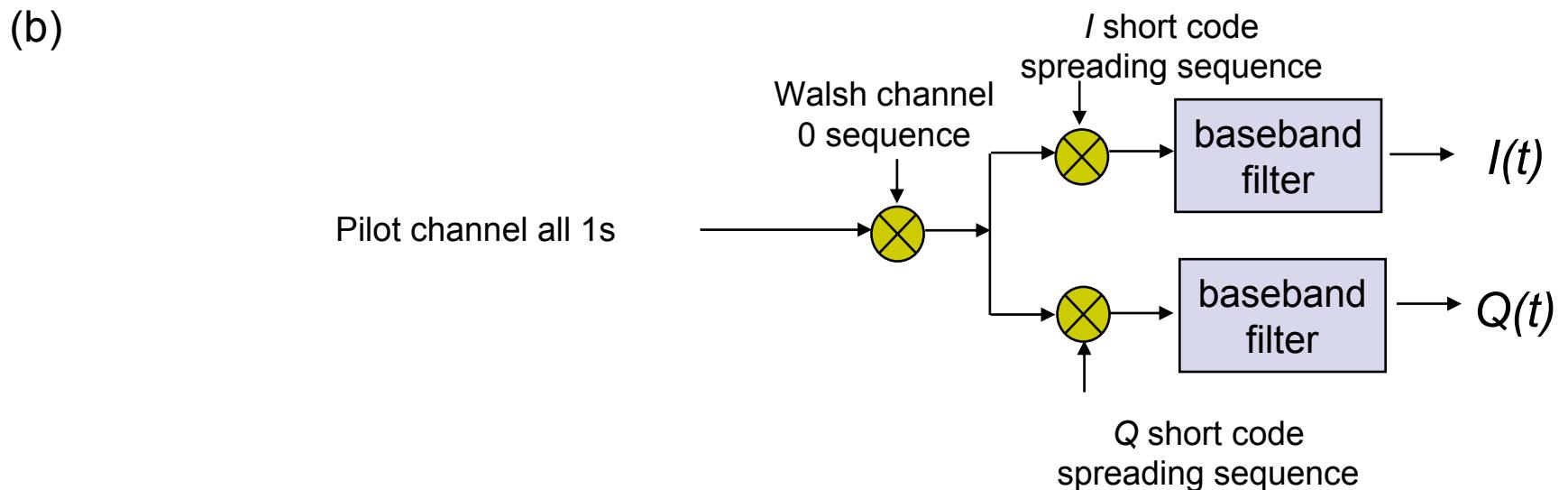
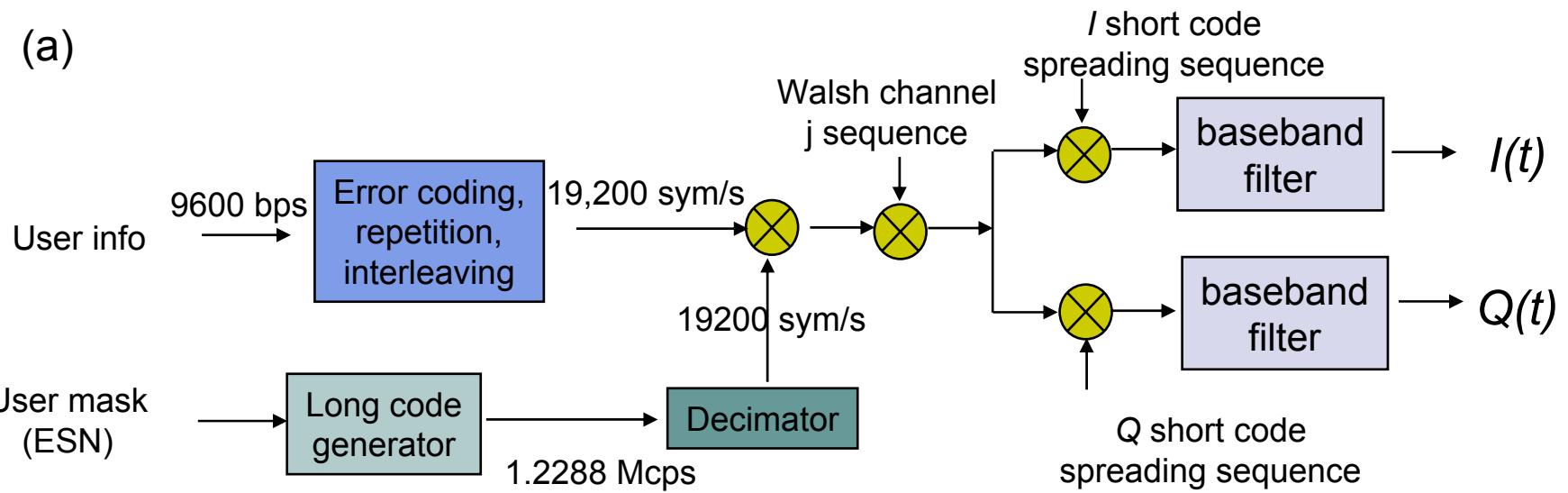
40 ms

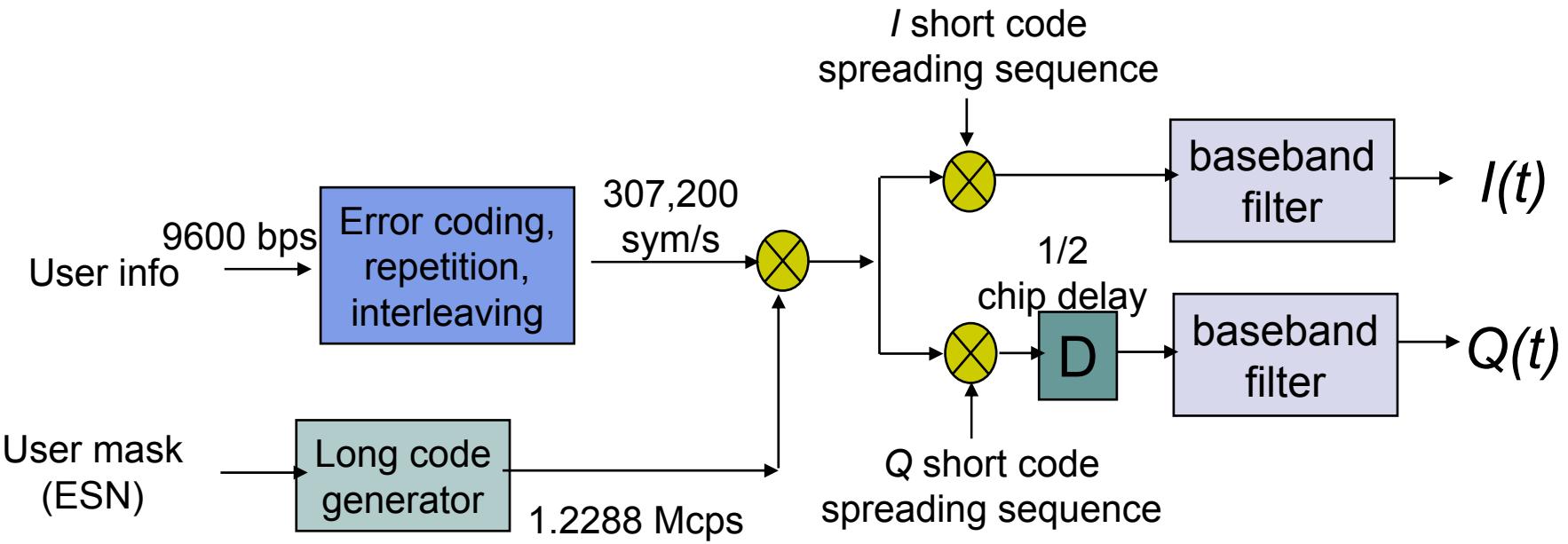
(a)



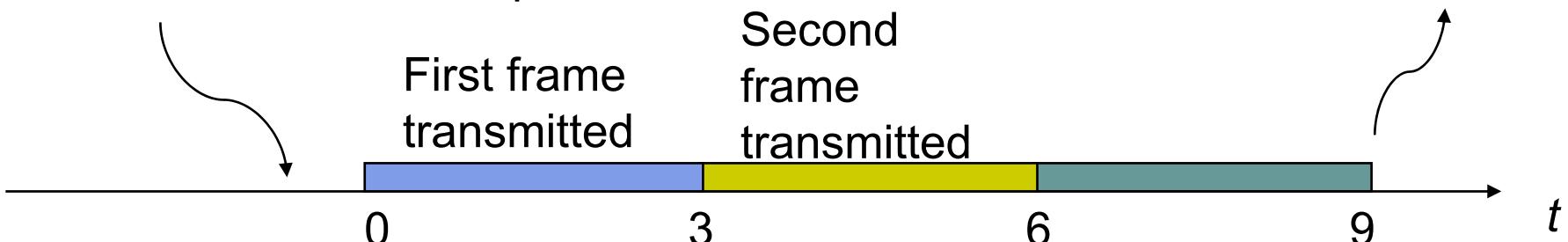
(b)





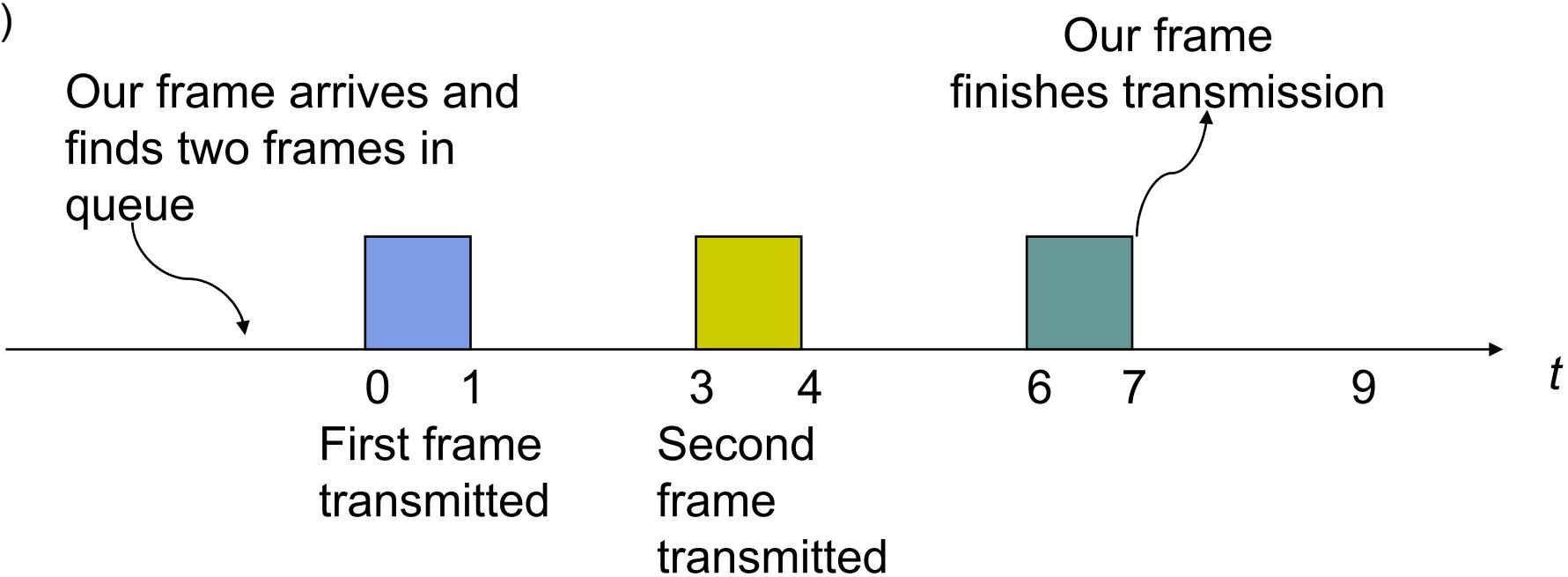


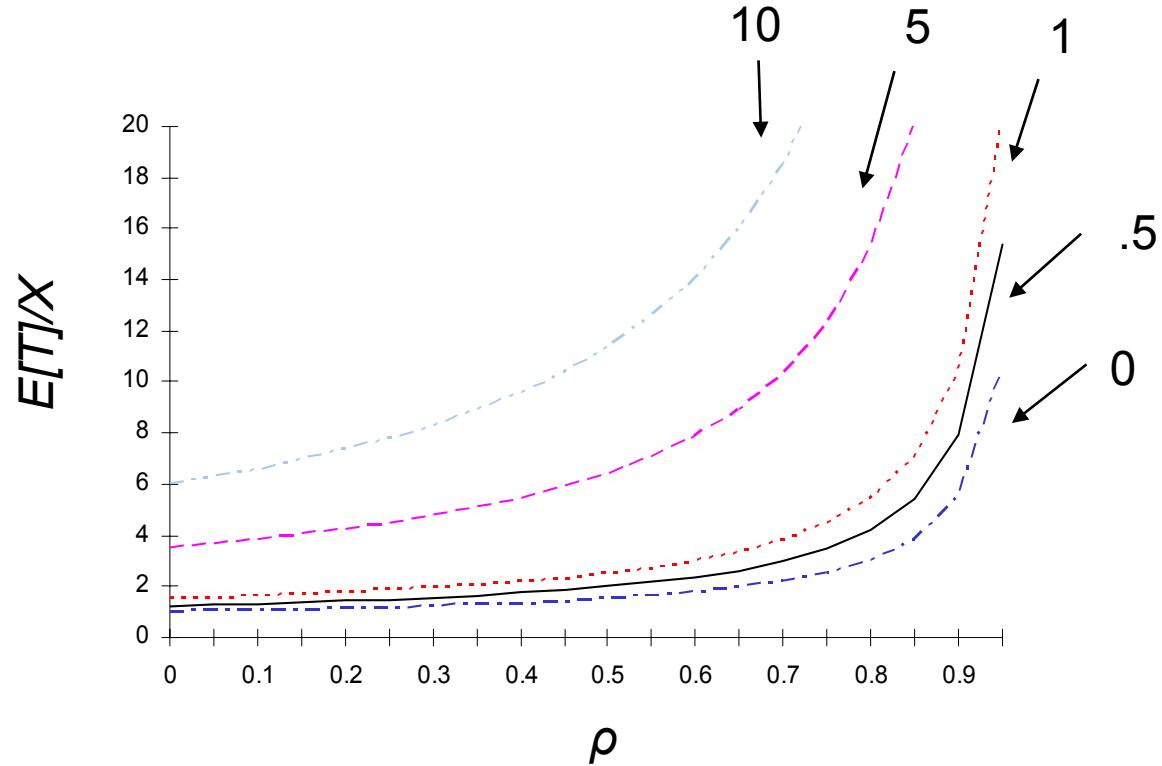
(a) Our frame arrives and finds two frames in queue



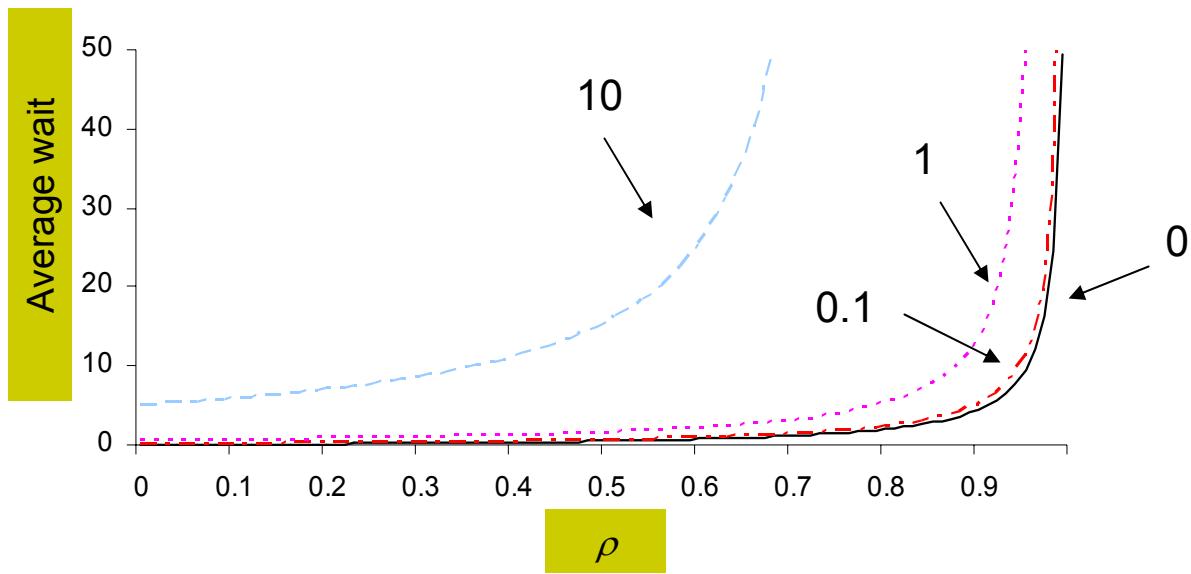
(b)

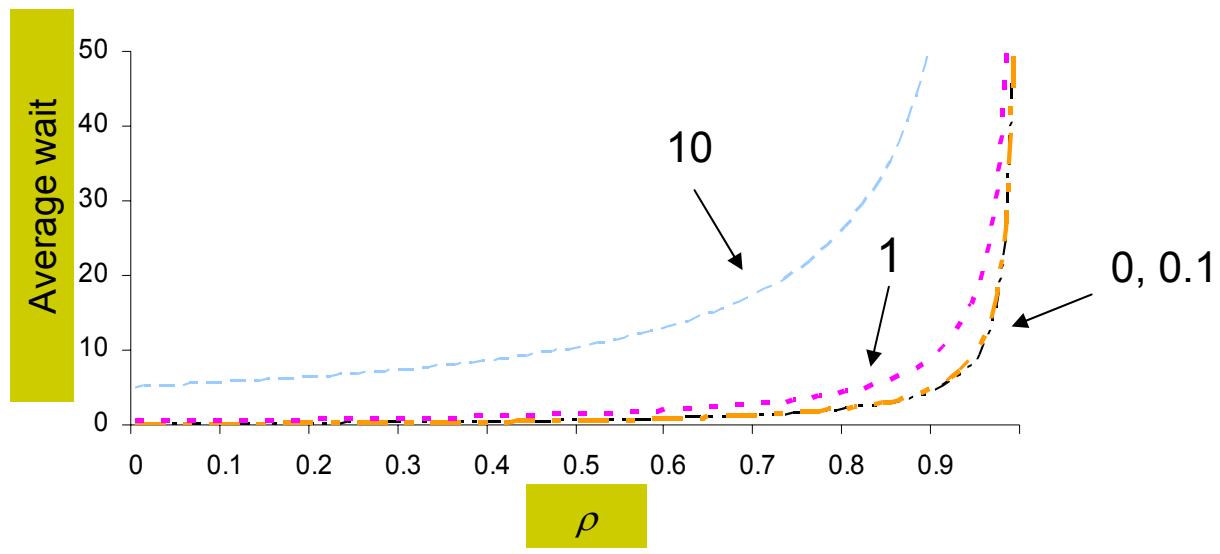
Our frame arrives and finds two frames in queue

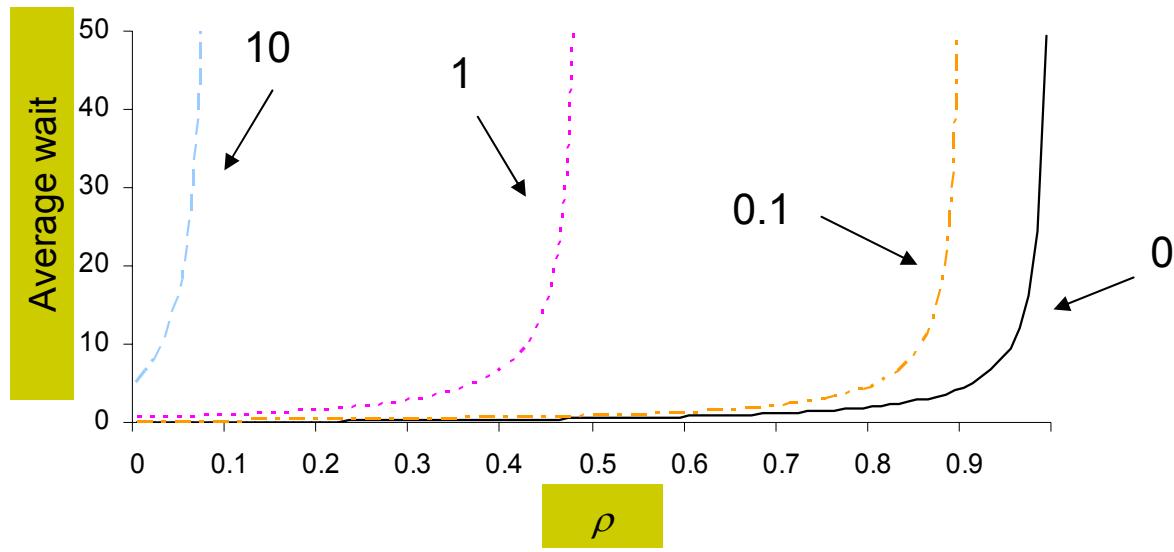




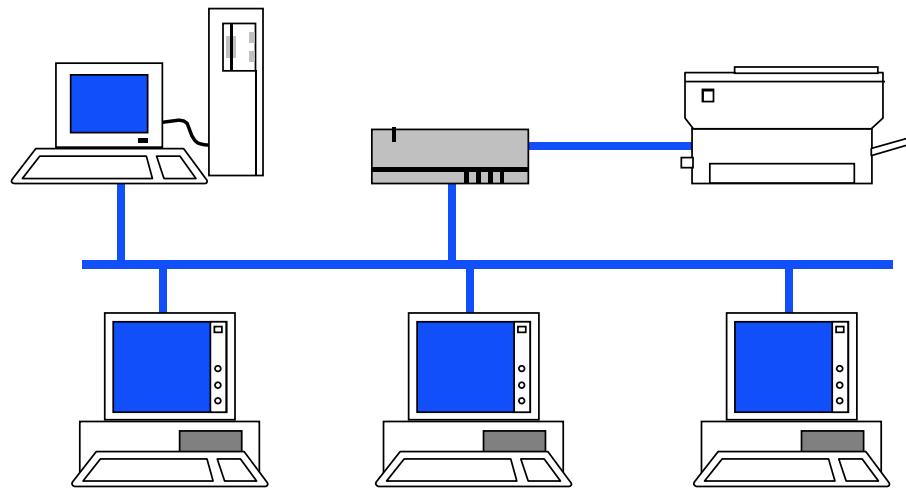
$$a' = \frac{\tau'}{X}$$



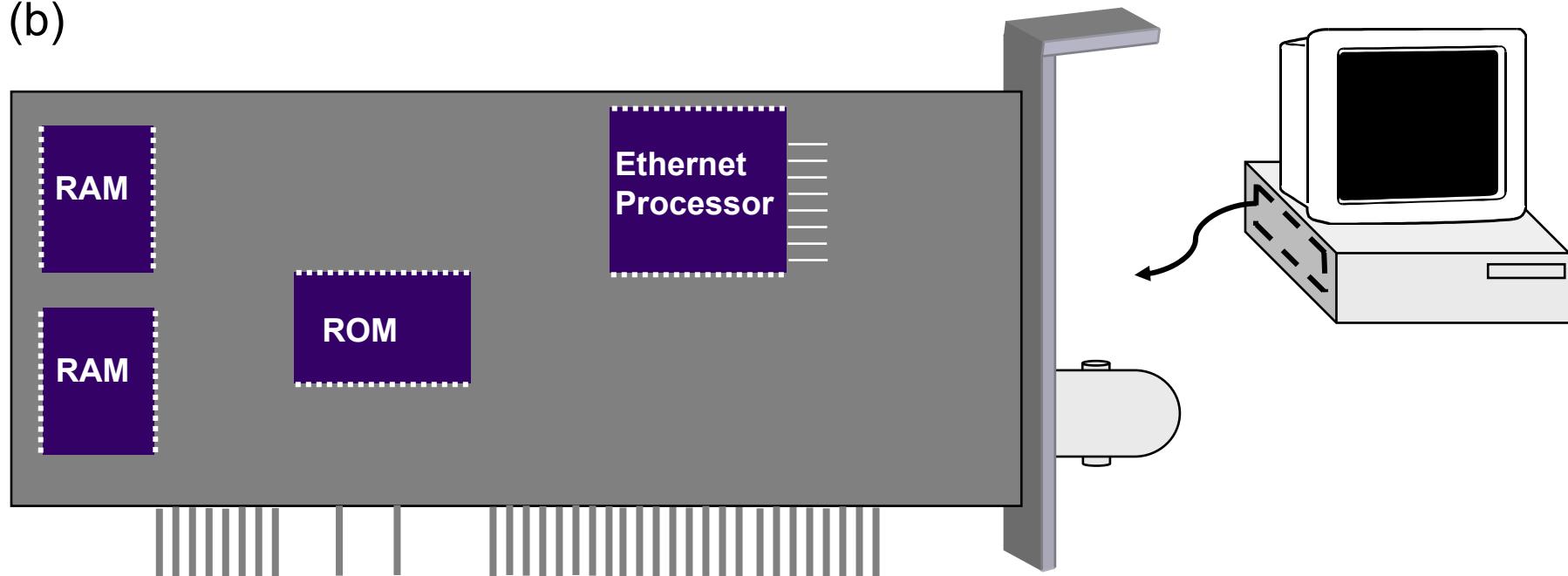


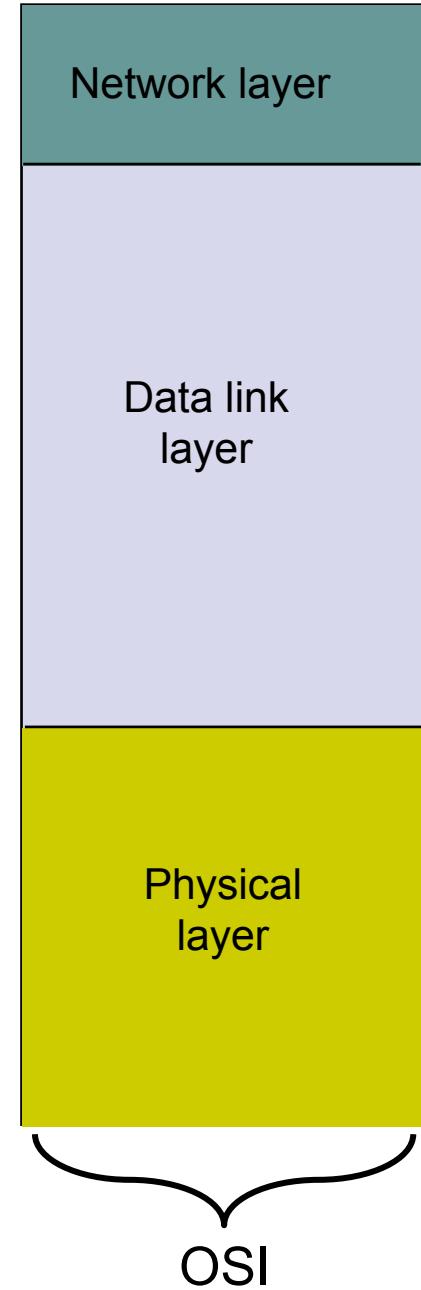
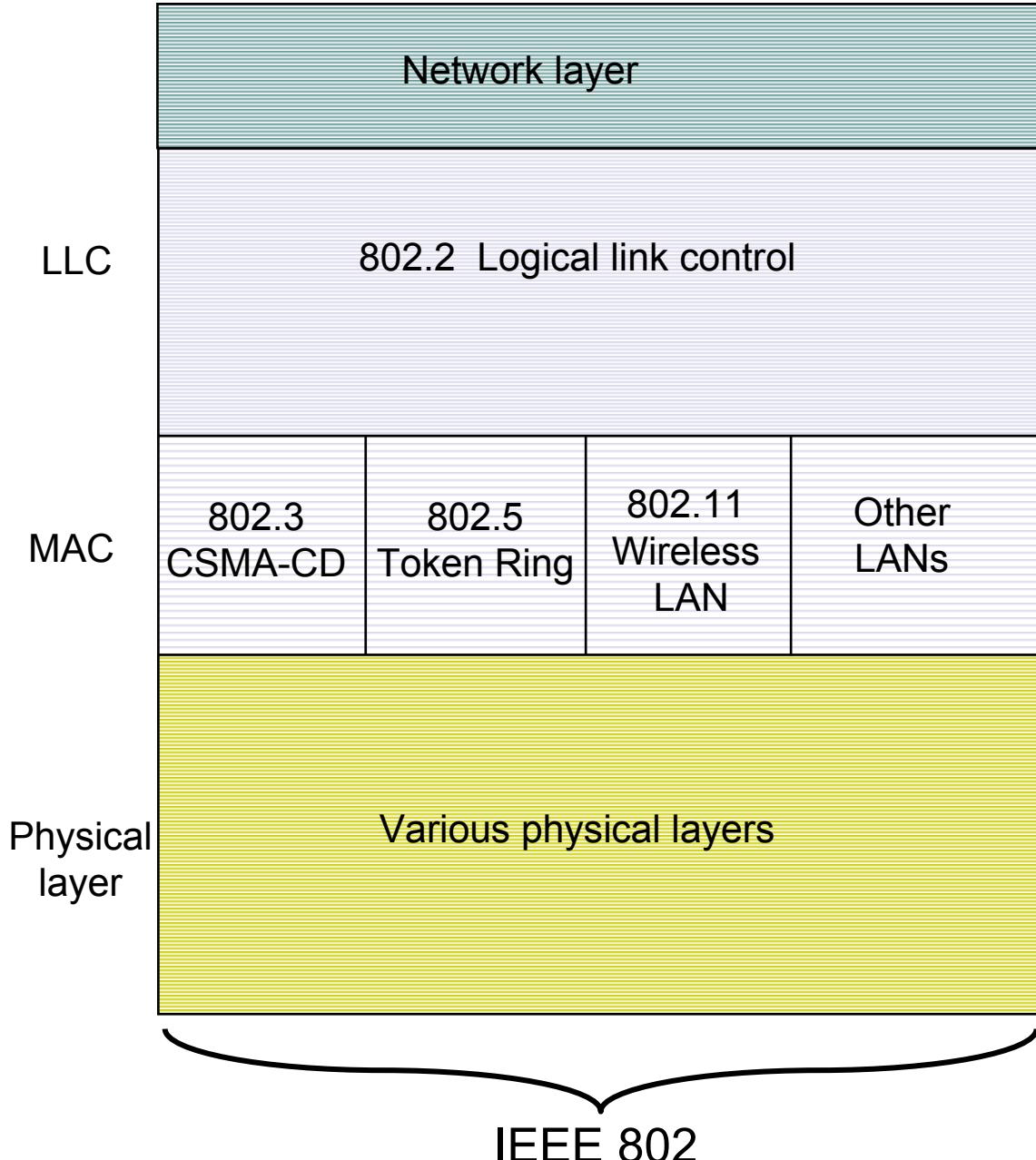


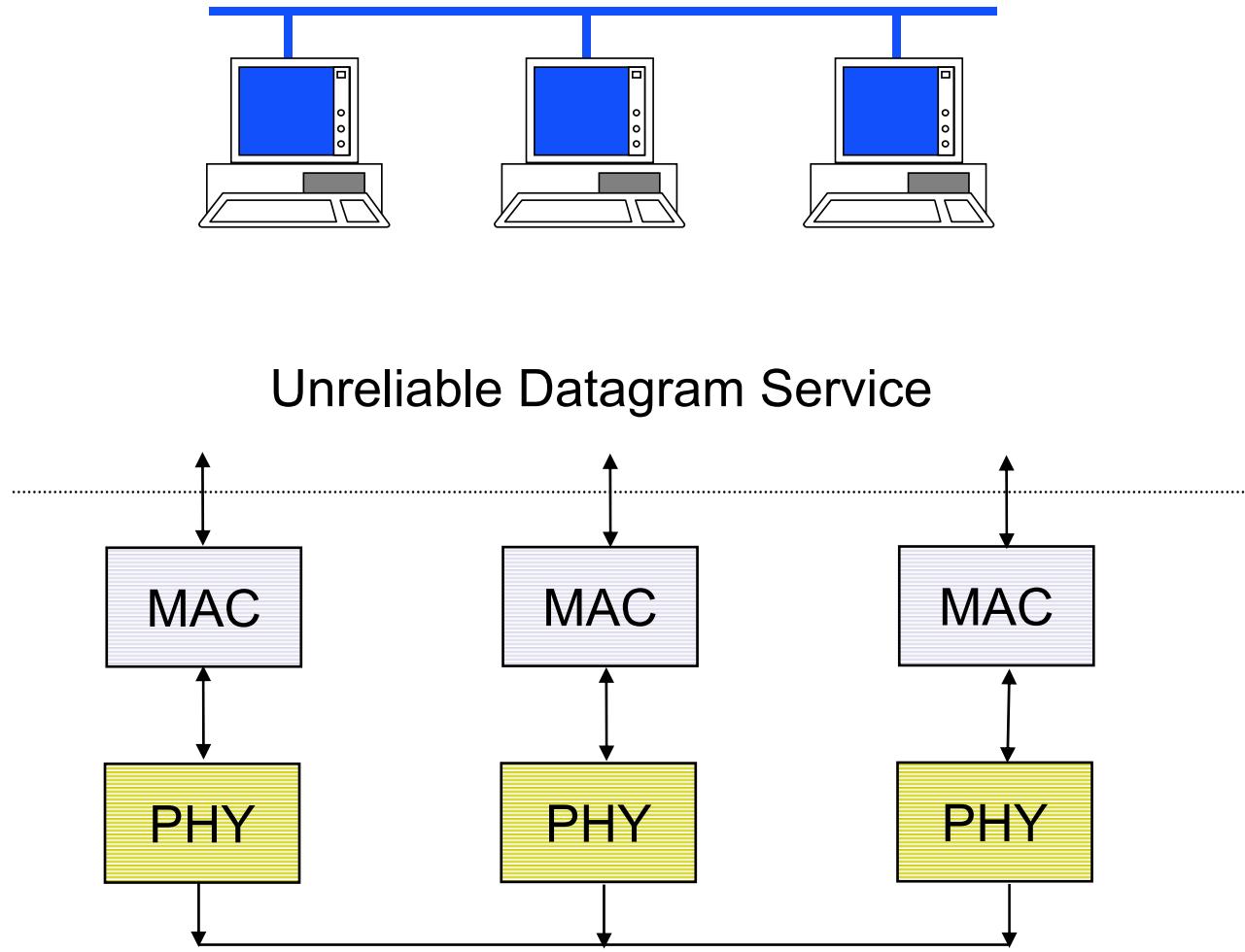
(a)

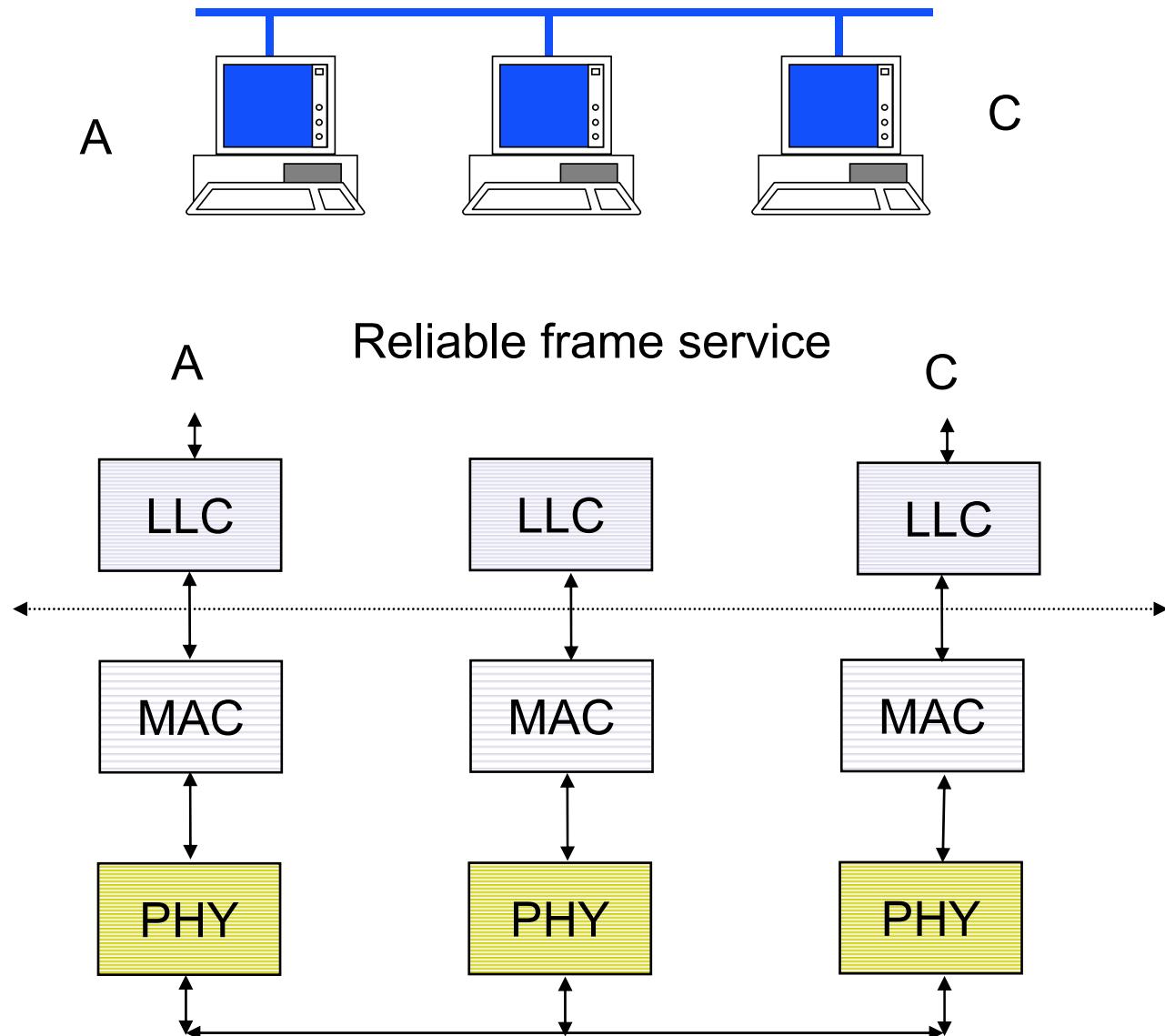


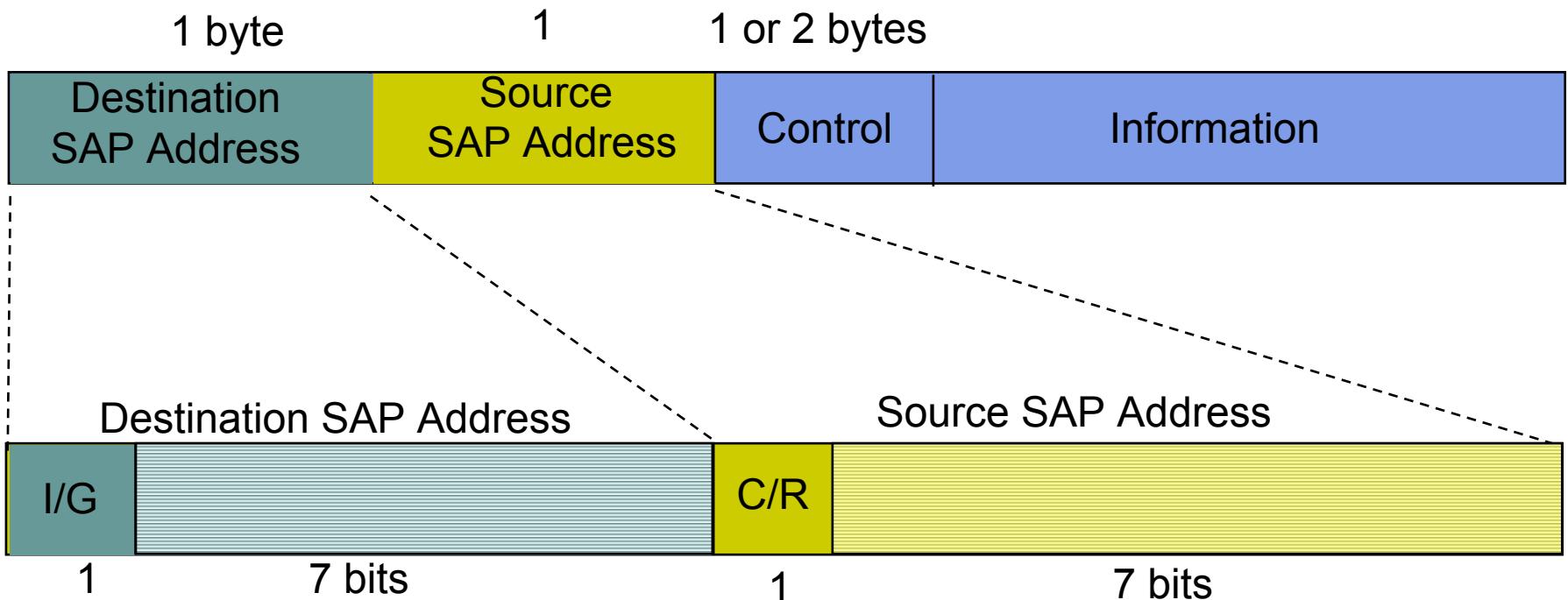
(b)





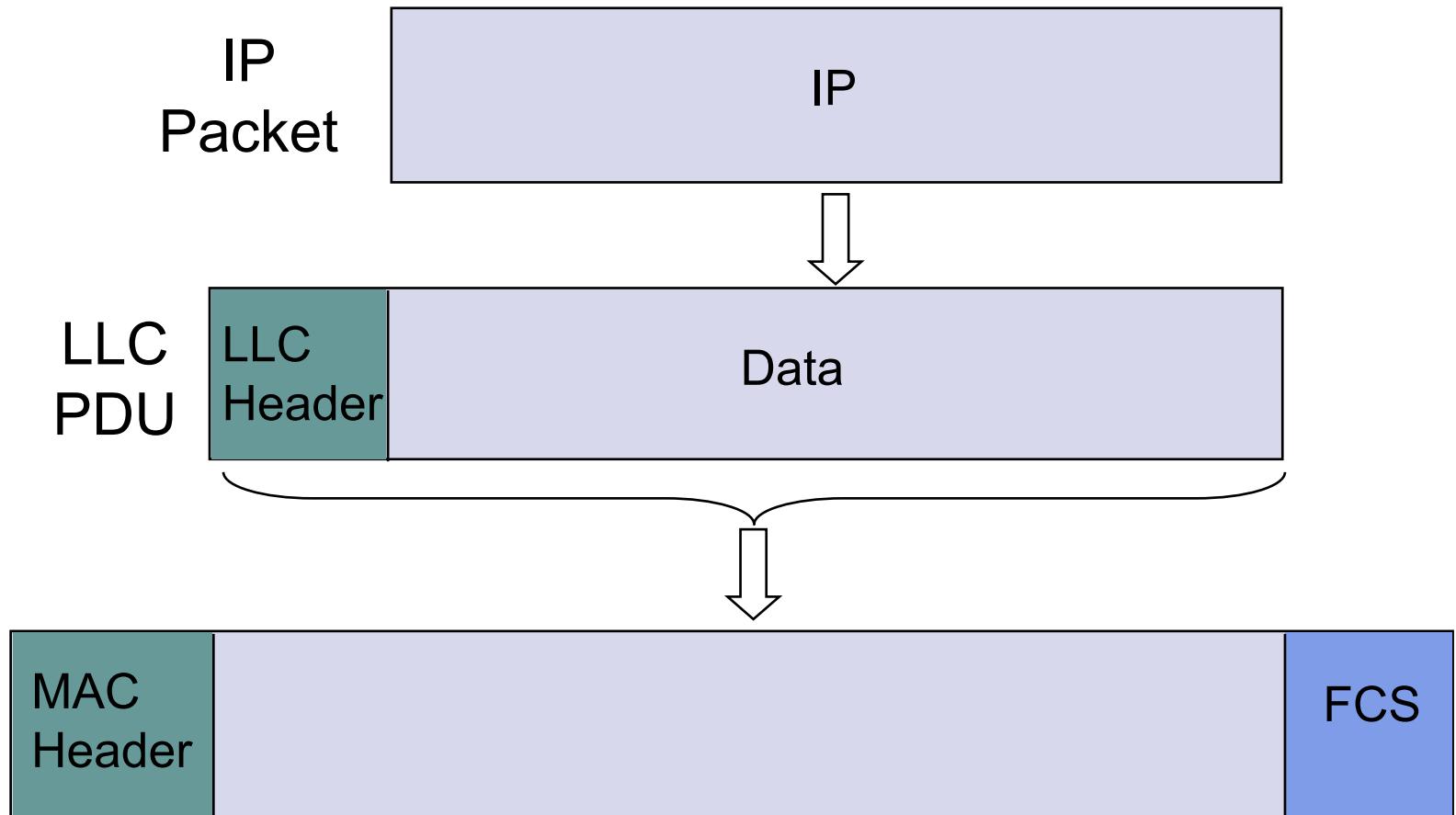


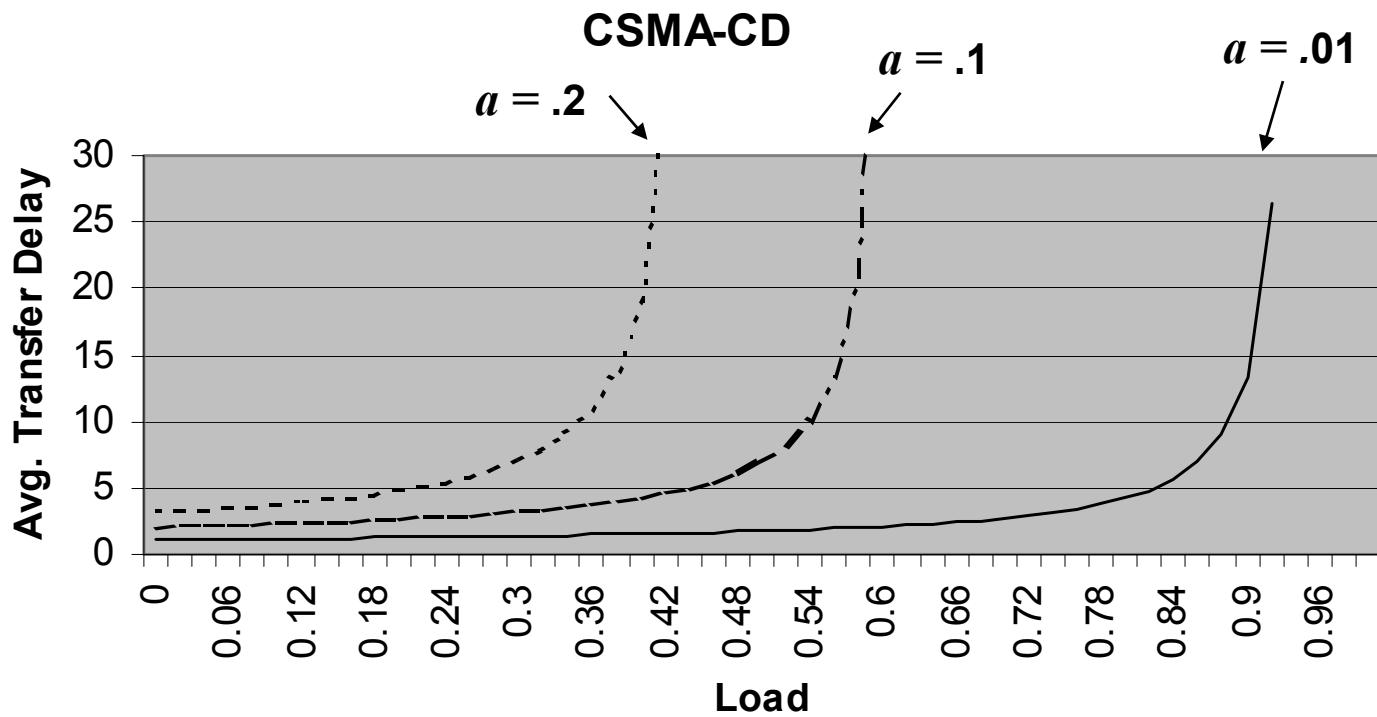


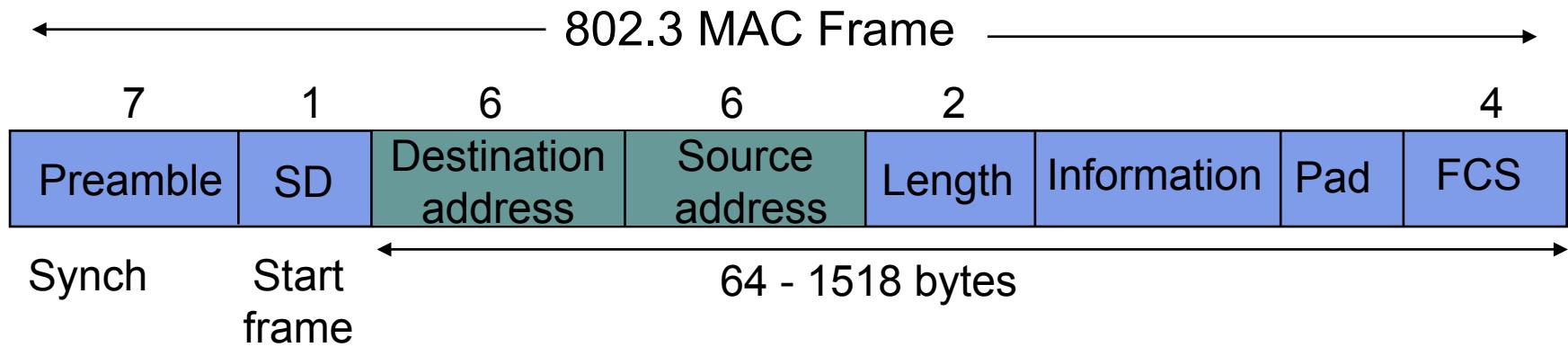


I/G = Individual or group address

C/R = Command or response frame





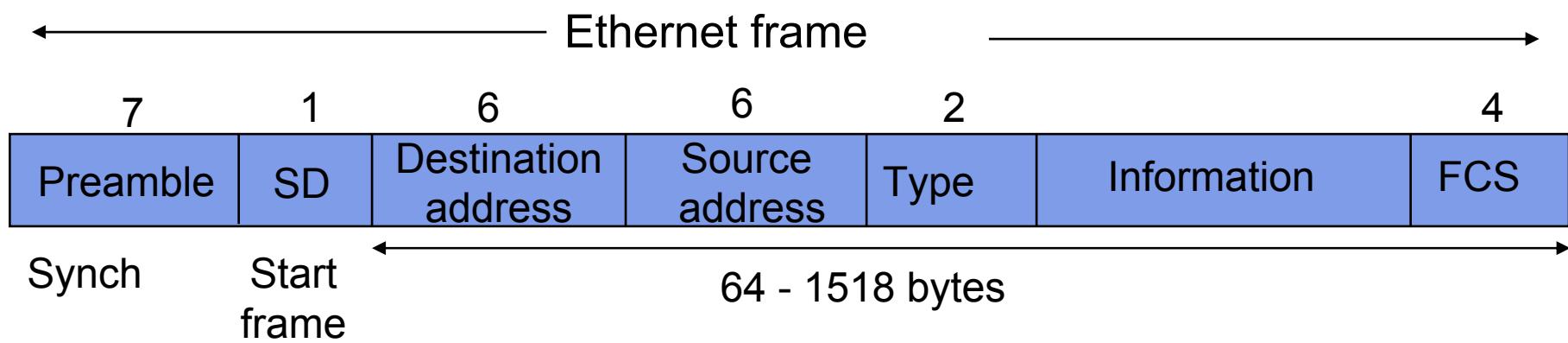


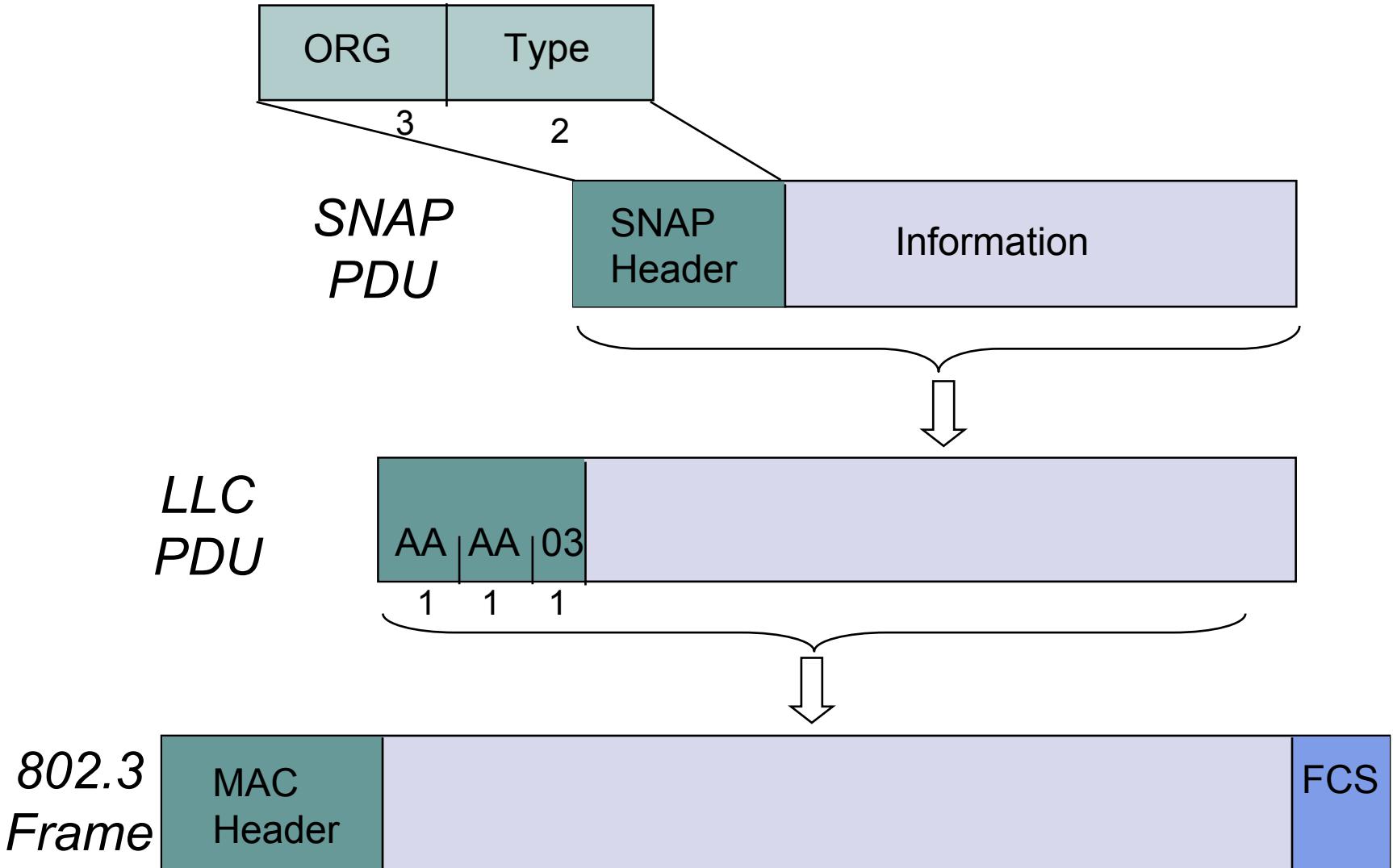
- Destination address is either single address or group address (broadcast = 111...111)



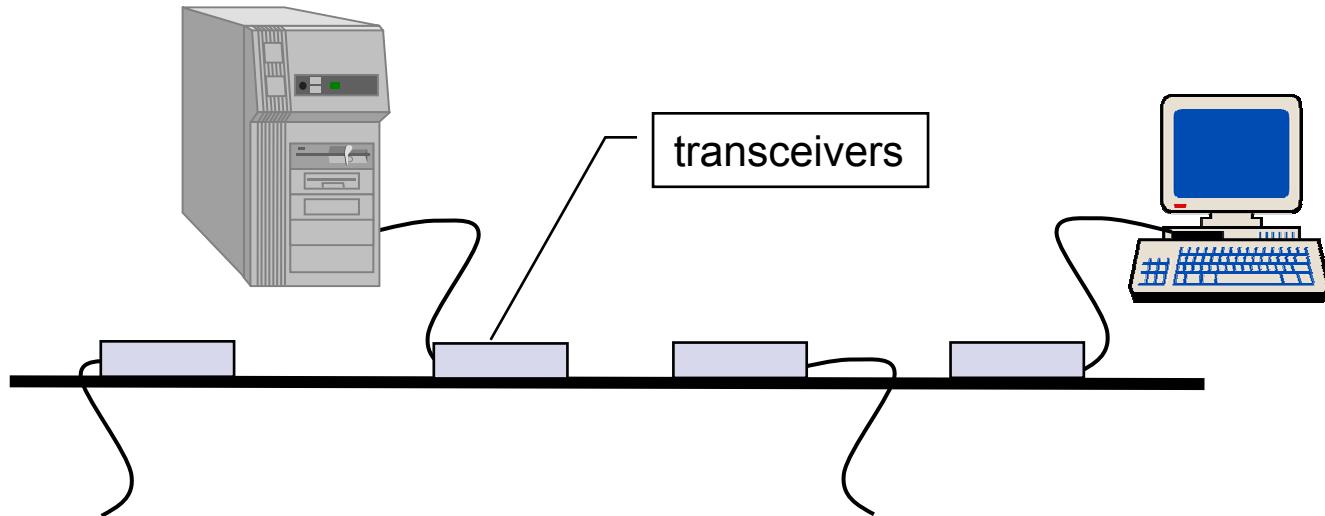
- Addresses are defined on local or universal basis
- 2^{46} possible global addresses



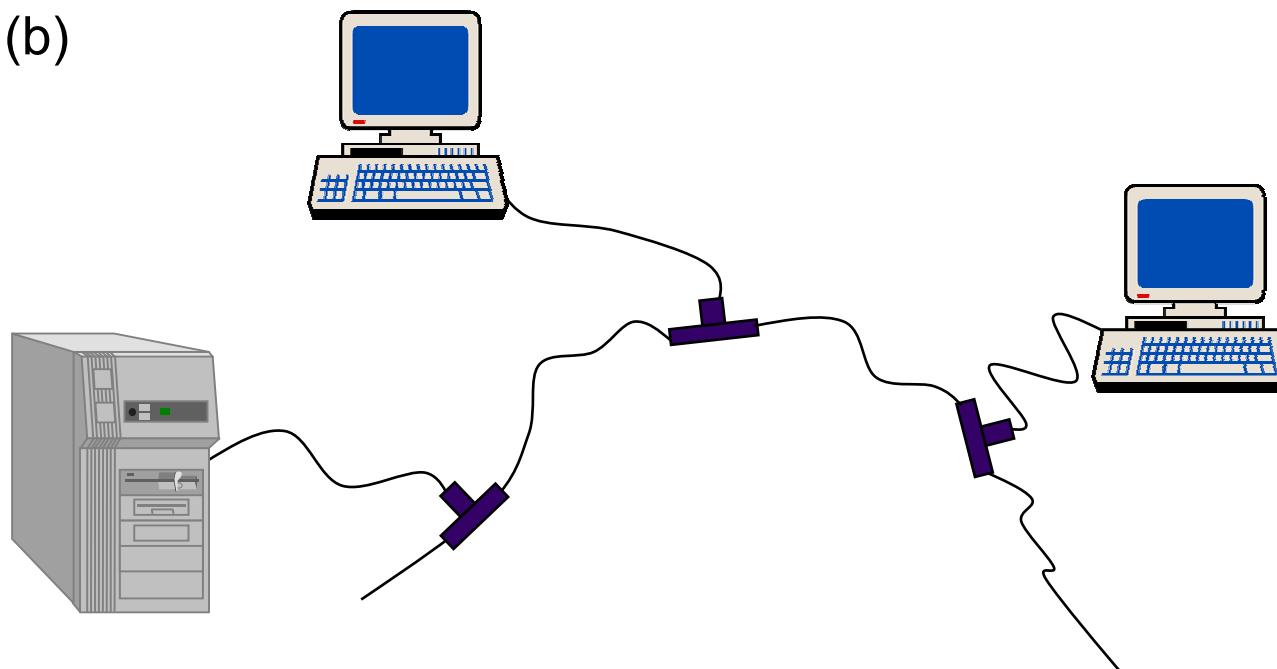


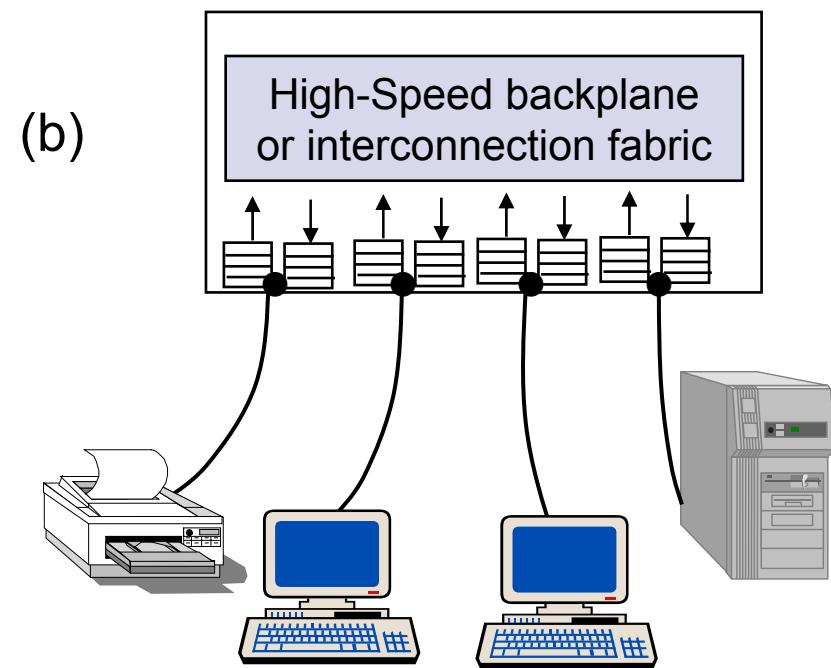
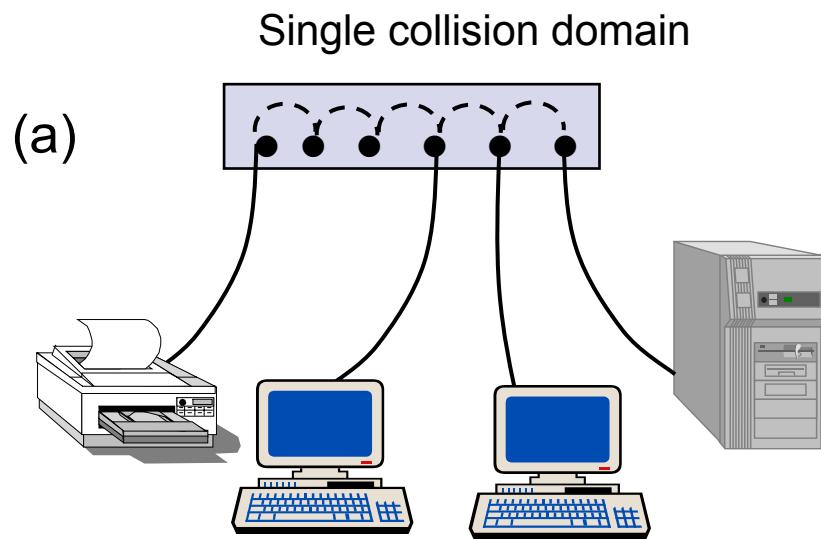


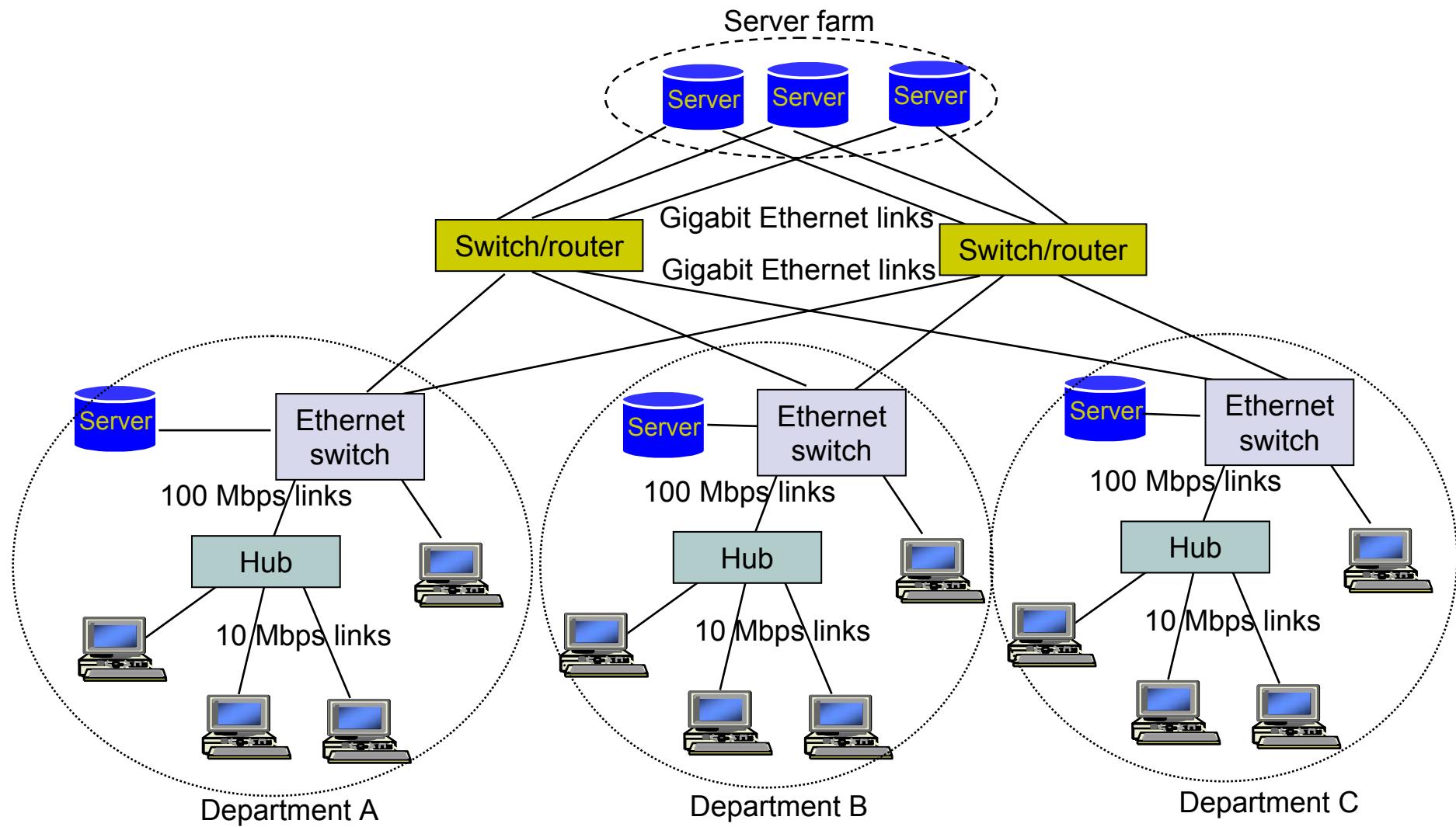
(a)

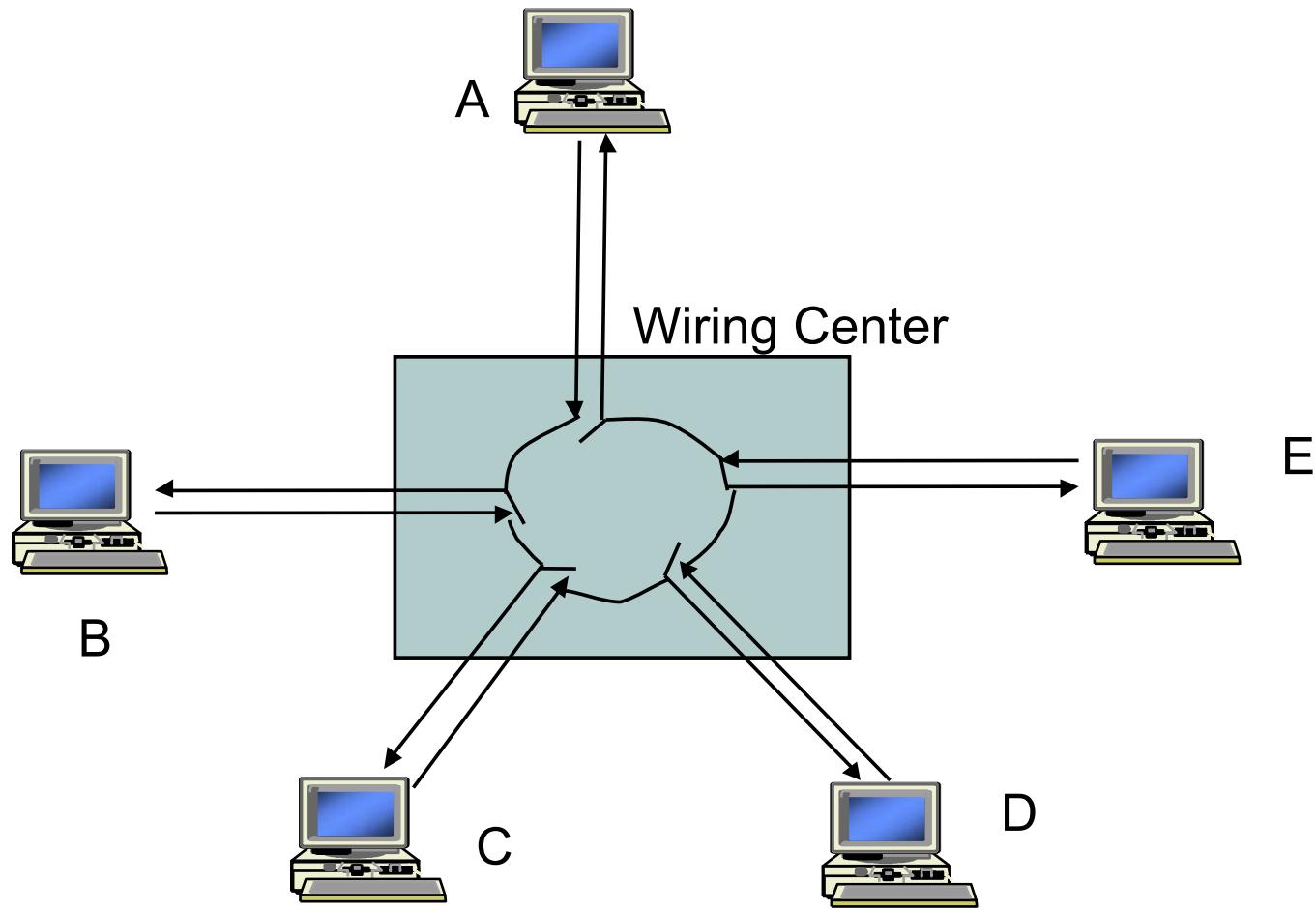


(b)

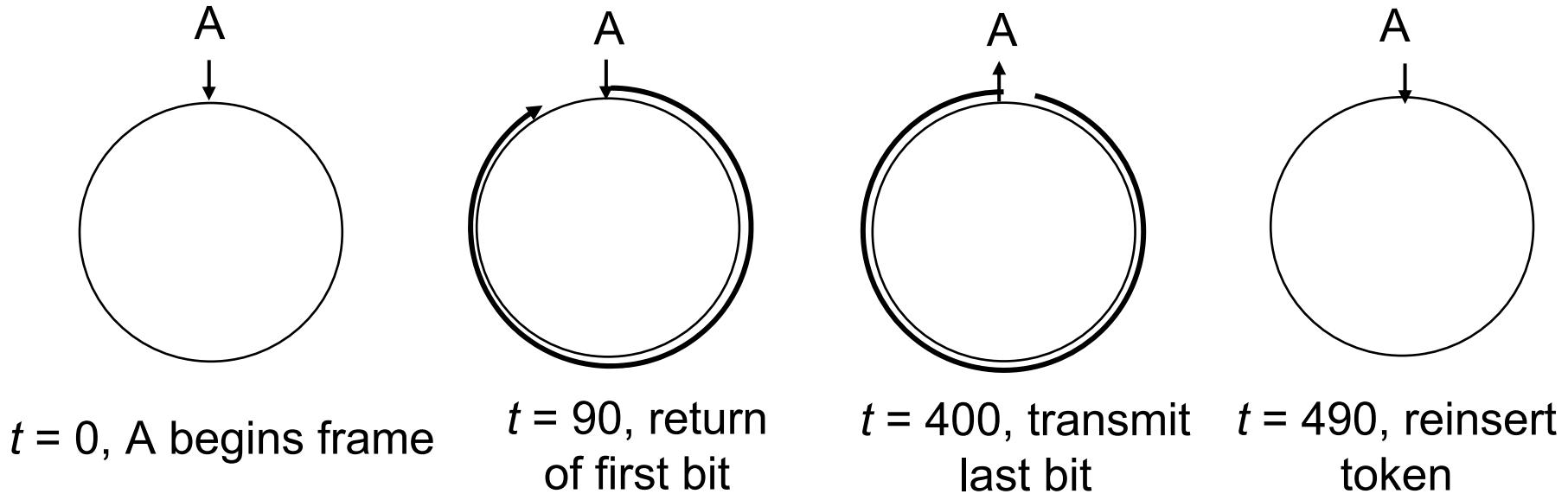




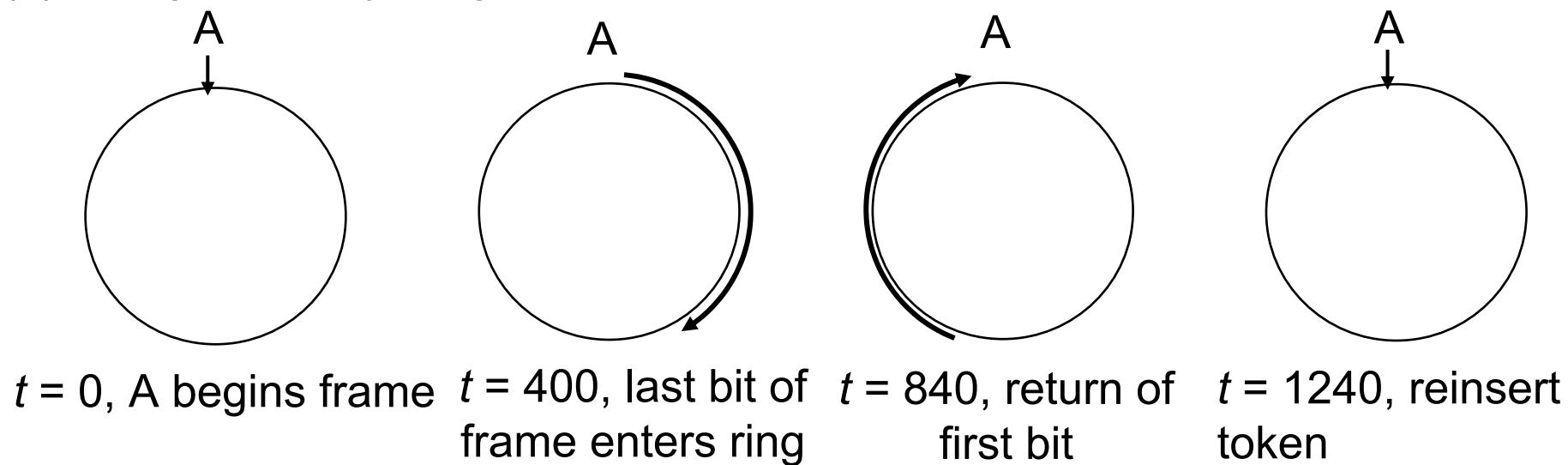




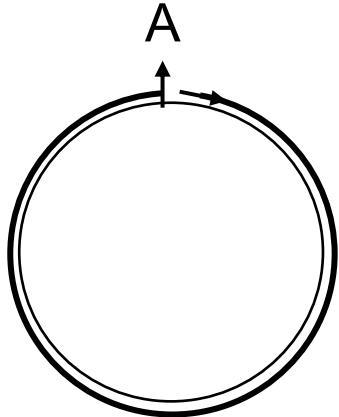
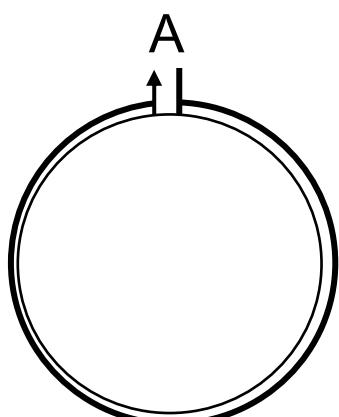
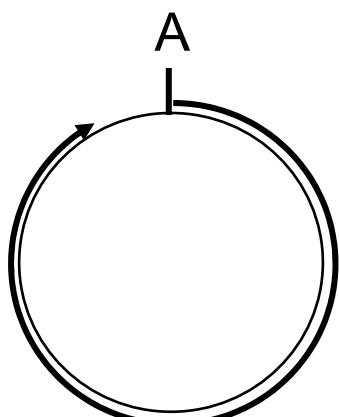
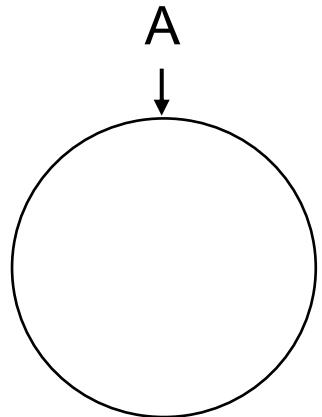
(a) Low Latency Ring



(b) High Latency Ring



(a) Low Latency Ring



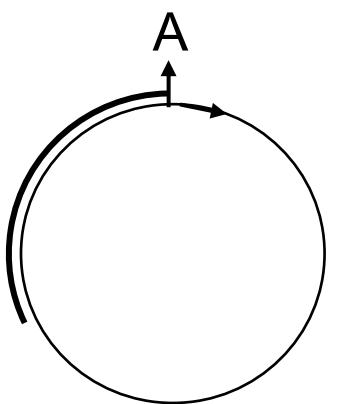
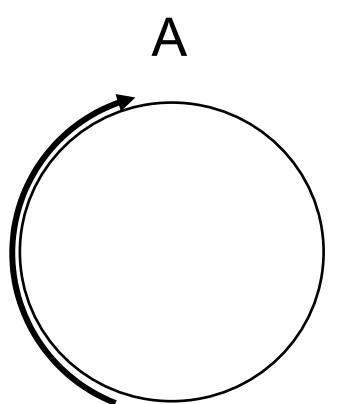
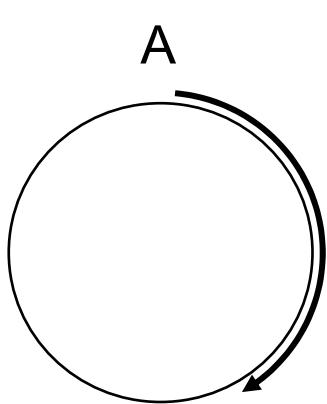
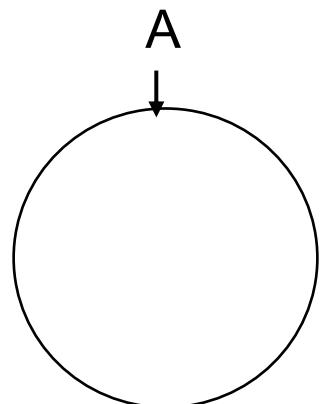
$t = 0$, A begins frame

$t = 90$, return
of first bit

$t = 210$, return of
header

$t = 400$, last bit enters
ring, reinsert token

(b) High Latency Ring



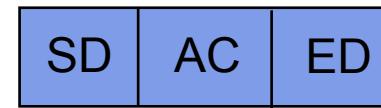
$t = 0$, A begins frame

$t = 400$, transmit
last bit

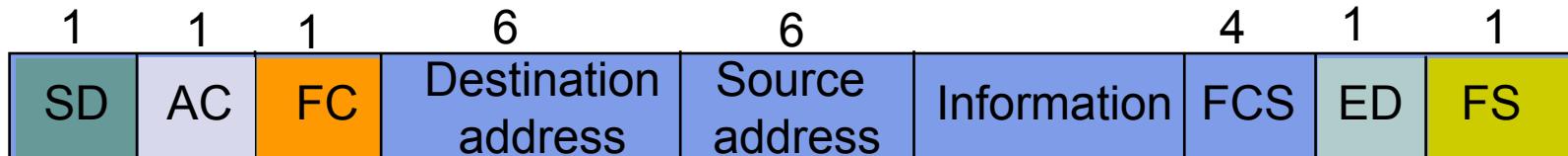
$t = 840$, arrival
first frame bit

$t = 960$, reinsert
token

Token frame format



Data frame format



Starting delimiter



J, K nondata symbols (line code)

Access control



PPP=priority; T=token bit
M=monitor bit; RRR=reservation

Frame control



FF = frame type
ZZZZZZ = control bits

Ending delimiter

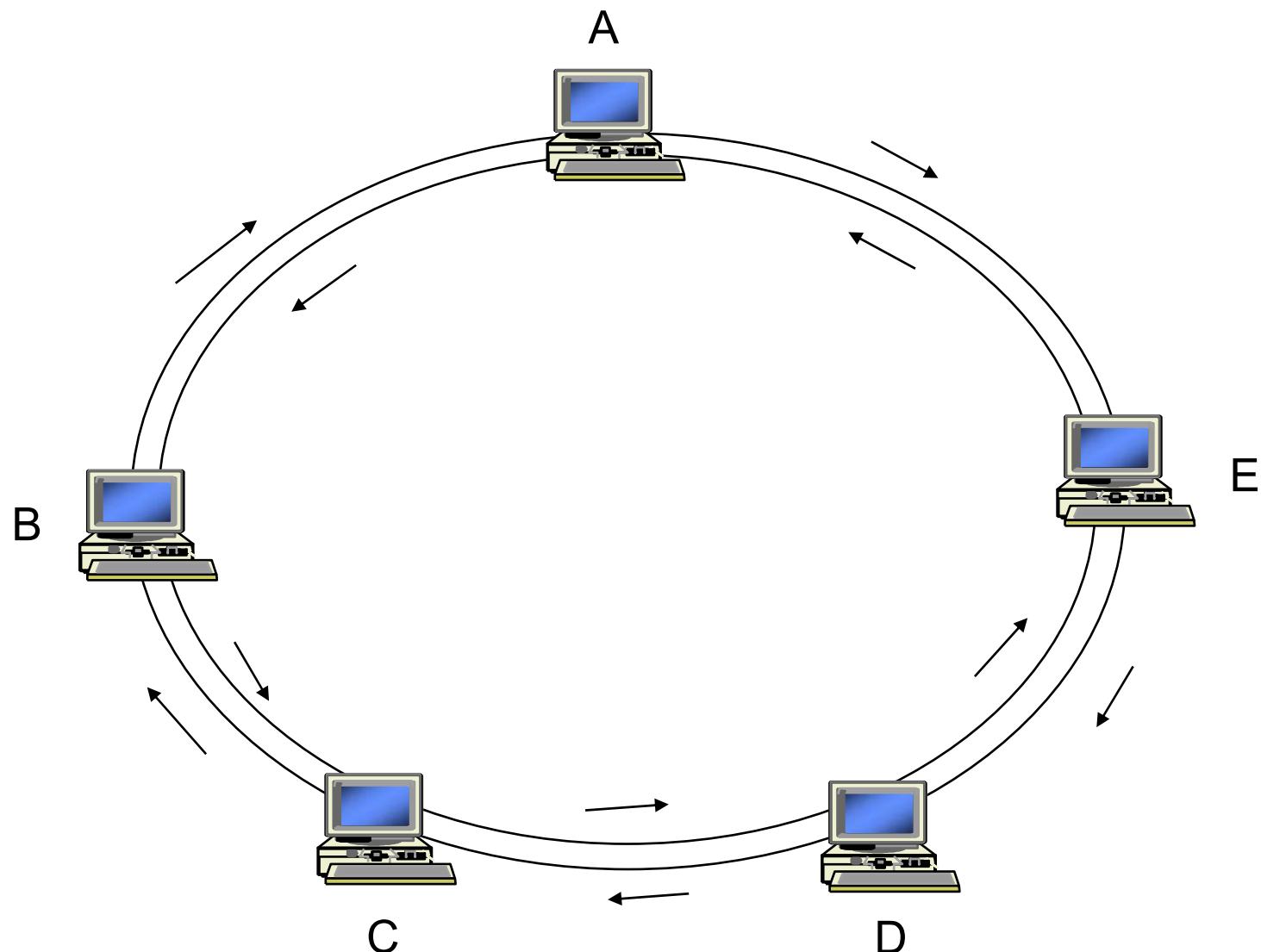


I = intermediate-frame bit
E = error-detection bit

Frame status



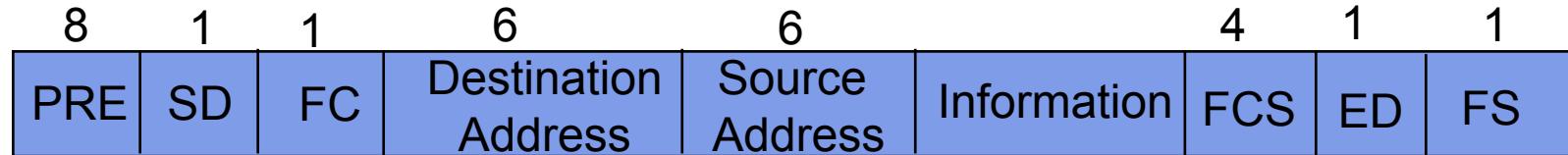
A = address-recognized bit
xx = undefined
C = frame-copied bit



Token Frame Format



Data Frame Format

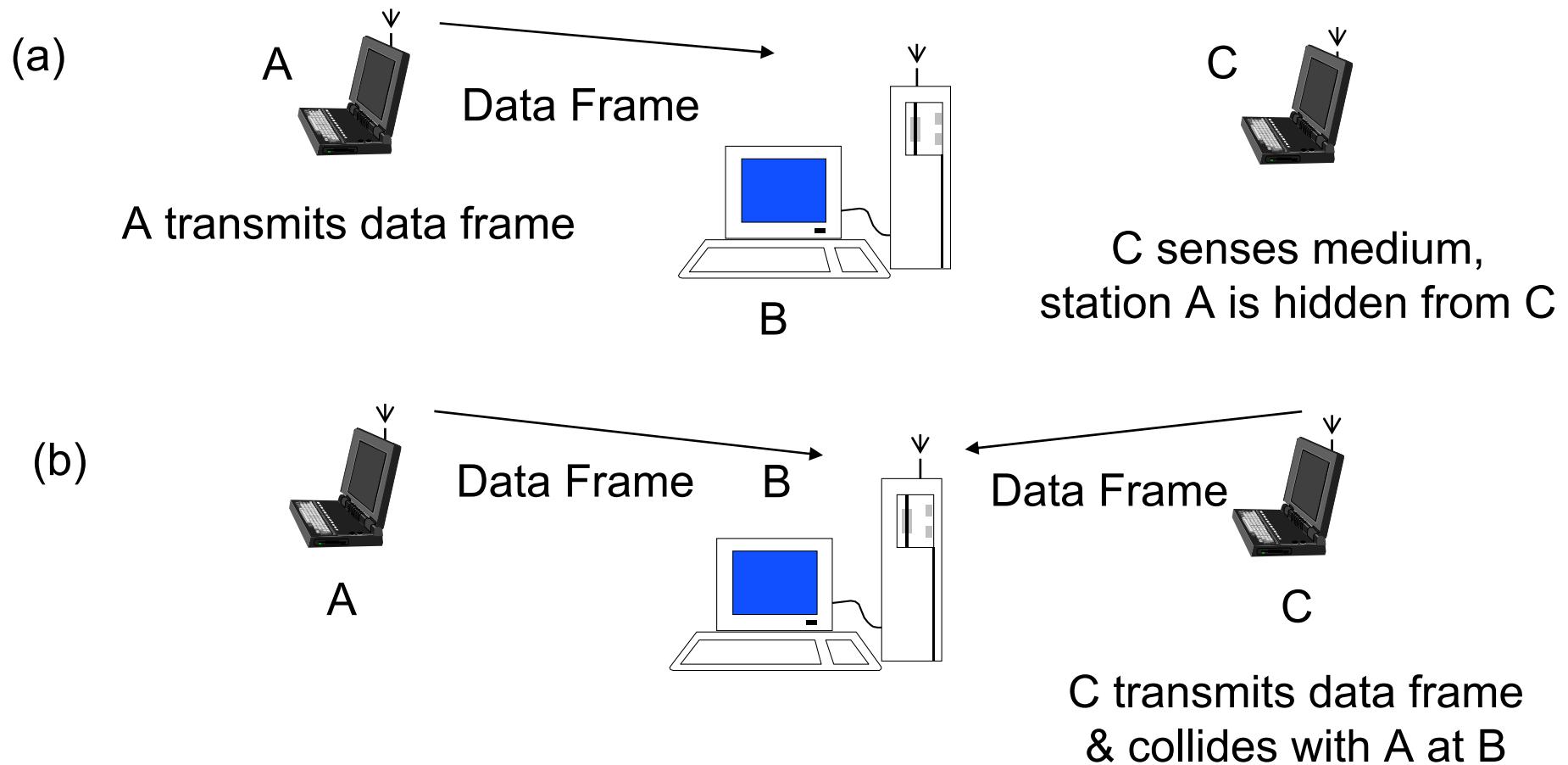


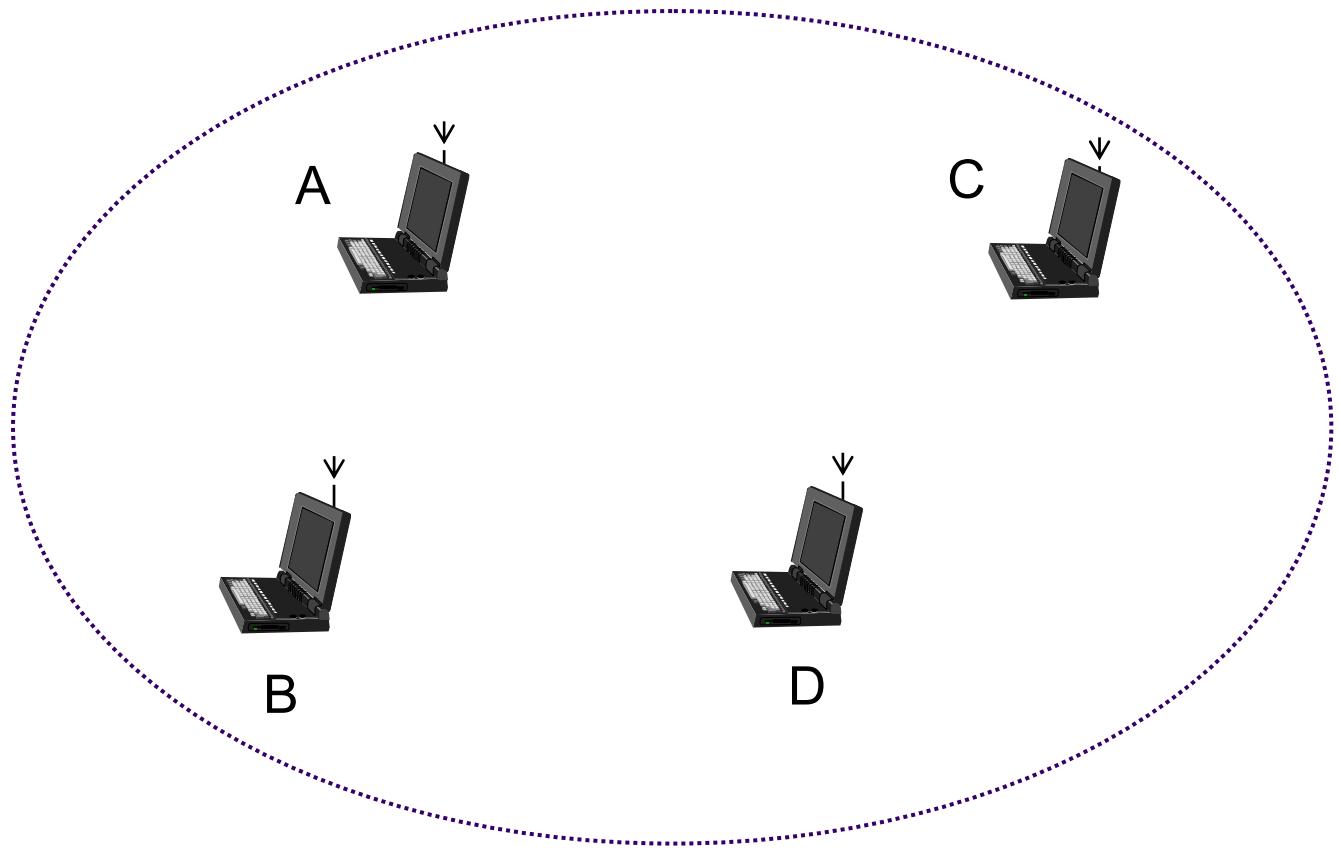
Preamble

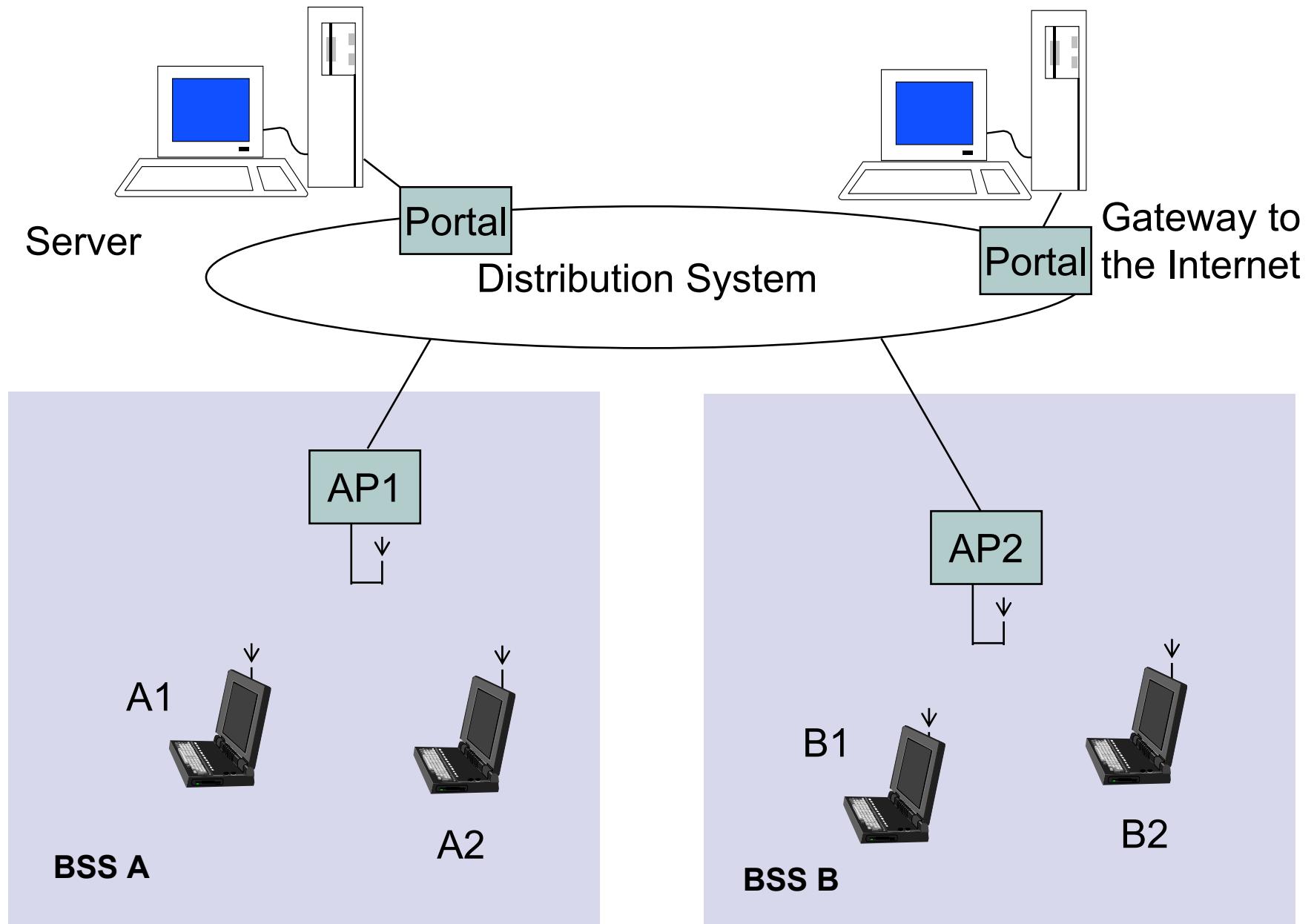
Frame
control

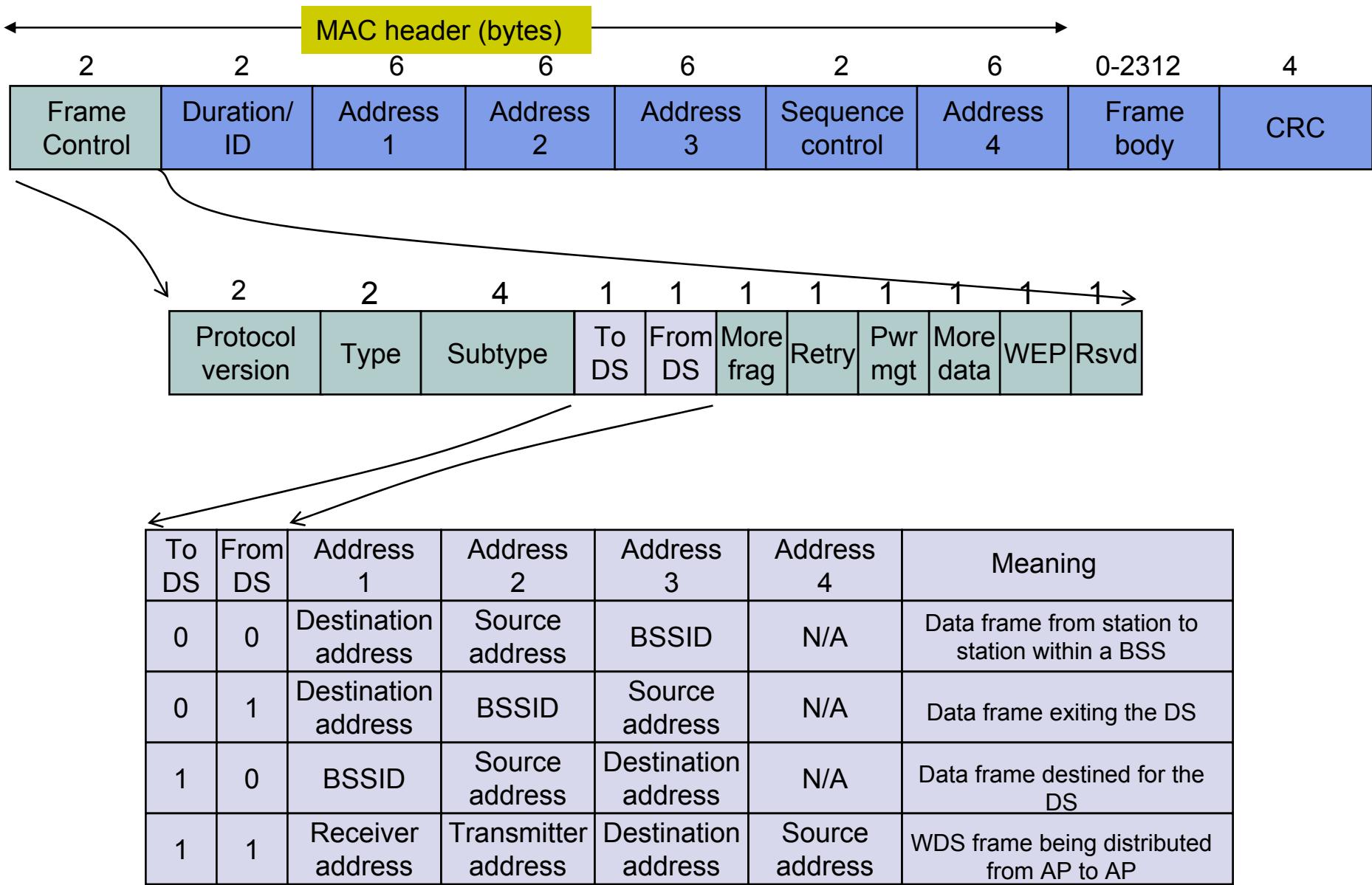
CLFFZZZZ

C = synch/asynch
L = address length (16 or 48 bits)
FF = LLC/MAC control/reserved frame type



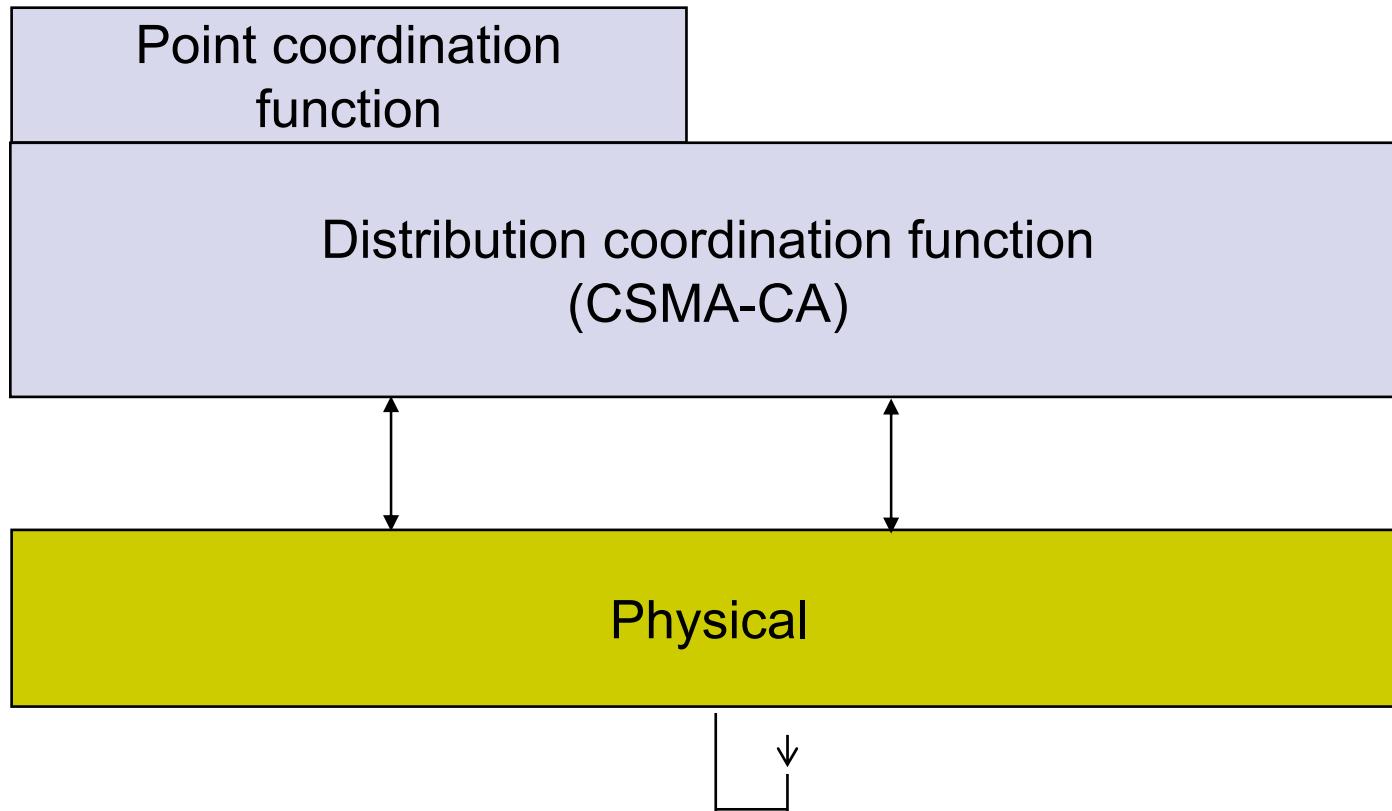


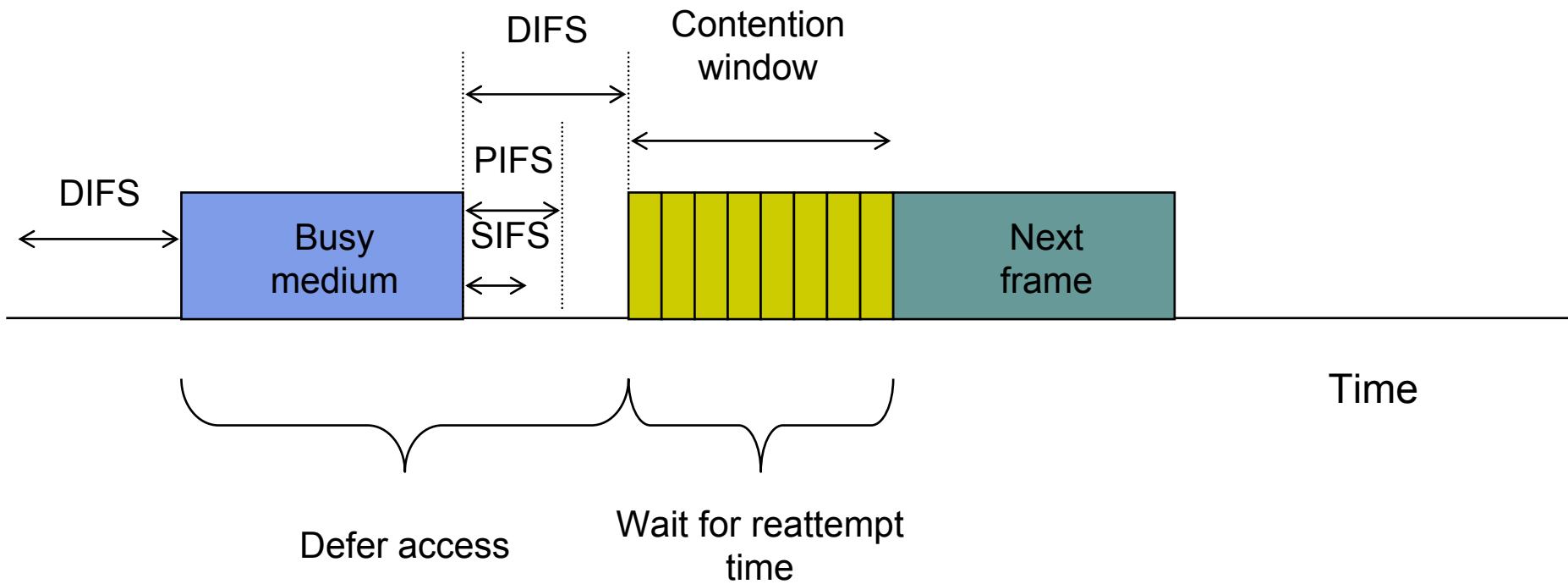


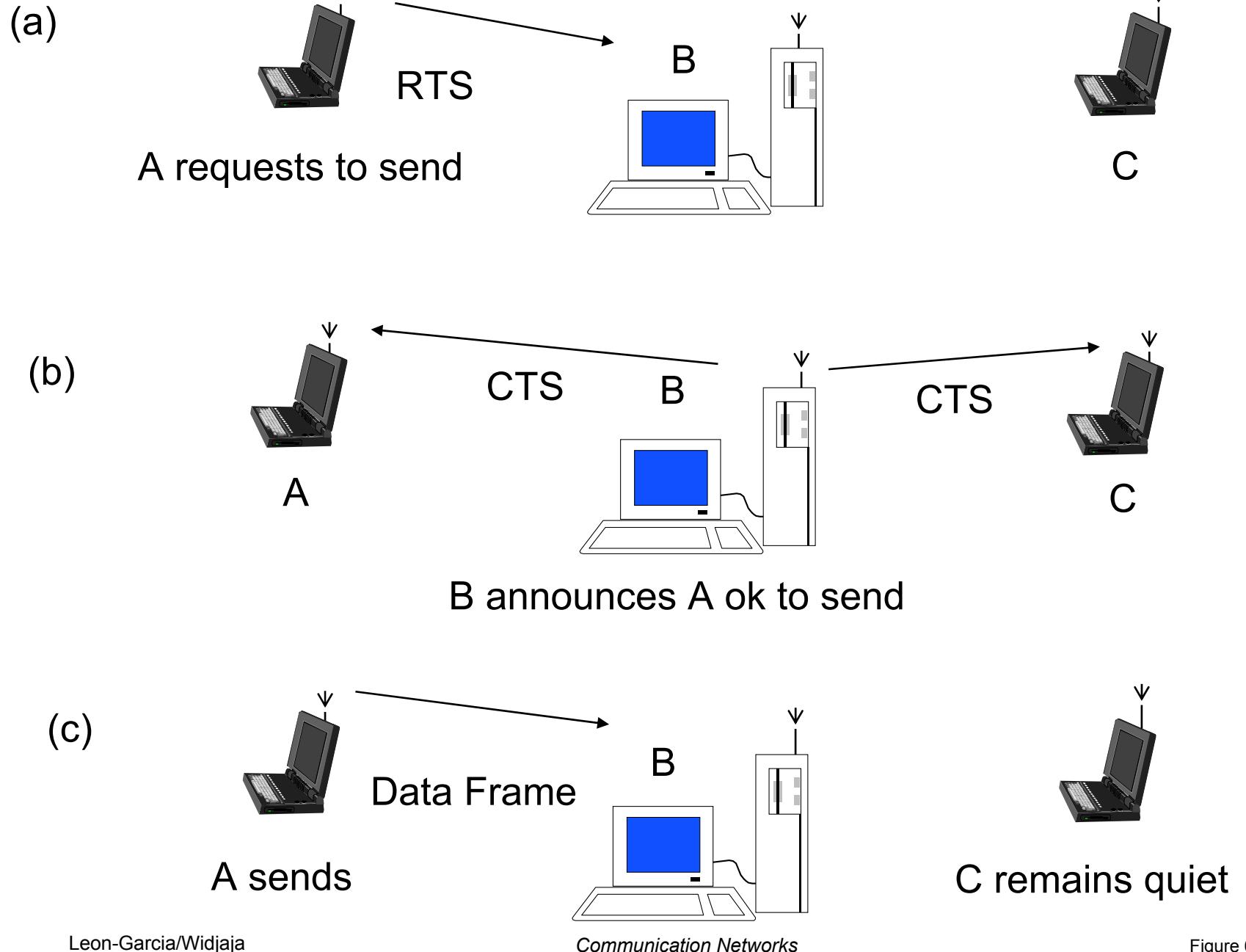


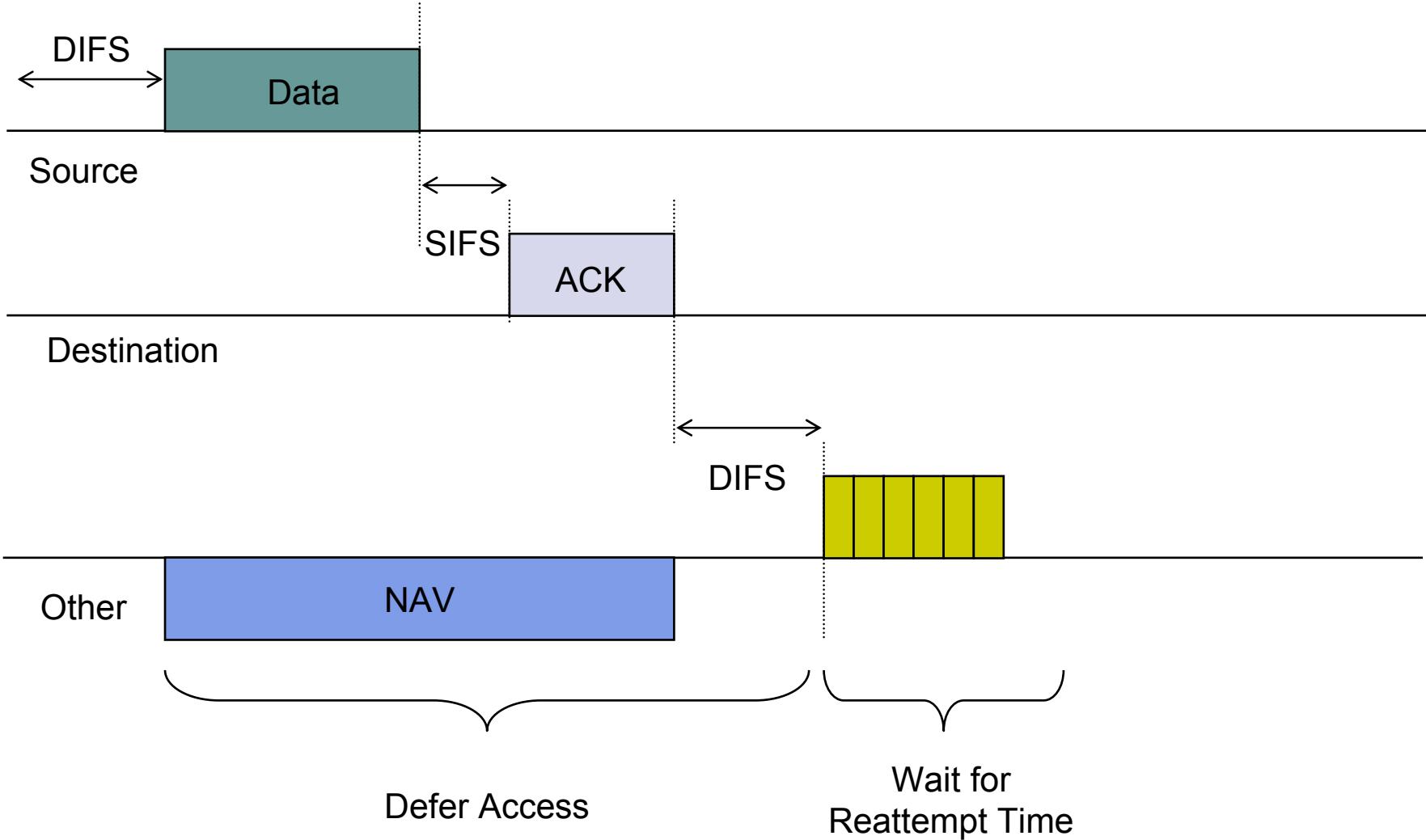
Contention-free service

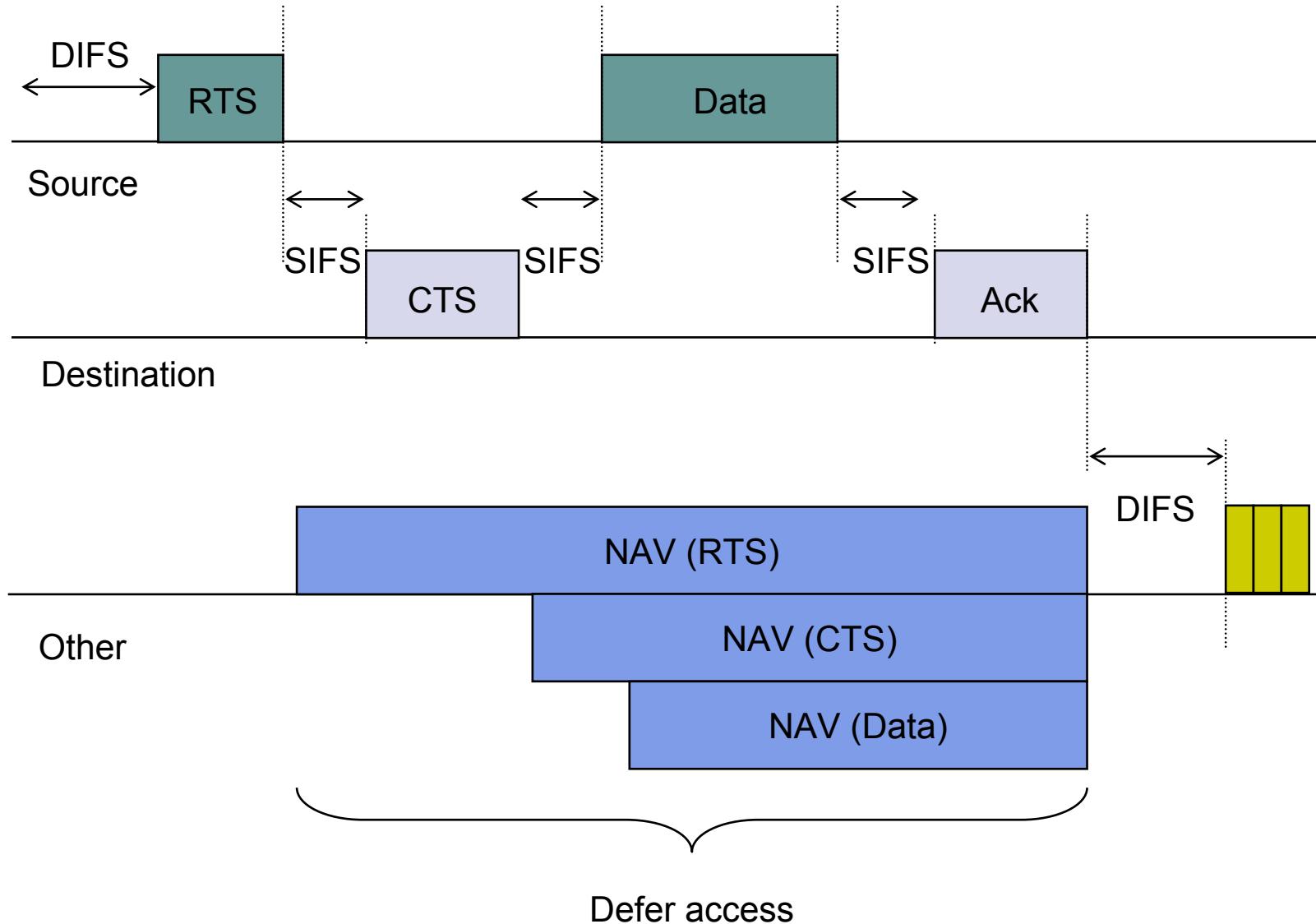
Contention service

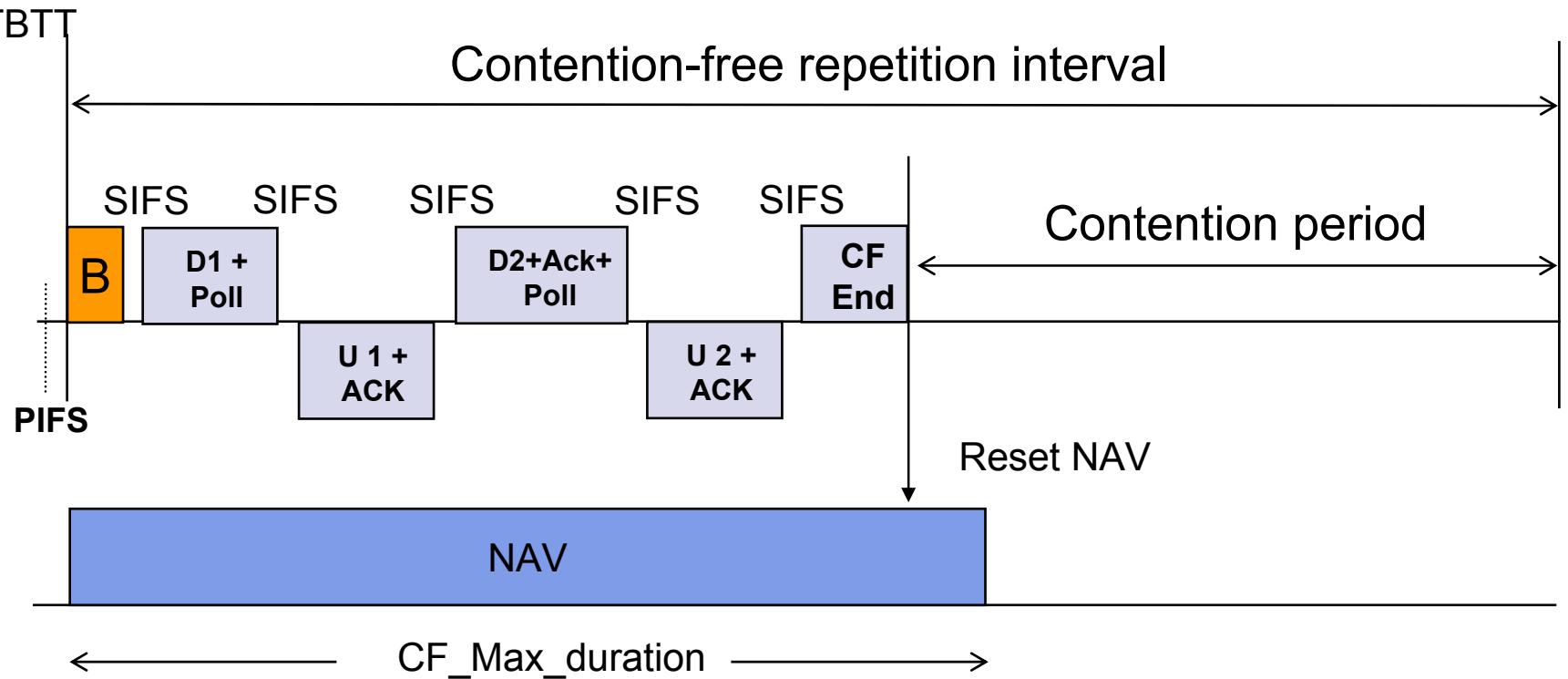










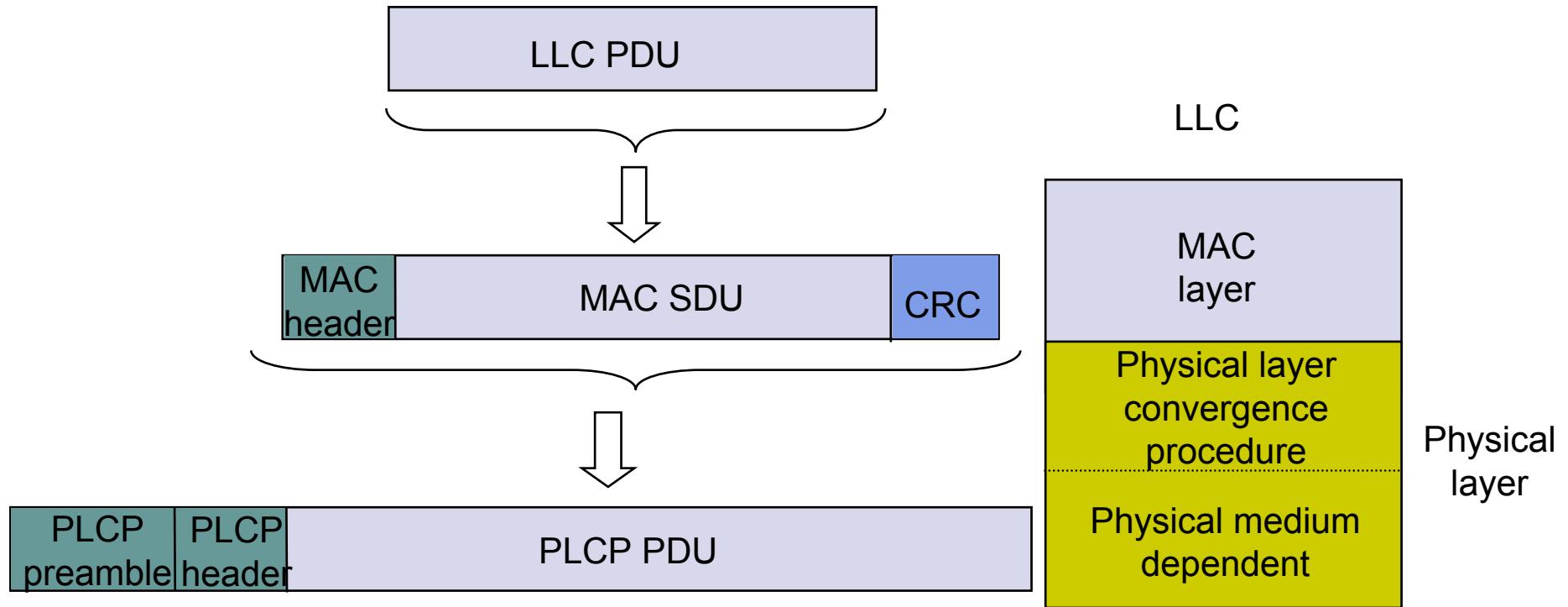


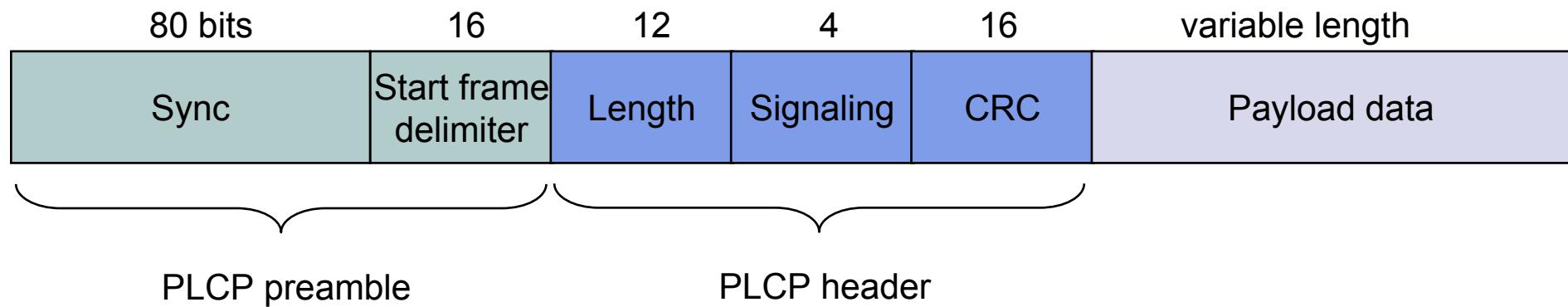
D1, D2 = frame sent by point coordinator

U1, U2 = frame sent by polled station

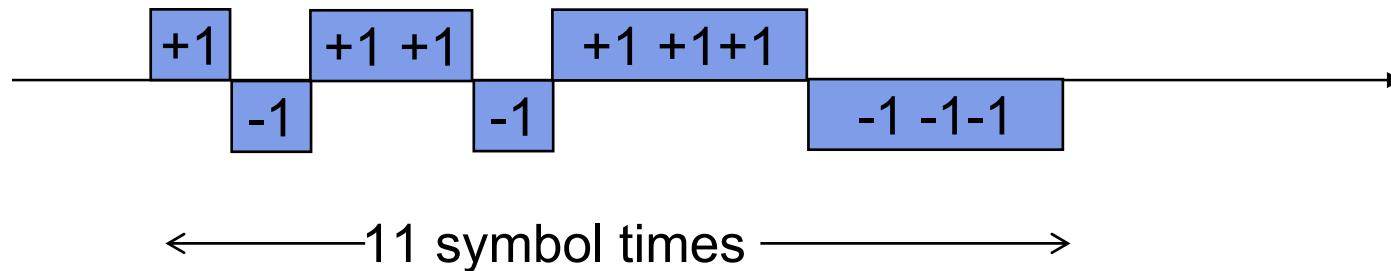
TBTT = target beacon transmission time

B = beacon frame

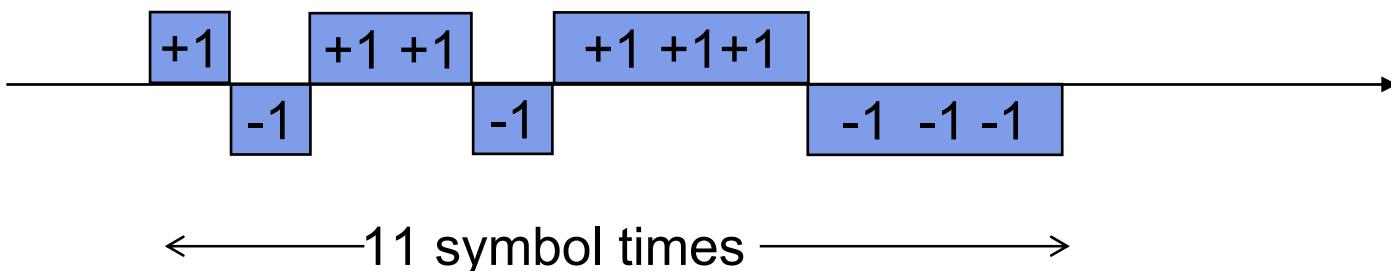




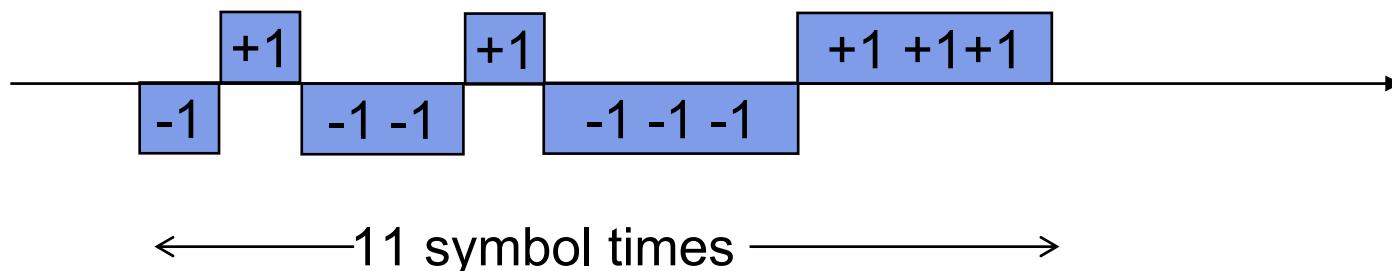
11-chip Barker sequence:

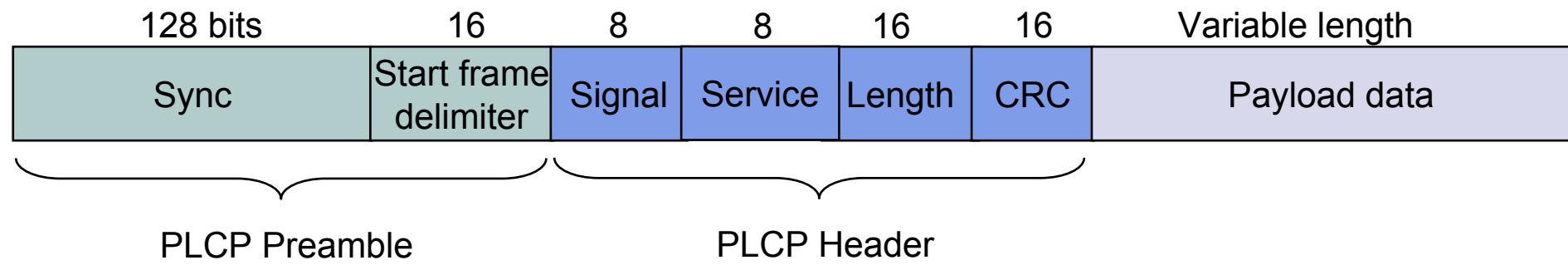


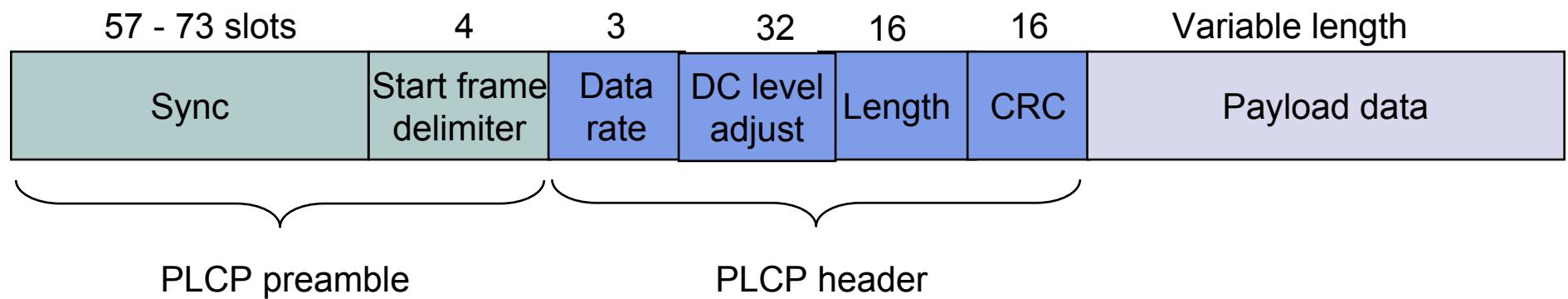
To transmit +1, send:

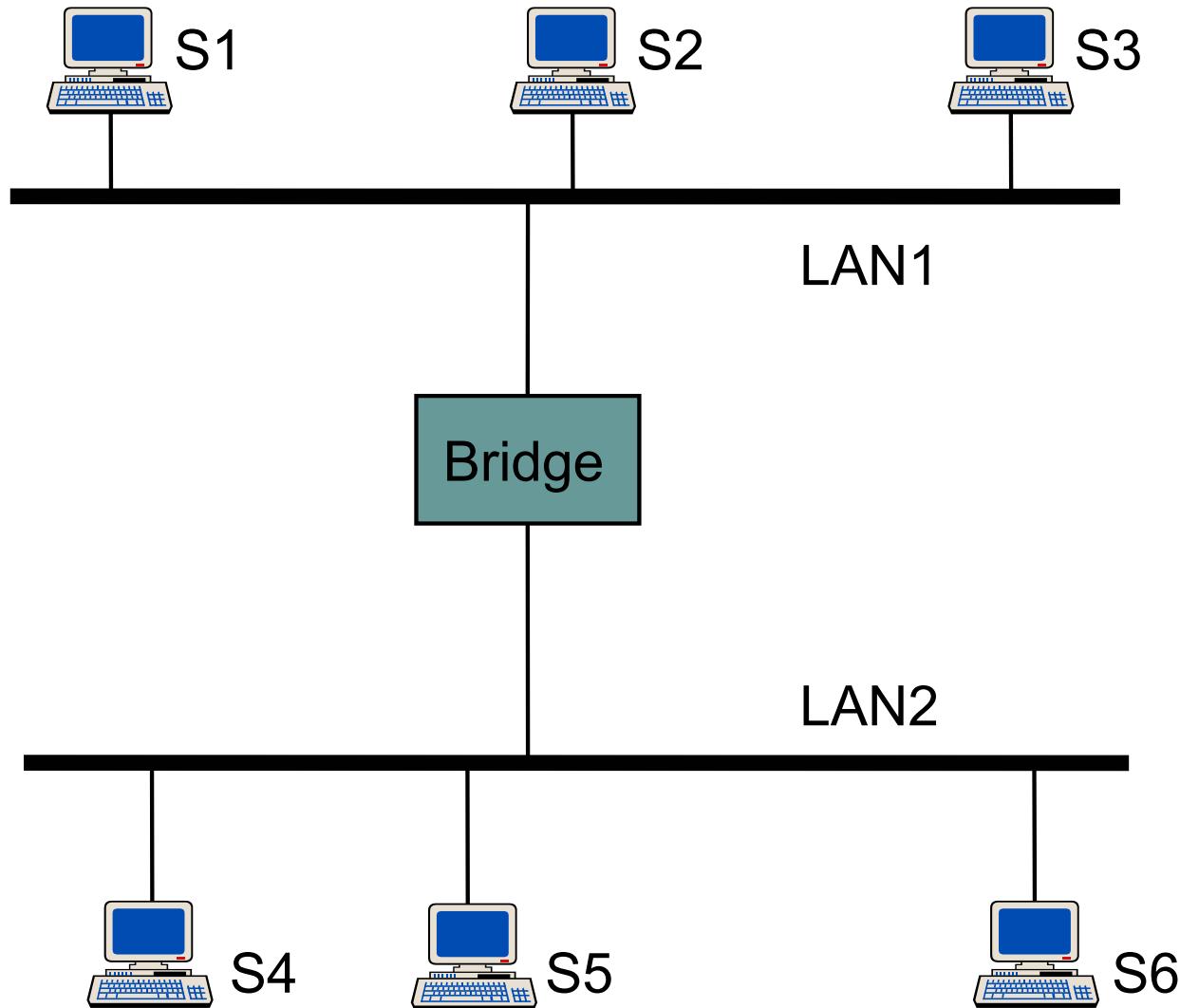


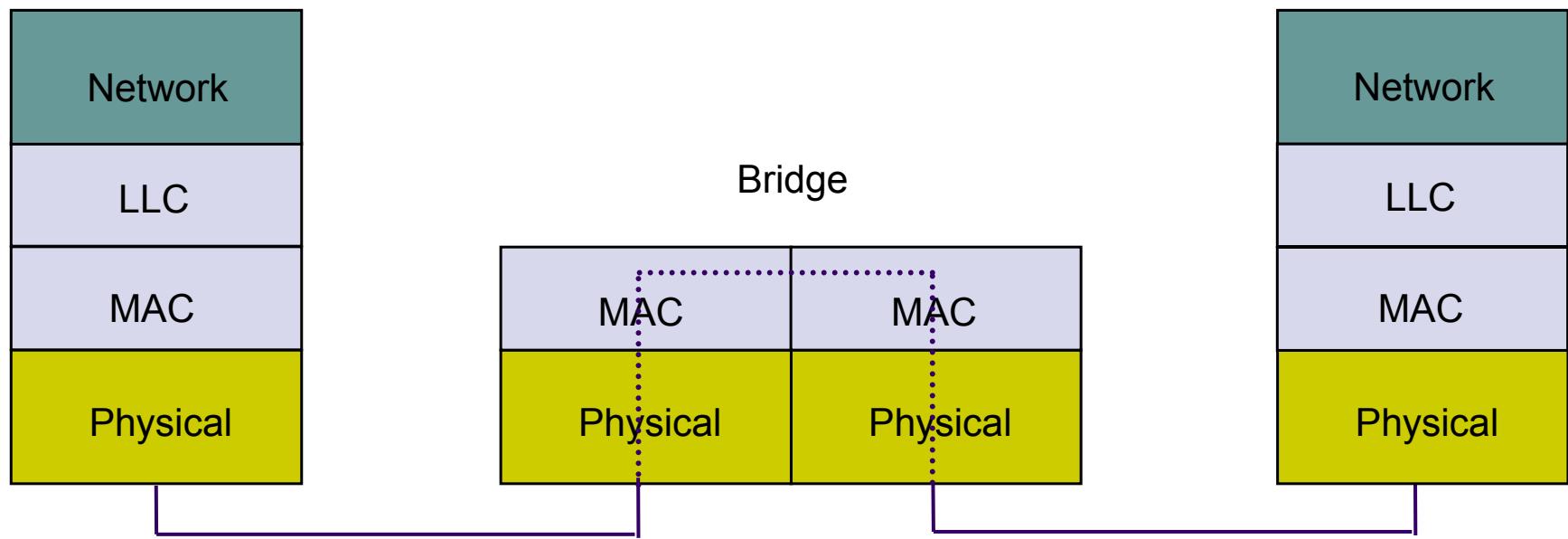
To transmit -1, send:

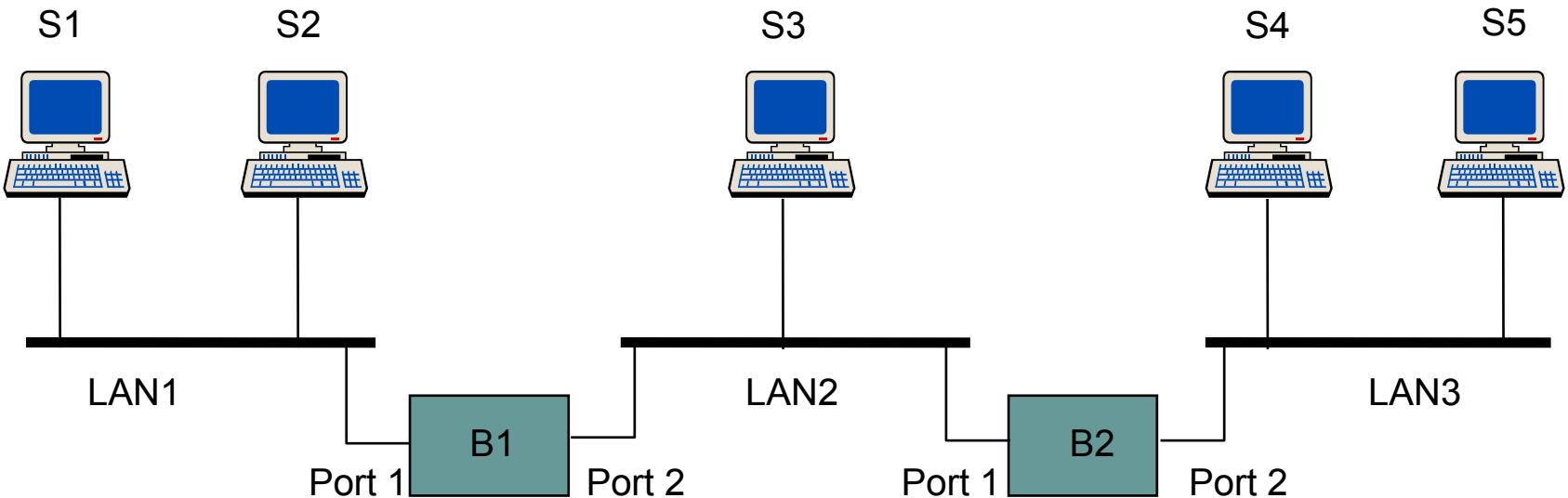






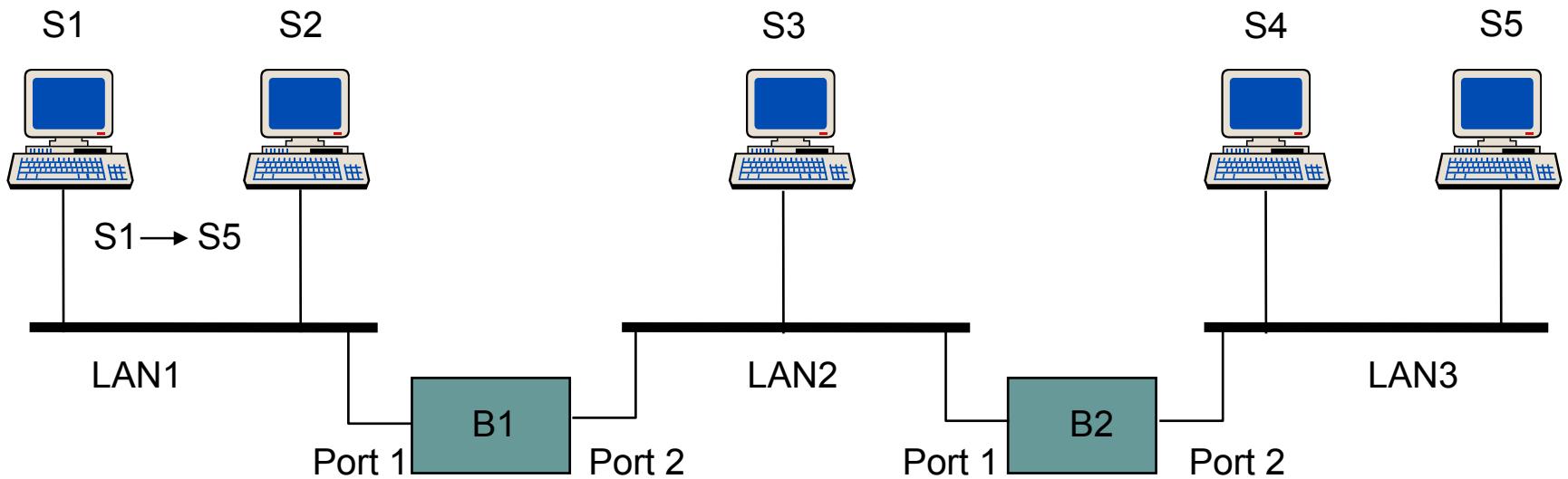






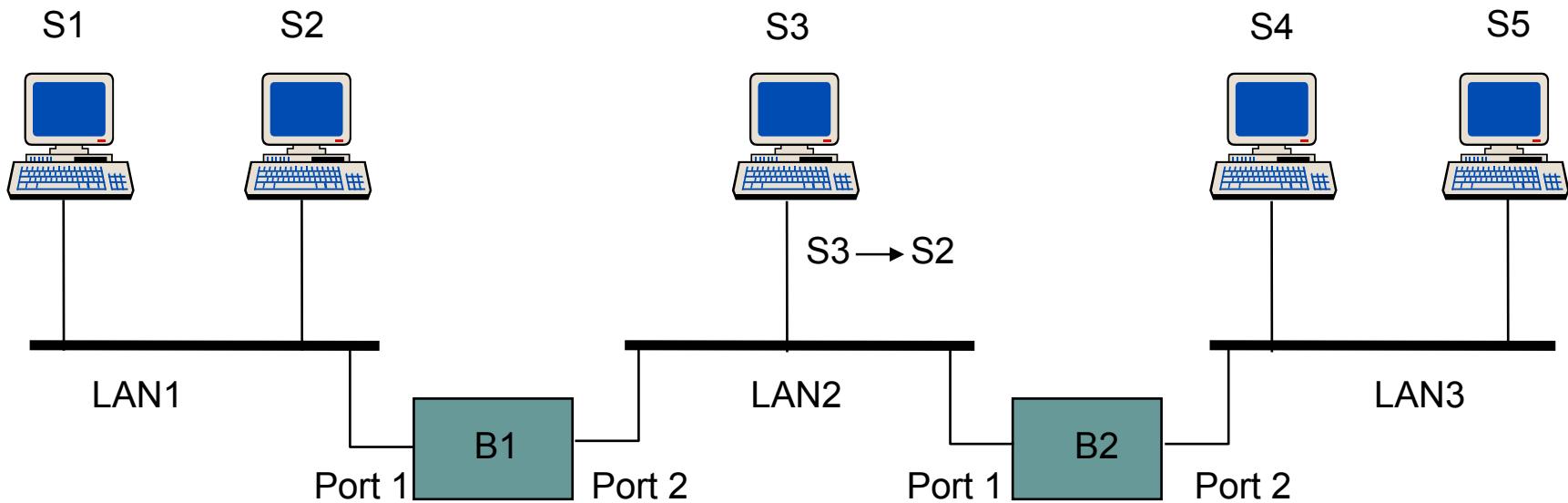
Address	Port

Address	Port



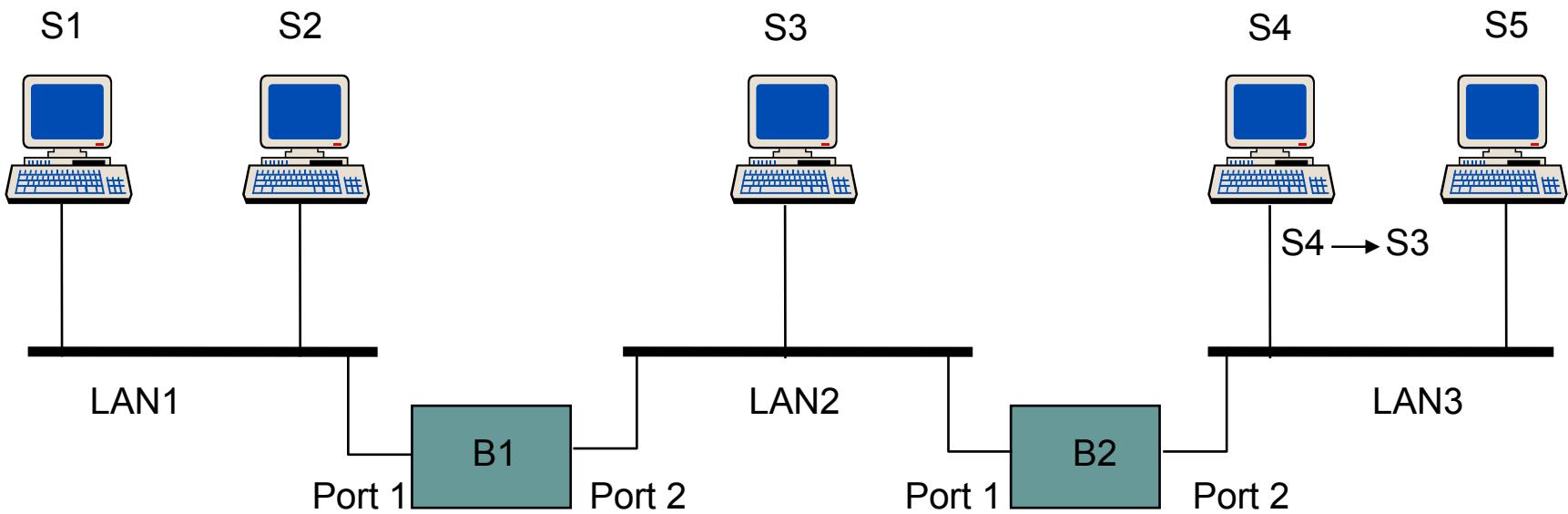
Address	Port
S1	1

Address	Port
S1	1



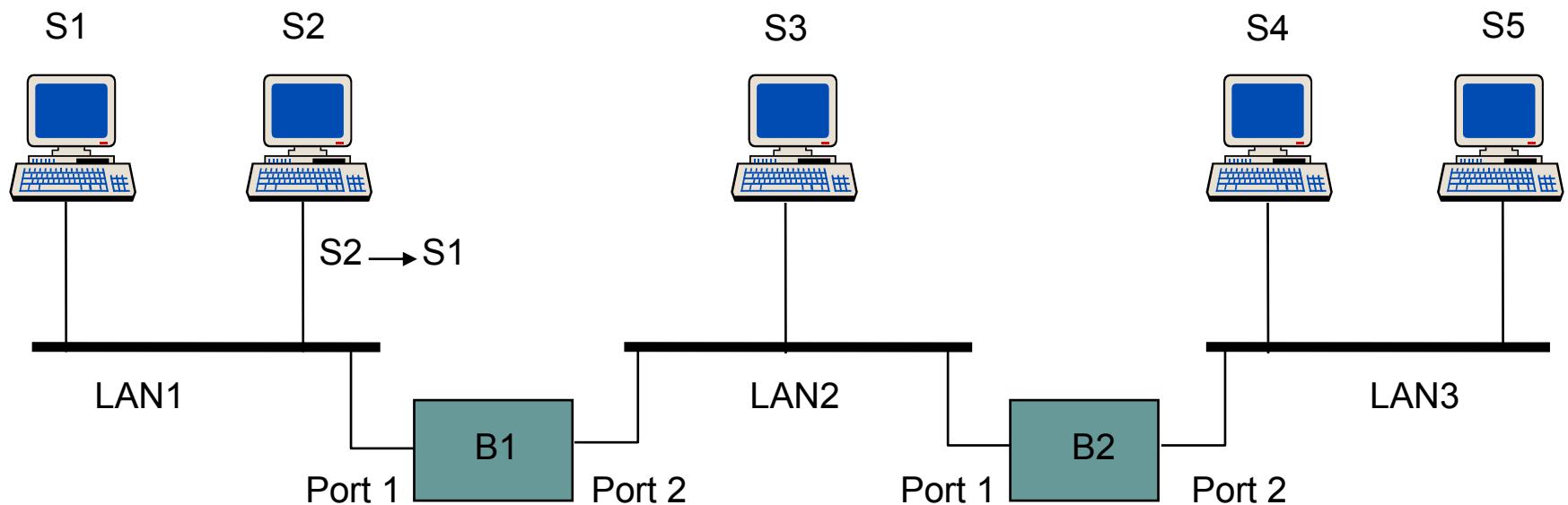
Address	Port
S1	1
S3	1

Address	Port
S1	1
S3	1



Address	Port
S1	1
S3	2
S4	2

Address	Port
S1	1
S3	1
S4	2



Address	Port
S1	1
S3	2
S4	2
S2	1

Address	Port
S1	1
S3	1
S4	2

